



September 2000

QFET™

# FQA10N80

## 800V N-Channel MOSFET

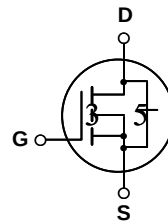
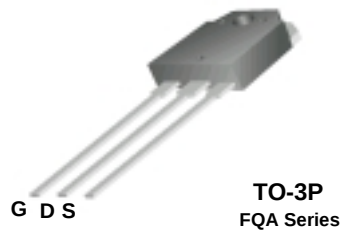
### General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply.

### Features

- 9.8A, 800V,  $R_{DS(on)} = 1.05\Omega$  @  $V_{GS} = 10V$
- Low gate charge ( typical 55 nC)
- Low  $C_{rss}$  ( typical 24 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

| Symbol         | Parameter                                                                     | FQA10N80    | Units               |
|----------------|-------------------------------------------------------------------------------|-------------|---------------------|
| $V_{DSS}$      | Drain-Source Voltage                                                          | 800         | V                   |
| $I_D$          | Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )                       | 9.8         | A                   |
|                | - Continuous ( $T_C = 100^\circ\text{C}$ )                                    | 6.19        | A                   |
| $I_{DM}$       | Drain Current - Pulsed (Note 1)                                               | 39.2        | A                   |
| $V_{GSS}$      | Gate-Source Voltage                                                           | $\pm 30$    | V                   |
| $E_{AS}$       | Single Pulsed Avalanche Energy (Note 2)                                       | 920         | mJ                  |
| $I_{AR}$       | Avalanche Current (Note 1)                                                    | 9.8         | A                   |
| $E_{AR}$       | Repetitive Avalanche Energy (Note 1)                                          | 24          | mJ                  |
| dv/dt          | Peak Diode Recovery dv/dt (Note 3)                                            | 4.0         | V/ns                |
| $P_D$          | Power Dissipation ( $T_C = 25^\circ\text{C}$ )                                | 240         | W                   |
|                | - Derate above $25^\circ\text{C}$                                             | 1.92        | W/ $^\circ\text{C}$ |
| $T_J, T_{STG}$ | Operating and Storage Temperature Range                                       | -55 to +150 | $^\circ\text{C}$    |
| $T_L$          | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | 300         | $^\circ\text{C}$    |

### Thermal Characteristics

| Symbol          | Parameter                               | Typ  | Max  | Units              |
|-----------------|-----------------------------------------|------|------|--------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case    | --   | 0.52 | $^\circ\text{C/W}$ |
| $R_{\theta CS}$ | Thermal Resistance, Case-to-Sink        | 0.24 | --   | $^\circ\text{C/W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient | --   | 40   | $^\circ\text{C/W}$ |

**Electrical Characteristics** $T_C = 25^\circ\text{C}$  unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
|--------|-----------|-----------------|-----|-----|-----|-------|
|--------|-----------|-----------------|-----|-----|-----|-------|

**Off Characteristics**

|                                |                                           |                                                                   |     |     |      |                     |
|--------------------------------|-------------------------------------------|-------------------------------------------------------------------|-----|-----|------|---------------------|
| $BV_{DSS}$                     | Drain-Source Breakdown Voltage            | $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$               | 800 | --  | --   | V                   |
| $\Delta BV_{DSS} / \Delta T_J$ | Breakdown Voltage Temperature Coefficient | $I_D = 250\text{ }\mu\text{A}$ , Referenced to $25^\circ\text{C}$ | --  | 0.9 | --   | V/ $^\circ\text{C}$ |
| $I_{DSS}$                      | Zero Gate Voltage Drain Current           | $V_{DS} = 800\text{ V}, V_{GS} = 0\text{ V}$                      | --  | --  | 10   | $\mu\text{A}$       |
|                                |                                           | $V_{DS} = 640\text{ V}, T_C = 125^\circ\text{C}$                  | --  | --  | 100  | $\mu\text{A}$       |
| $I_{GSSF}$                     | Gate-Body Leakage Current, Forward        | $V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$                       | --  | --  | 100  | nA                  |
| $I_{GSSR}$                     | Gate-Body Leakage Current, Reverse        | $V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$                      | --  | --  | -100 | nA                  |

**On Characteristics**

|              |                                   |                                                     |     |      |      |          |
|--------------|-----------------------------------|-----------------------------------------------------|-----|------|------|----------|
| $V_{GS(th)}$ | Gate Threshold Voltage            | $V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$     | 3.0 | --   | 5.0  | V        |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance | $V_{GS} = 10\text{ V}, I_D = 4.9\text{ A}$          | --  | 0.81 | 1.05 | $\Omega$ |
| $g_{FS}$     | Forward Transconductance          | $V_{DS} = 50\text{ V}, I_D = 4.9\text{ A}$ (Note 4) | --  | 10   | --   | S        |

**Dynamic Characteristics**

|           |                              |                                                                      |    |      |      |    |
|-----------|------------------------------|----------------------------------------------------------------------|----|------|------|----|
| $C_{iss}$ | Input Capacitance            | $V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$<br>$f = 1.0\text{ MHz}$ | -- | 2100 | 2700 | pF |
| $C_{oss}$ | Output Capacitance           |                                                                      | -- | 215  | 280  | pF |
| $C_{rss}$ | Reverse Transfer Capacitance |                                                                      | -- | 24   | 30   | pF |

**Switching Characteristics**

|              |                     |                                                                                             |    |     |     |    |
|--------------|---------------------|---------------------------------------------------------------------------------------------|----|-----|-----|----|
| $t_{d(on)}$  | Turn-On Delay Time  | $V_{DD} = 400\text{ V}, I_D = 9.8\text{ A},$<br>$R_G = 25\text{ }\Omega$<br><br>(Note 4, 5) | -- | 45  | 100 | ns |
| $t_r$        | Turn-On Rise Time   |                                                                                             | -- | 115 | 240 | ns |
| $t_{d(off)}$ | Turn-Off Delay Time |                                                                                             | -- | 125 | 260 | ns |
| $t_f$        | Turn-Off Fall Time  |                                                                                             | -- | 75  | 160 | ns |
| $Q_g$        | Total Gate Charge   | $V_{DS} = 640\text{ V}, I_D = 9.8\text{ A},$<br>$V_{GS} = 10\text{ V}$<br><br>(Note 4, 5)   | -- | 55  | 71  | nC |
| $Q_{gs}$     | Gate-Source Charge  |                                                                                             | -- | 12  | --  | nC |
| $Q_{gd}$     | Gate-Drain Charge   |                                                                                             | -- | 26  | --  | nC |

**Drain-Source Diode Characteristics and Maximum Ratings**

|                 |                                                       |                                                |    |      |     |    |
|-----------------|-------------------------------------------------------|------------------------------------------------|----|------|-----|----|
| I <sub>S</sub>  | Maximum Continuous Drain-Source Diode Forward Current | --                                             | -- | 9.8  | A   |    |
| I <sub>SM</sub> | Maximum Pulsed Drain-Source Diode Forward Current     | --                                             | -- | 39.2 | A   |    |
| V <sub>SD</sub> | Drain-Source Diode Forward Voltage                    | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 9.8 A  | -- | --   | 1.4 | V  |
| t <sub>rr</sub> | Reverse Recovery Time                                 | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 9.8 A, | -- | 780  | --  | ns |
| Q <sub>rr</sub> | Reverse Recovery Charge                               | dI <sub>F</sub> / dt = 100 A/μs (Note 4)       | -- | 9.4  | --  | μC |

**Notes:**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2.  $L = 18\text{ mH}, I_{AS} = 9.8\text{ A}, V_{DD} = 50\text{ V}, R_G = 25\text{ }\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{SD} \leq 9.8\text{ A}, dI/dt \leq 200\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$
5. Essentially independent of operating temperature

## Typical Characteristics

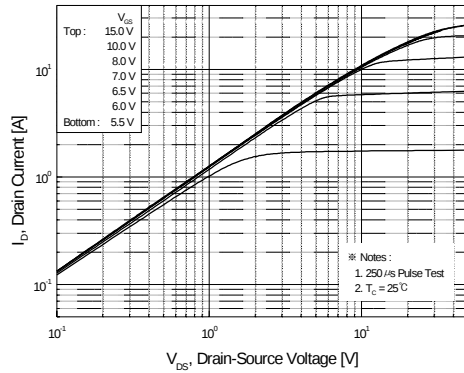


Figure 1. On-Region Characteristics

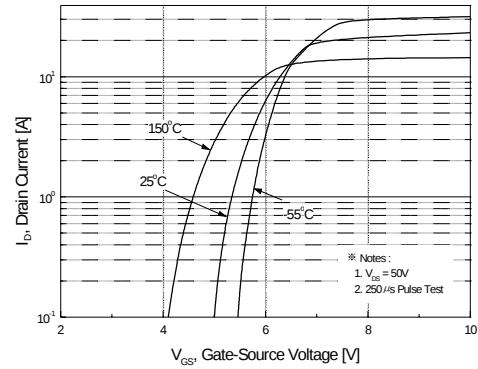


Figure 2. Transfer Characteristics

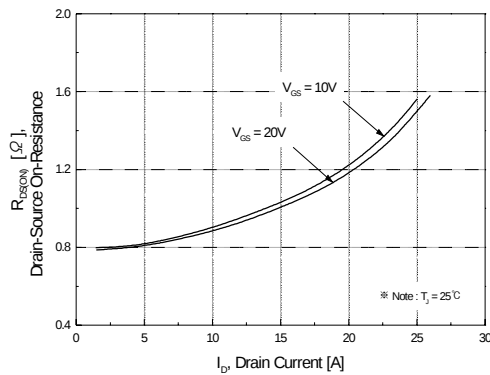


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

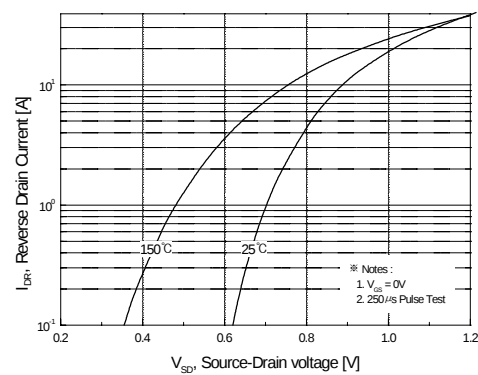


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

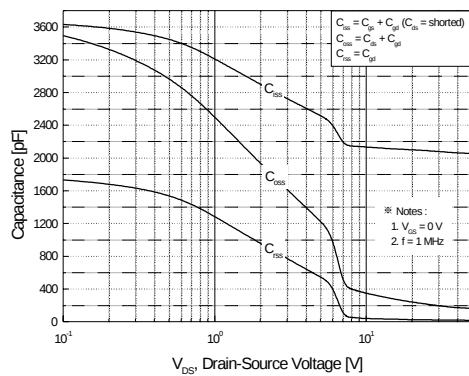


Figure 5. Capacitance Characteristics

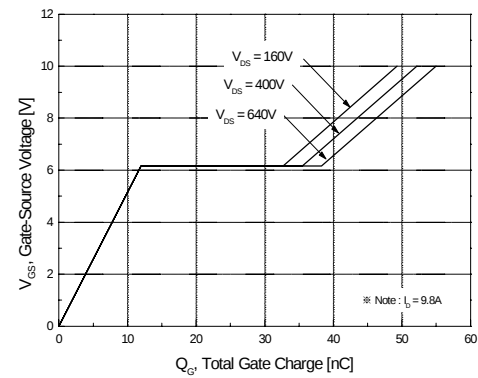
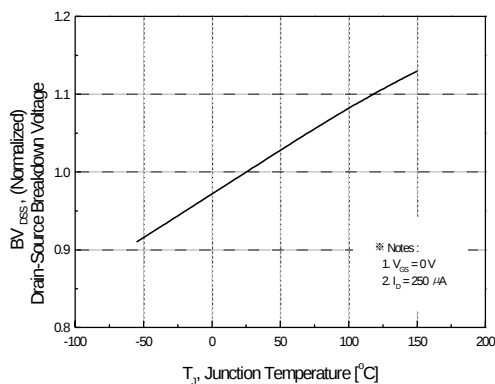
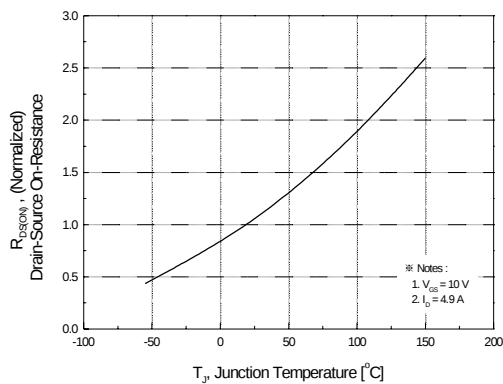


Figure 6. Gate Charge Characteristics

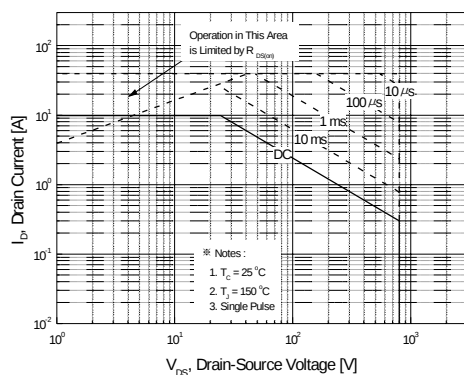
# Typical Characteristics (Continued)



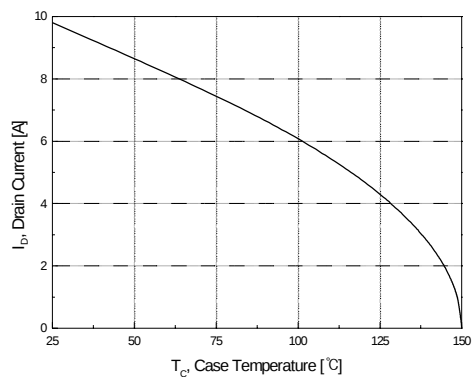
**Figure 7. Breakdown Voltage Variation vs. Temperature**



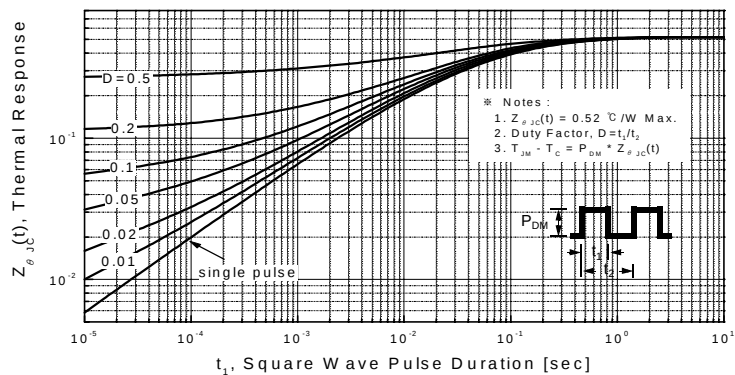
**Figure 8. On-Resistance Variation vs. Temperature**



**Figure 9. Maximum Safe Operating Area**

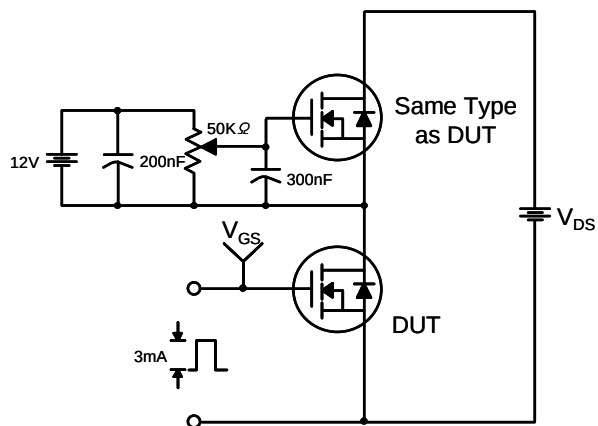


**Figure 10. Maximum Drain Current vs. Case Temperature**

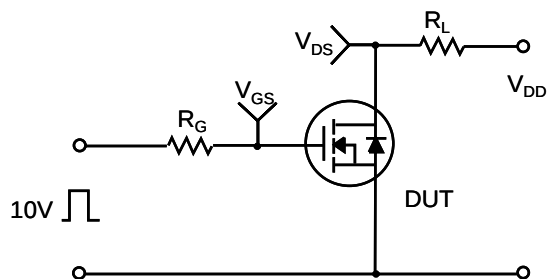


**Figure 11. Transient Thermal Response Curve**

## Gate Charge Test Circuit &amp; Waveform



## Resistive Switching Test Circuit &amp; Waveforms



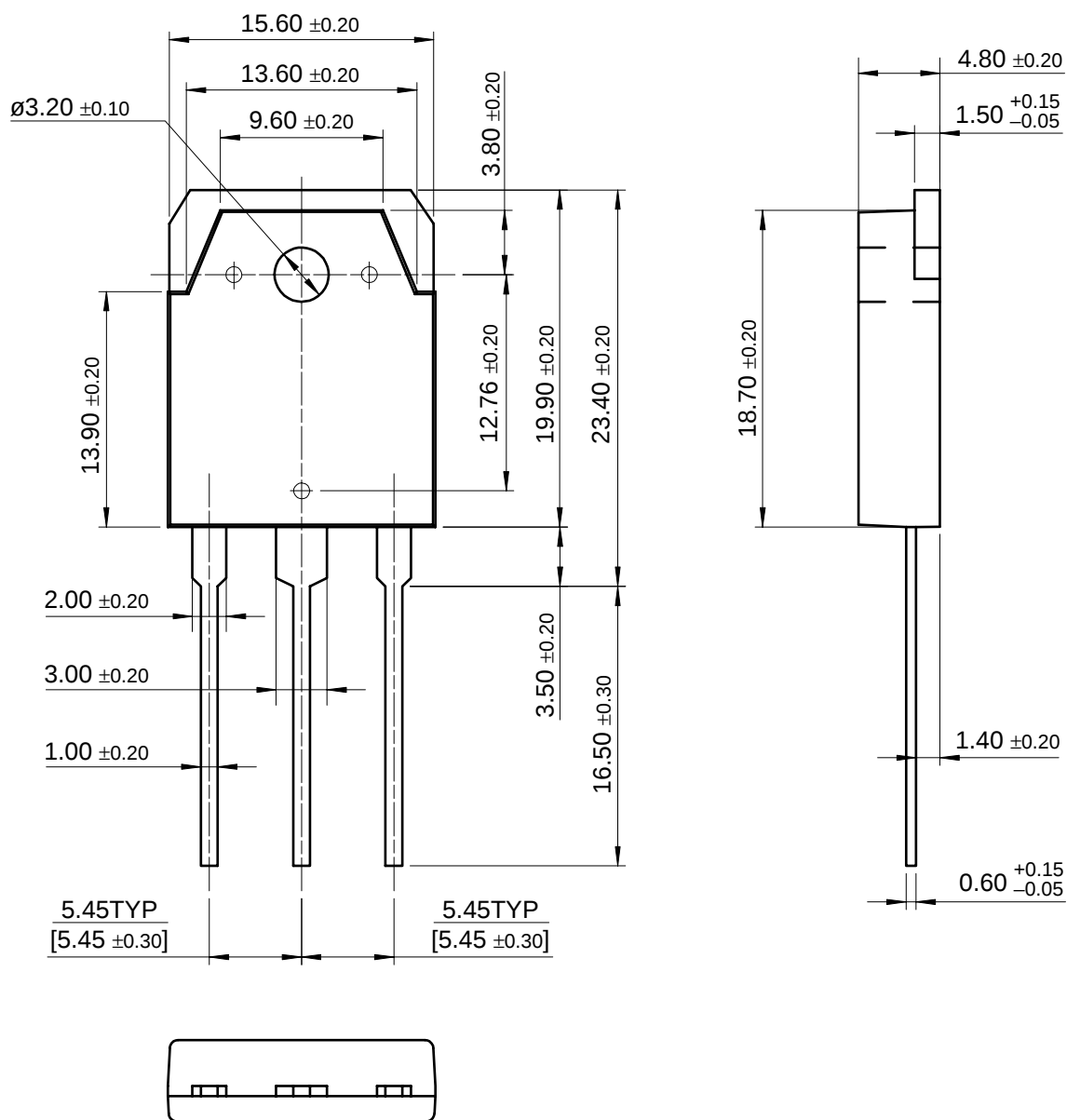
## Unclamped Inductive Switching Test Circuit &amp; Waveforms



Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms

## Package Dimensions

## TO-3P



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| CROSSVOLT™           | HiSeC™              | Quiet Series™       |      |
| DOVE™                | ISOPLANAR™          | SuperSOT™-3         |      |
| E <sup>2</sup> CMOS™ | MICROWIRE™          | SuperSOT™-6         |      |
| EnSigna™             | OPTOLOGIC™          | SuperSOT™-8         |      |
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