

Chip		
Name	MEGA8	
Calibration 0	A8	
Calibration 1	A9	
Calibration 2	A0	
Calibration 3	A1	
Lockbits [FF]		
Lockbit 65	11:No restrictions for SPM or LPM accessing the boot loader section	
Lockbit 43	11:No restrictions for SPM or LPM accessing the application section	
Lockbit 21	11:No memory lock features enabled for parallel and serial programming	
Fusebits [F1]		
Fusebit C	1:BODLEVEL 2.7V	
Fusebit B	1:BODEN disabled	
Fusebit KLA987	110001:reserved	
Fusebits High [D9]		
Fusebit High M	1:PIN PC6 is RESET	
Fusebit High J	1:WDT enabled by WDTCR	
Fusebit High I	0:SPI enabled	
Fusebit High H	1:CKOPT 1	
Fusebit High G	1:Erase EEPROM when chip erase	
Fusebit High FE	00:1024 words boot size, C00	
Fusebit High D	1:Reset vector is \$0000	

Refresh

Write LB

Write FS

Write FSH

Write FSE

Write PRG