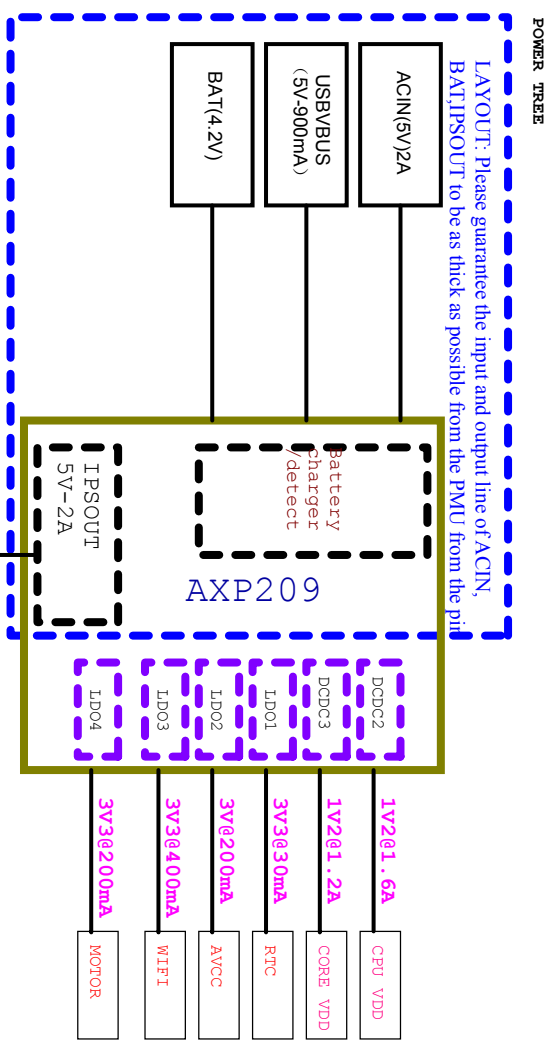


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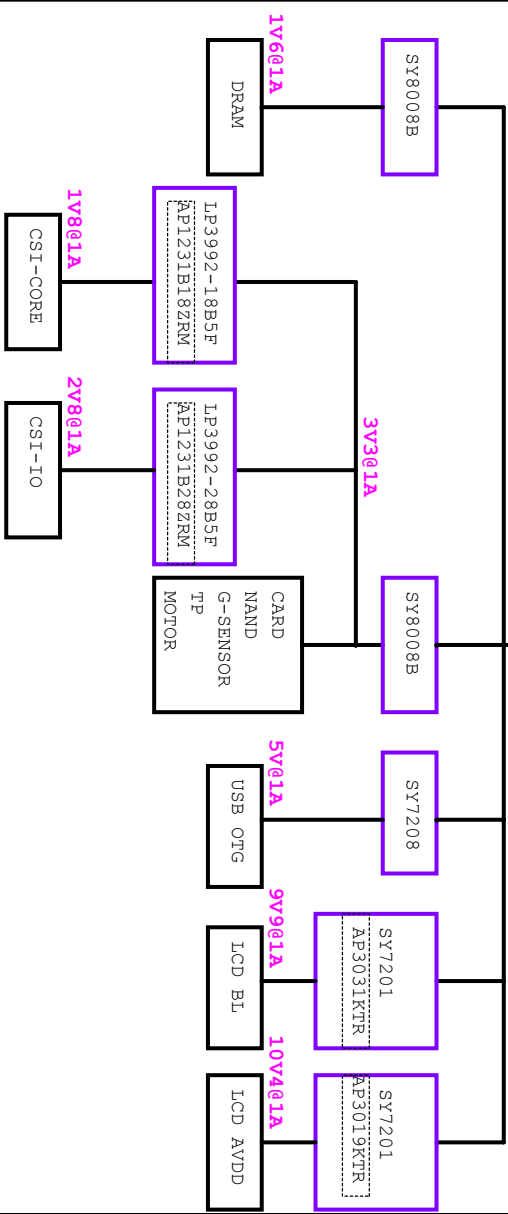
GPIO ASSIGNMENT

GPIO	Definition	Function
PB0	GPIO_SCR	GPIO
PB1	GPIO_SDA	
PB2	PWM0	LCD
PB3	GPIO-IN	SD0-DET-N
PB4	EXT	ALS-INT
PB10	GPIO-OUT	CSI-STY
PB15	Tw11_SCR	Tw11
PB16	Tw11_SDA	
PB17	Tw12_SCR	
PB18	Tw12_SDA	
PG0	GPIO-OUT	PA-SH0N
PG1	GPIO-OUT	MP-BN
PG2	GPIO-OUT	TP-NAKE-RST
PG3	UART-RX	UART-RX
PG4	UART-RX	UART-RX
PG9	EXT	TP-INT
PG10	GPIO-IN	USB0-LDDET
PG11	GPIO-IN	USB0-WBUSDET
PG12	GPIO-OUT	USB0-DRV
PB0	Definition	Function
GPIO0	GPIO-OUT	LCD-PWR
GPIO1	GPIO-OUT	LCD-BL-EN
GPIO2	GPIO-OUT	CSI-PWR-BN
GPIO3	GPIO-OUT	CSI-RST

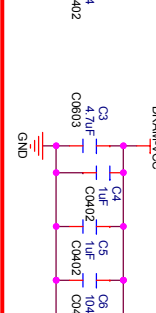
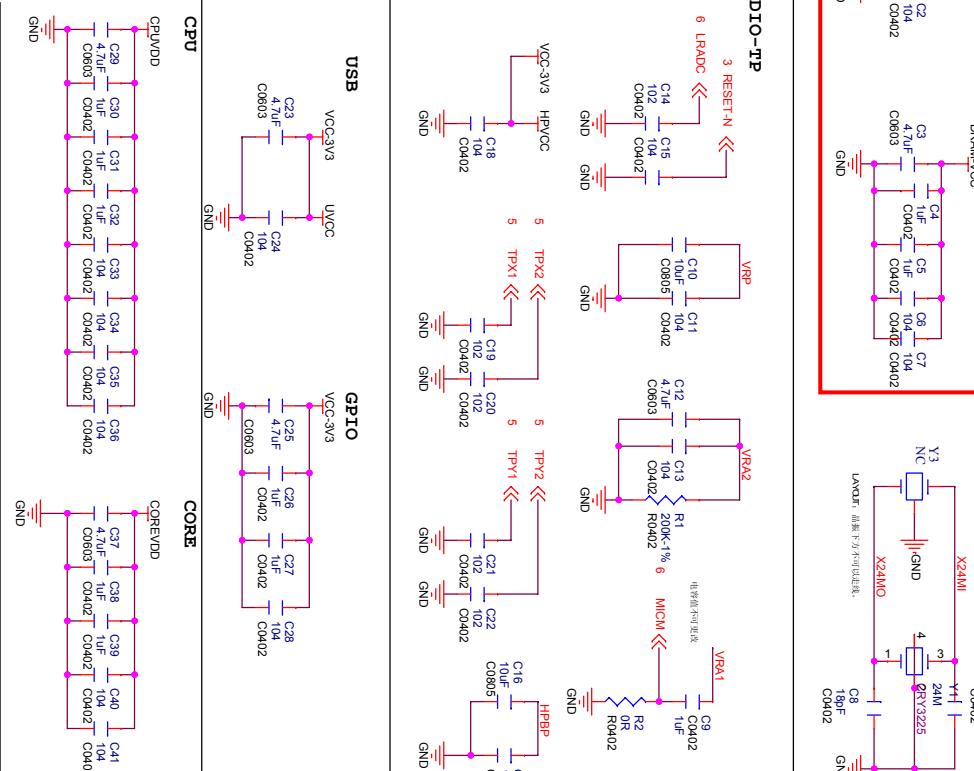


POWER TREE

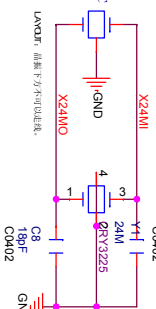
LAYOUT: Please guarantee the input and output line of ACIN, BATJPSOUT to be as thick as possible from the PMU from the pin



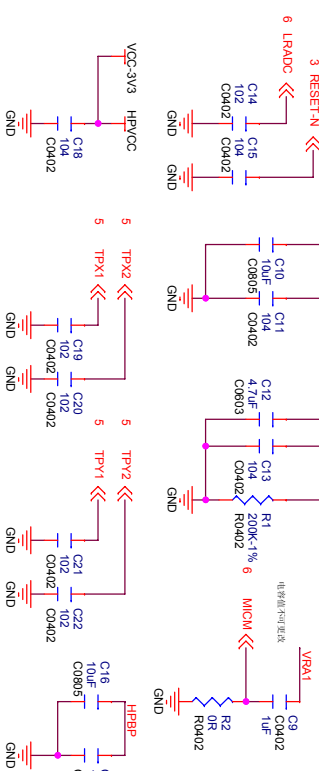
Do not modify the element tag within the frame



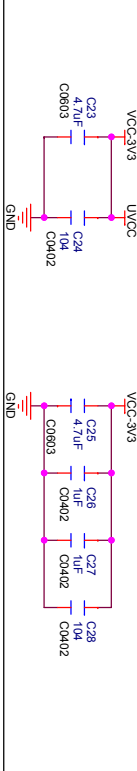
CRYSTAL



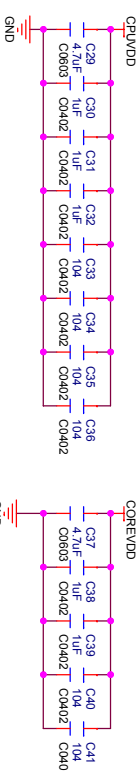
IO-TE



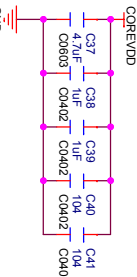
USB



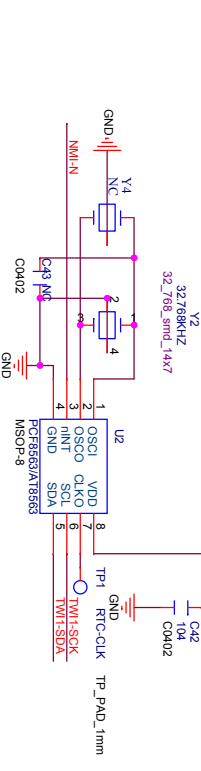
CPD



CORI



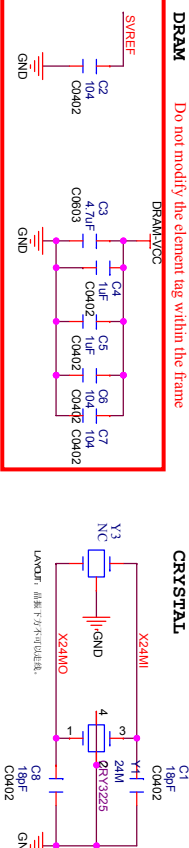
RTC



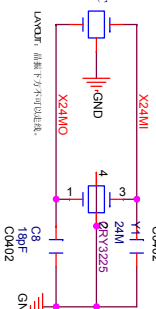
MI-PULVE



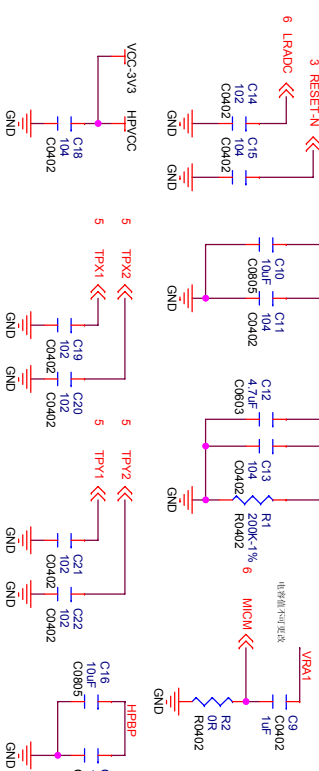
Do not modify the element tag within the frame



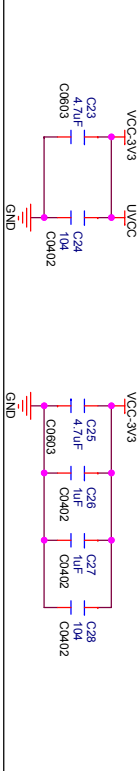
CRYSTAL



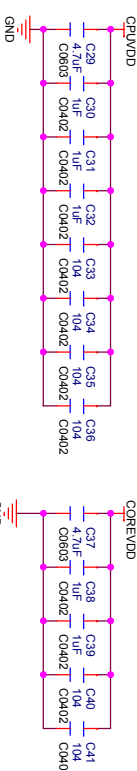
IO-TE



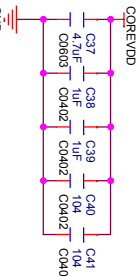
USB



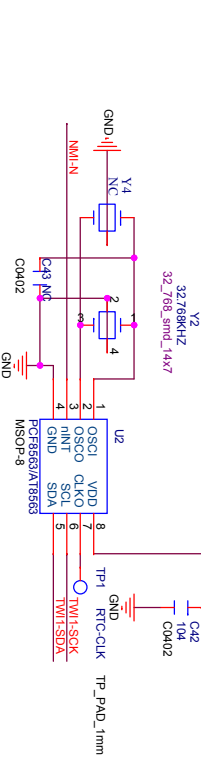
CPD



CORI



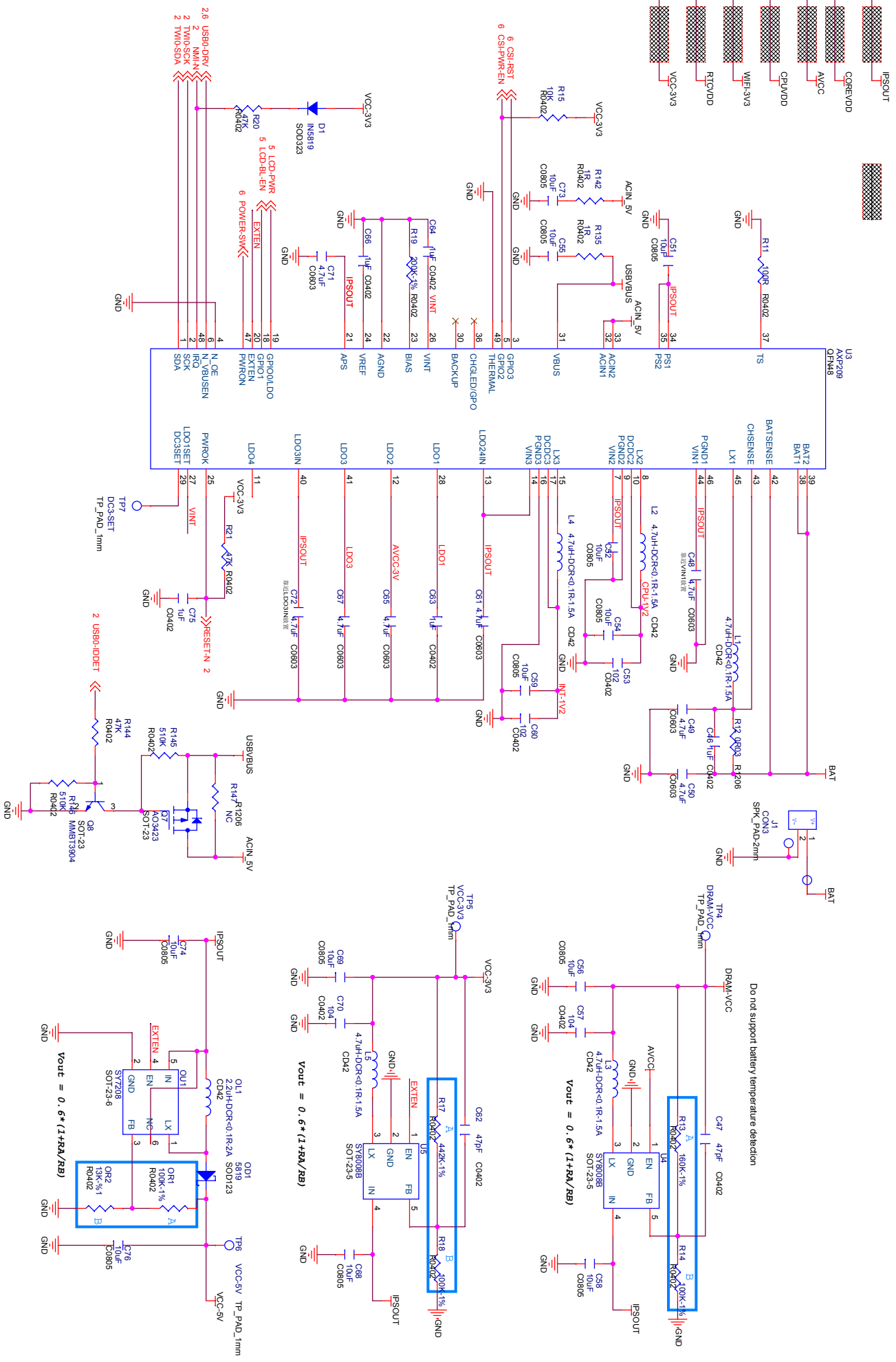
RTC

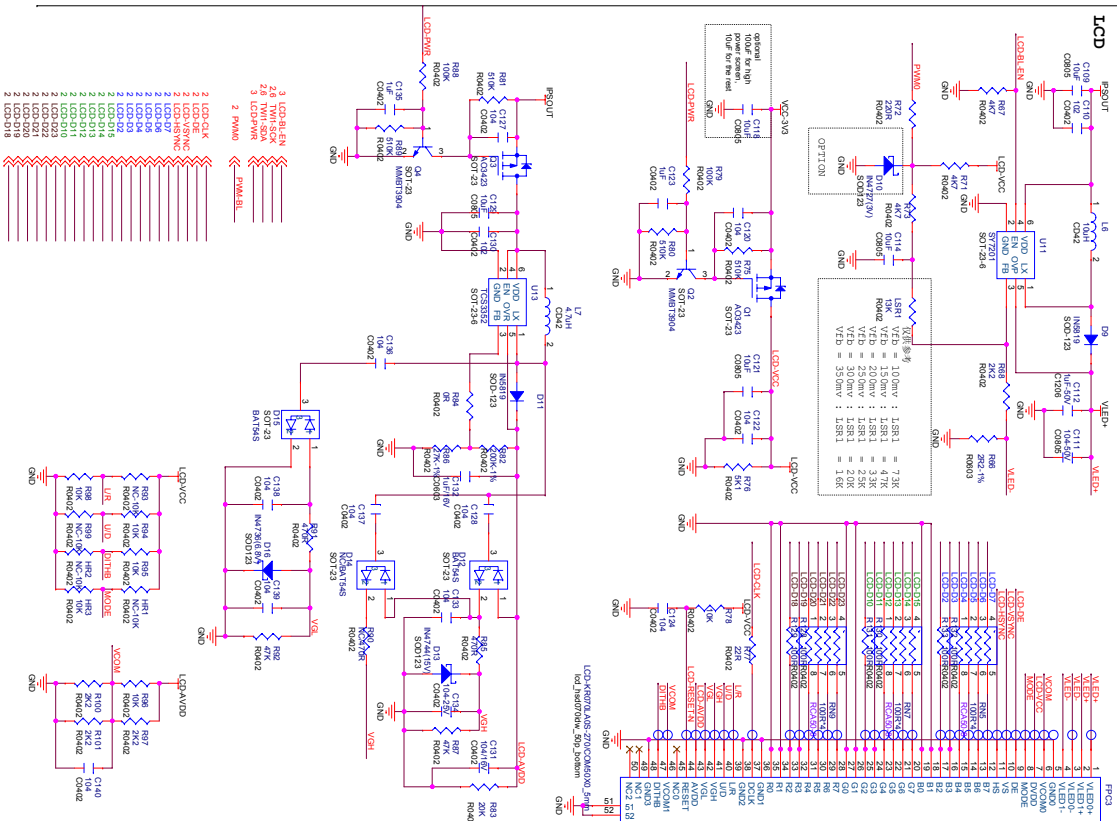


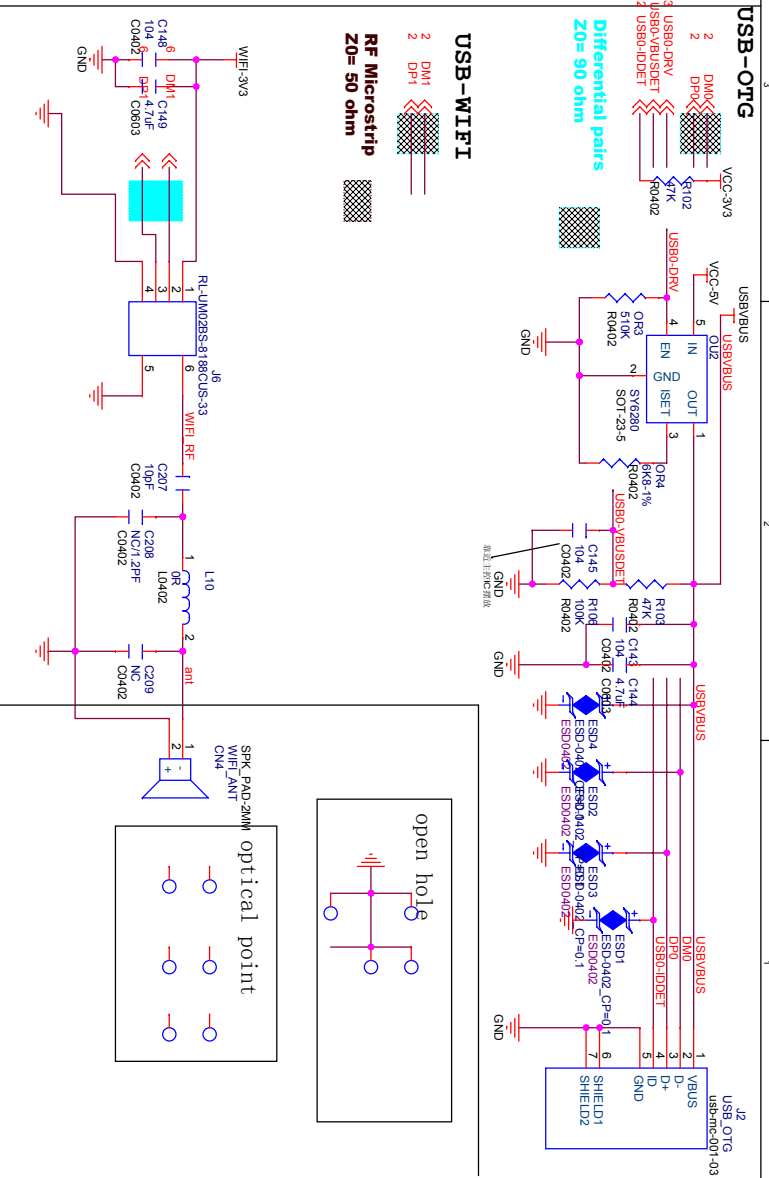
MI-PULVE



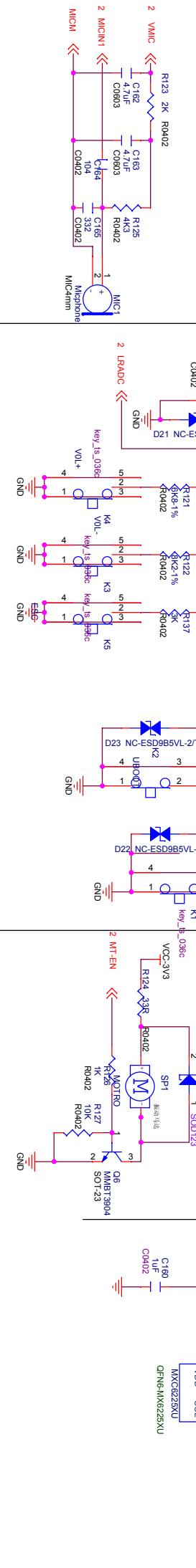
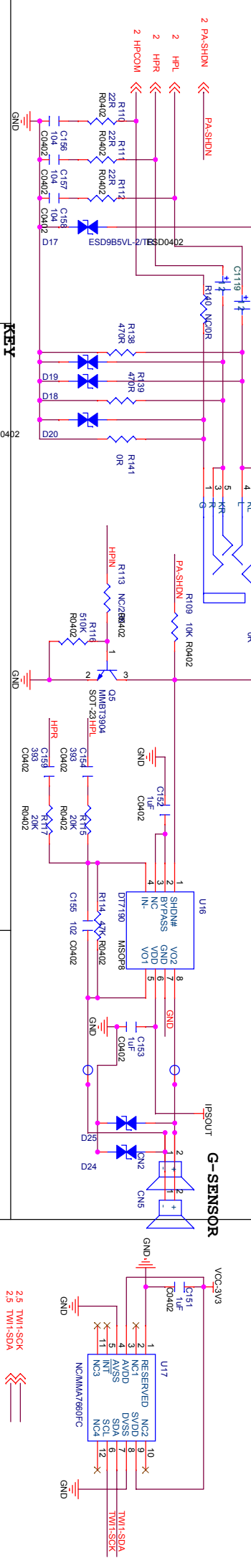
POWER LINE:Width>=60mi





[illegible]

The diagram shows a 24VDC power supply circuit. The input is a 24VDC source labeled 'HPN'. The circuit includes a 2kΩ resistor, a 100μF capacitor, and a 1N4001 diode. The output is labeled 'C1118' and '24VDC'.



DI301 NC/5B19

1	INT	GND	6	GND
2	NC	TVH-SDA	5	TVH-SDA
3	VDD	TVH-SCK	4	TVH-SCK
		SCL		

