VBLE4 / VBLE5

Eureka

LA-8868P REV 1.0 Schematic

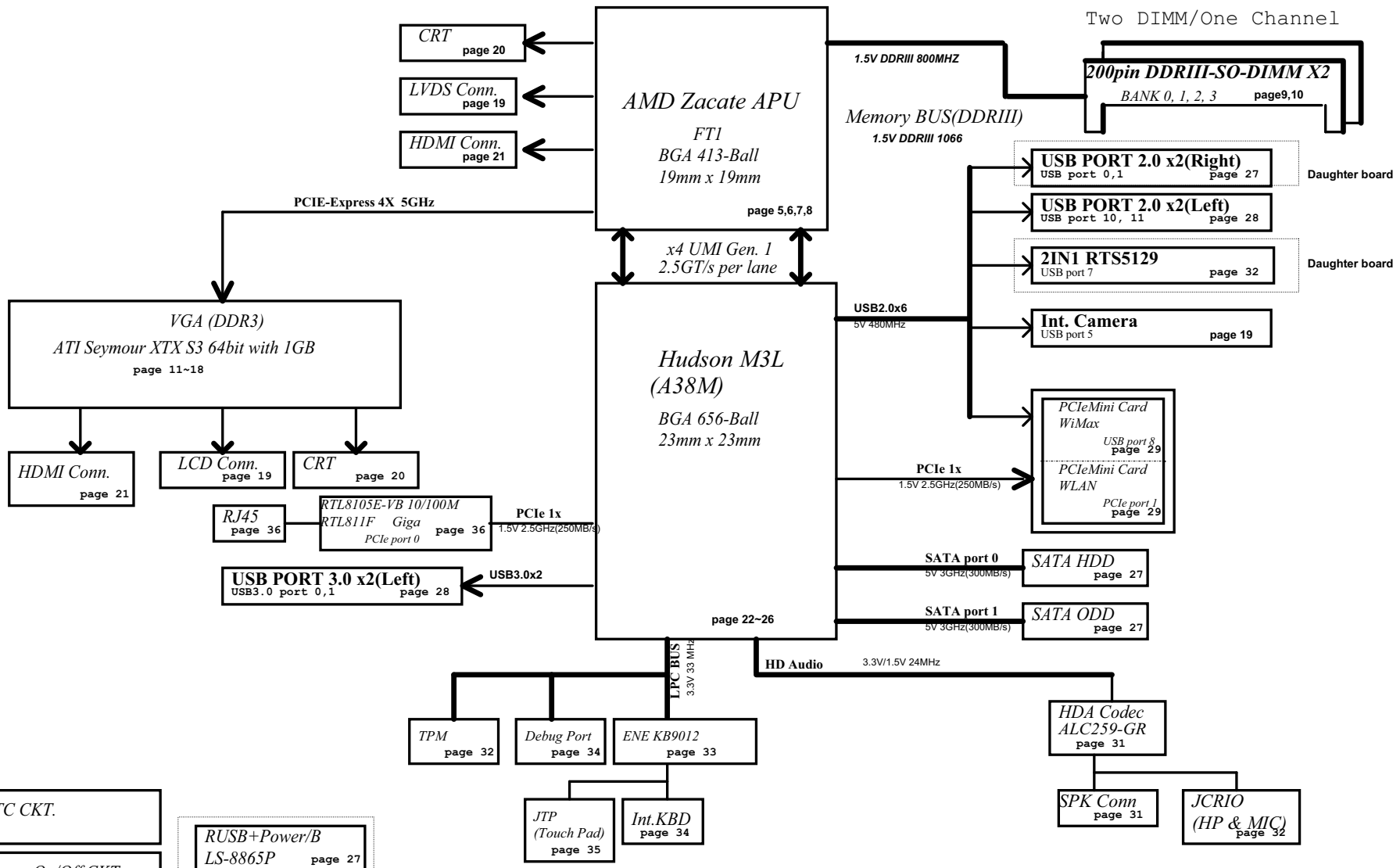
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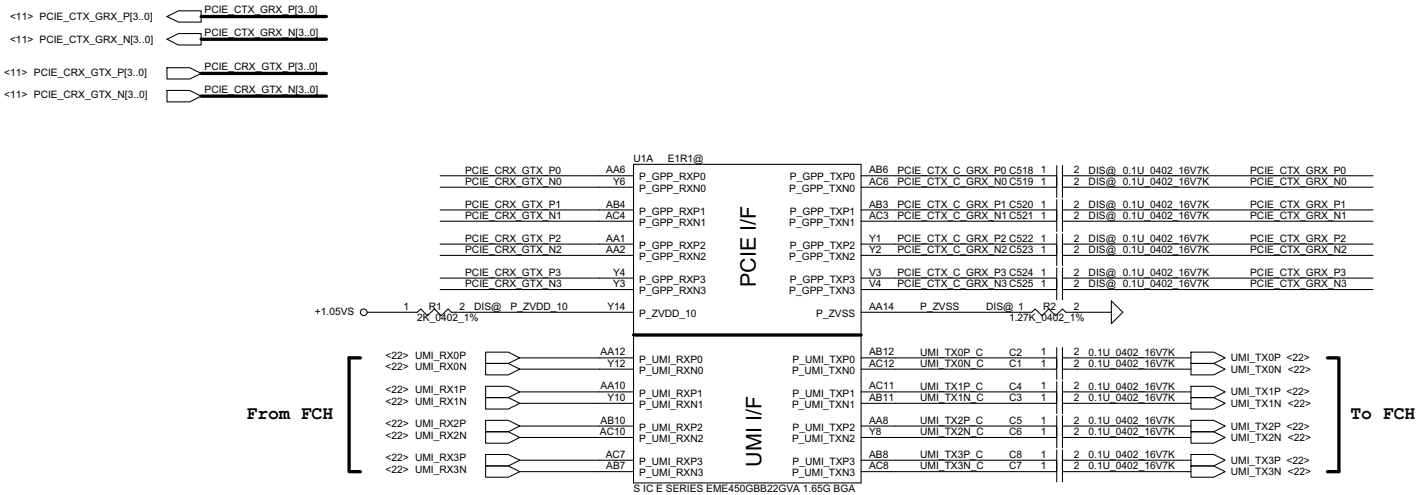
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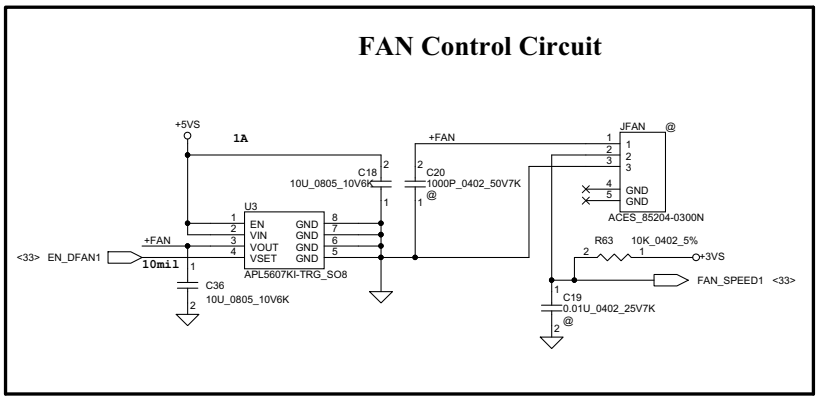
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FAN Control Circuit

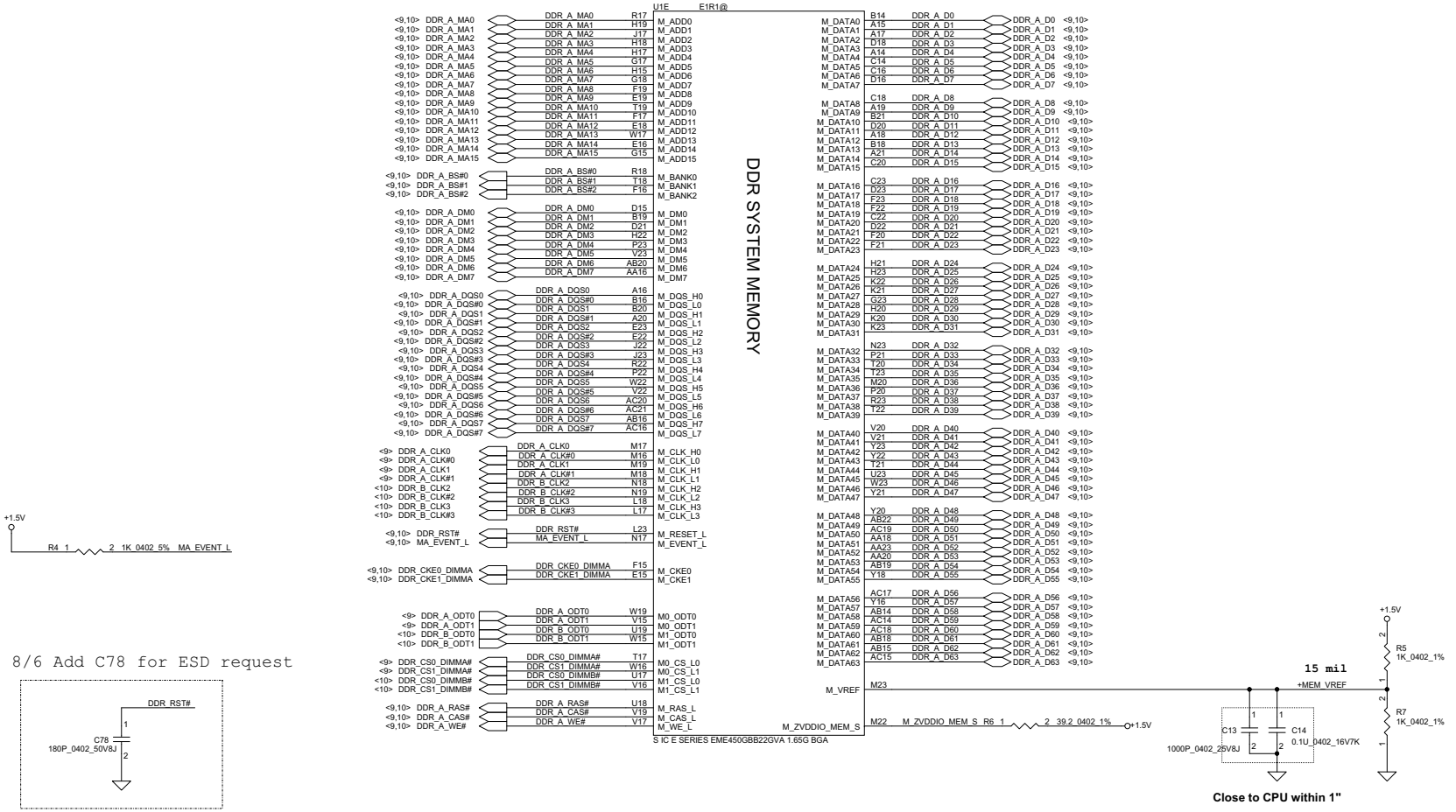


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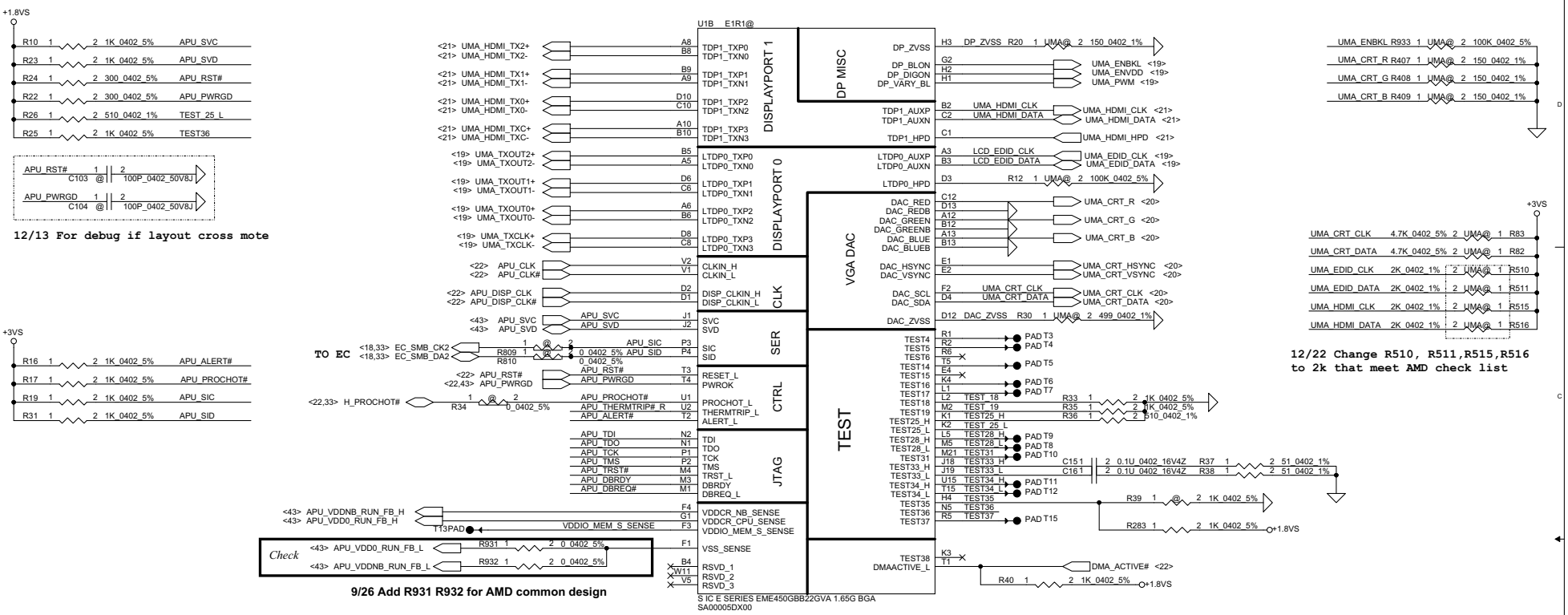


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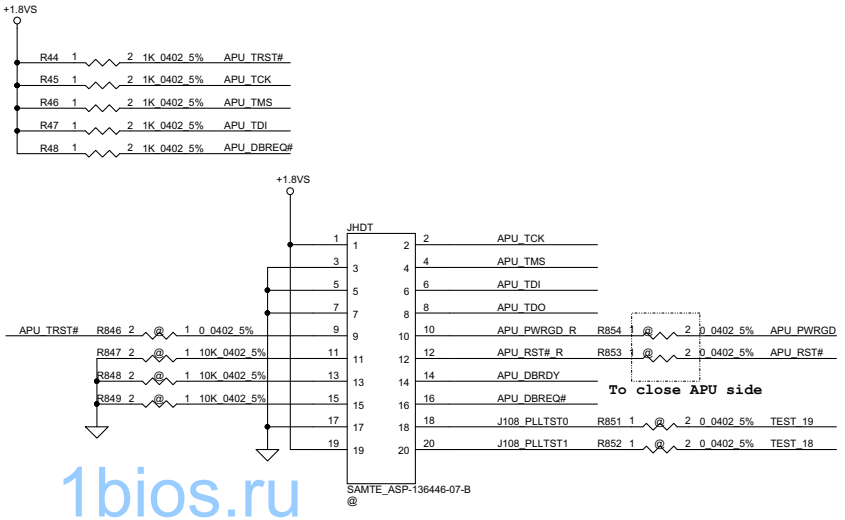
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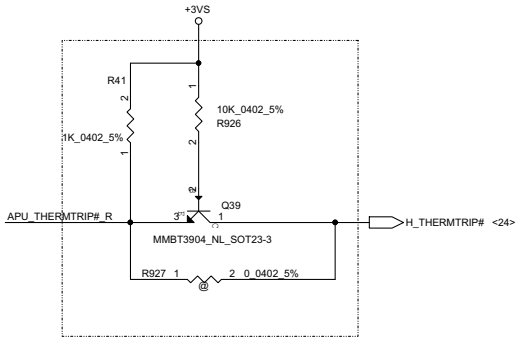
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AMD HDT Debug Conn



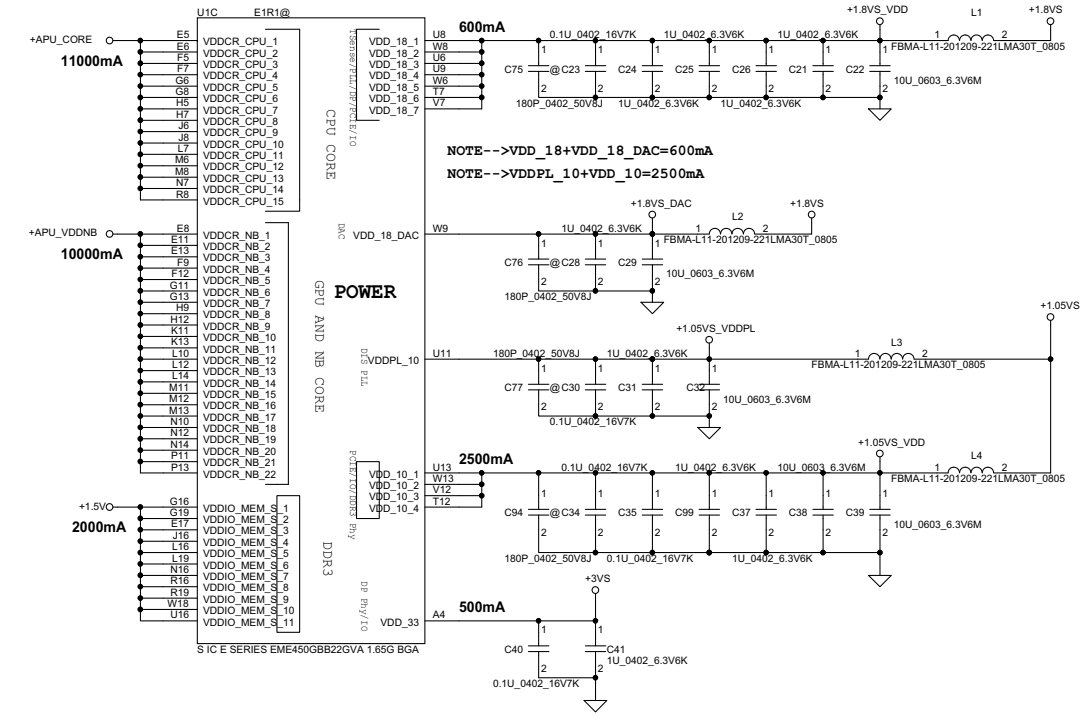


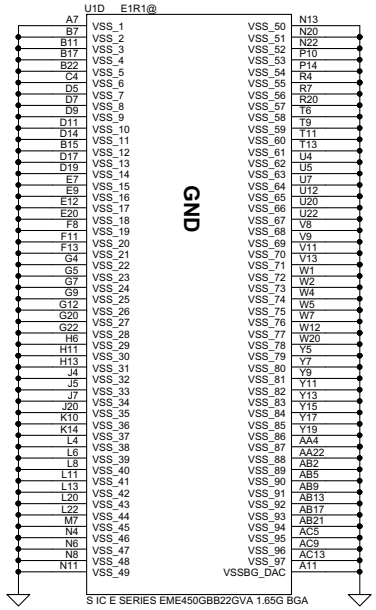
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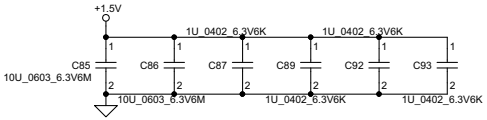
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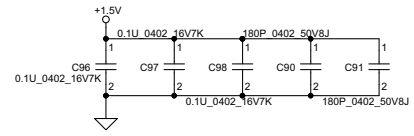
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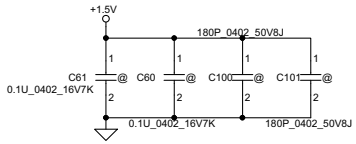
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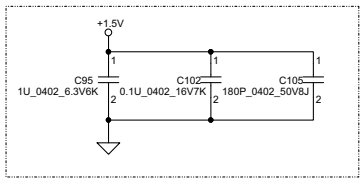








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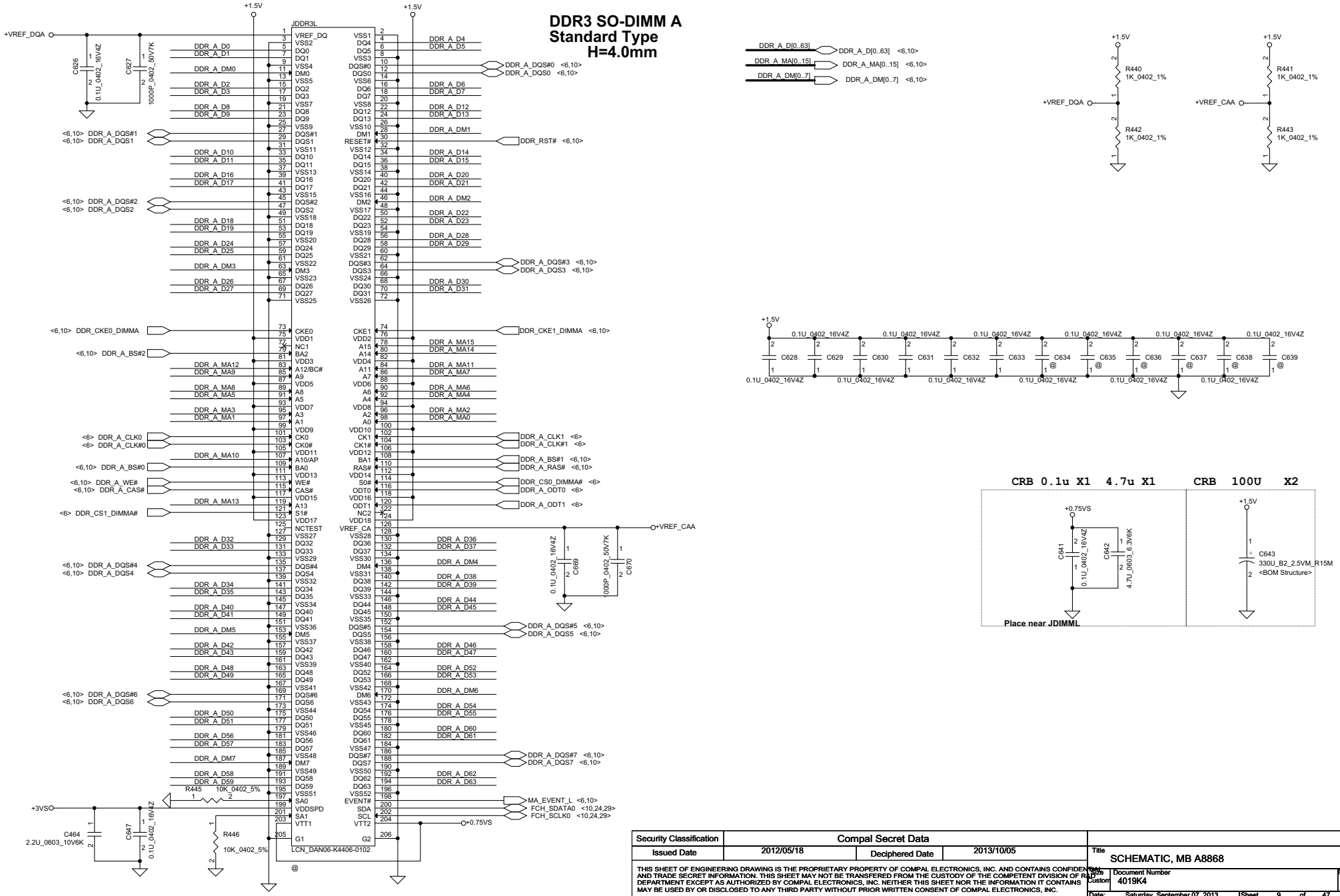
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DDR3 SO-DIMM A
Standard Type
H=4.0mm



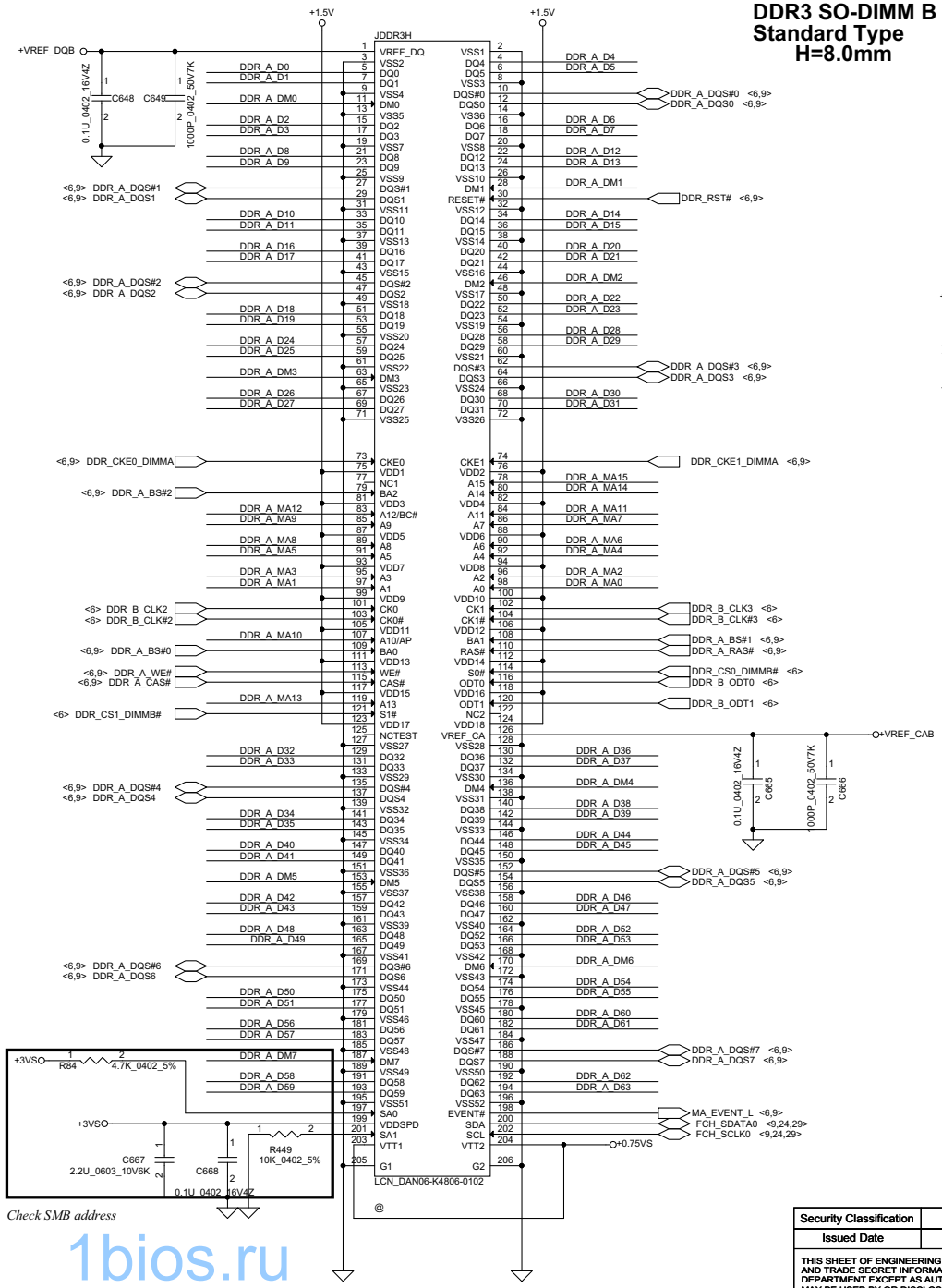
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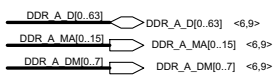
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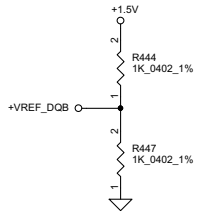
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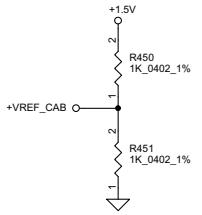
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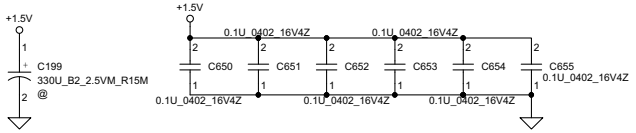
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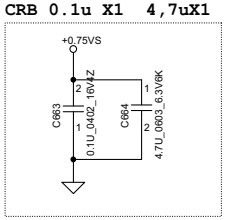








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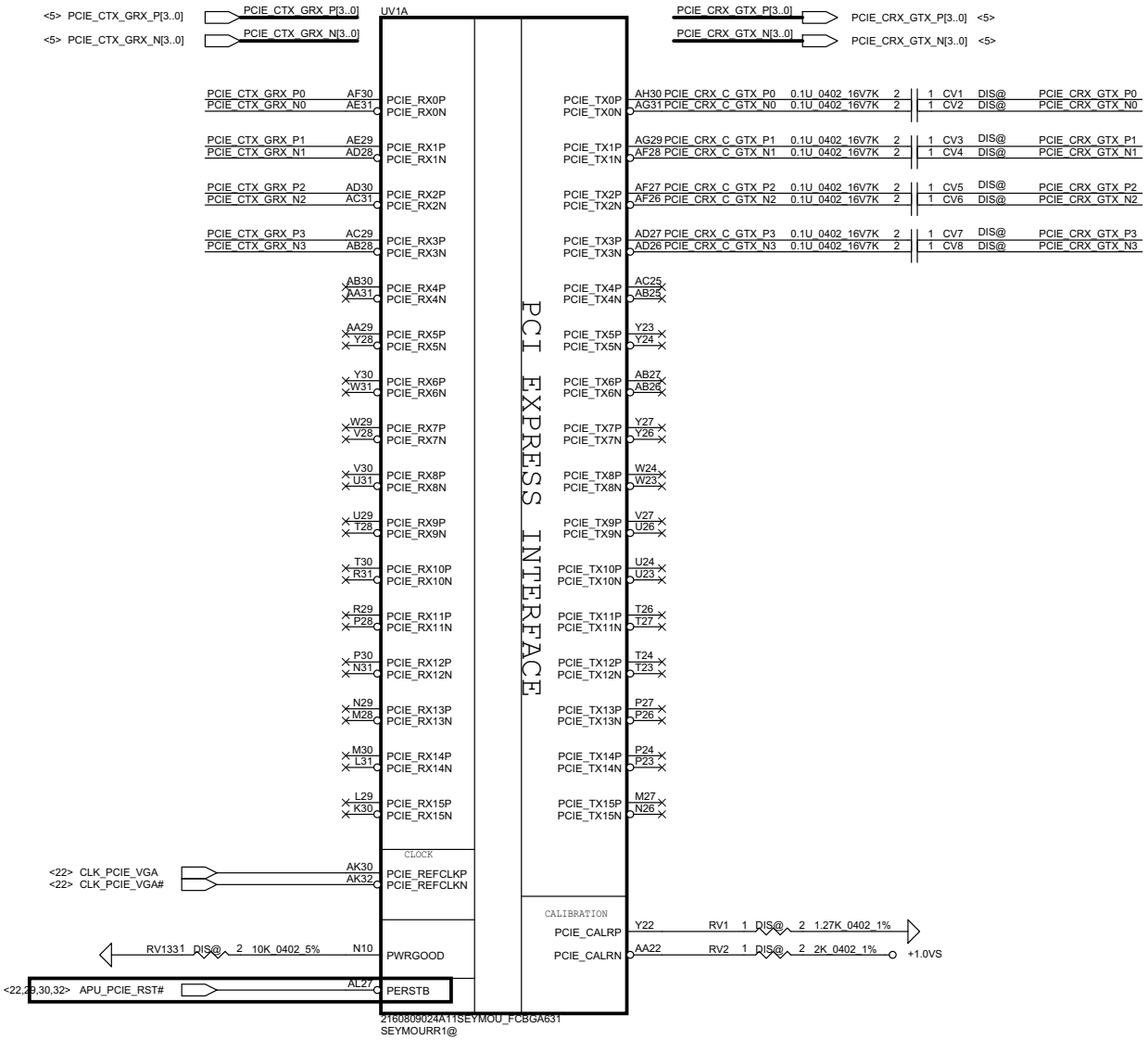


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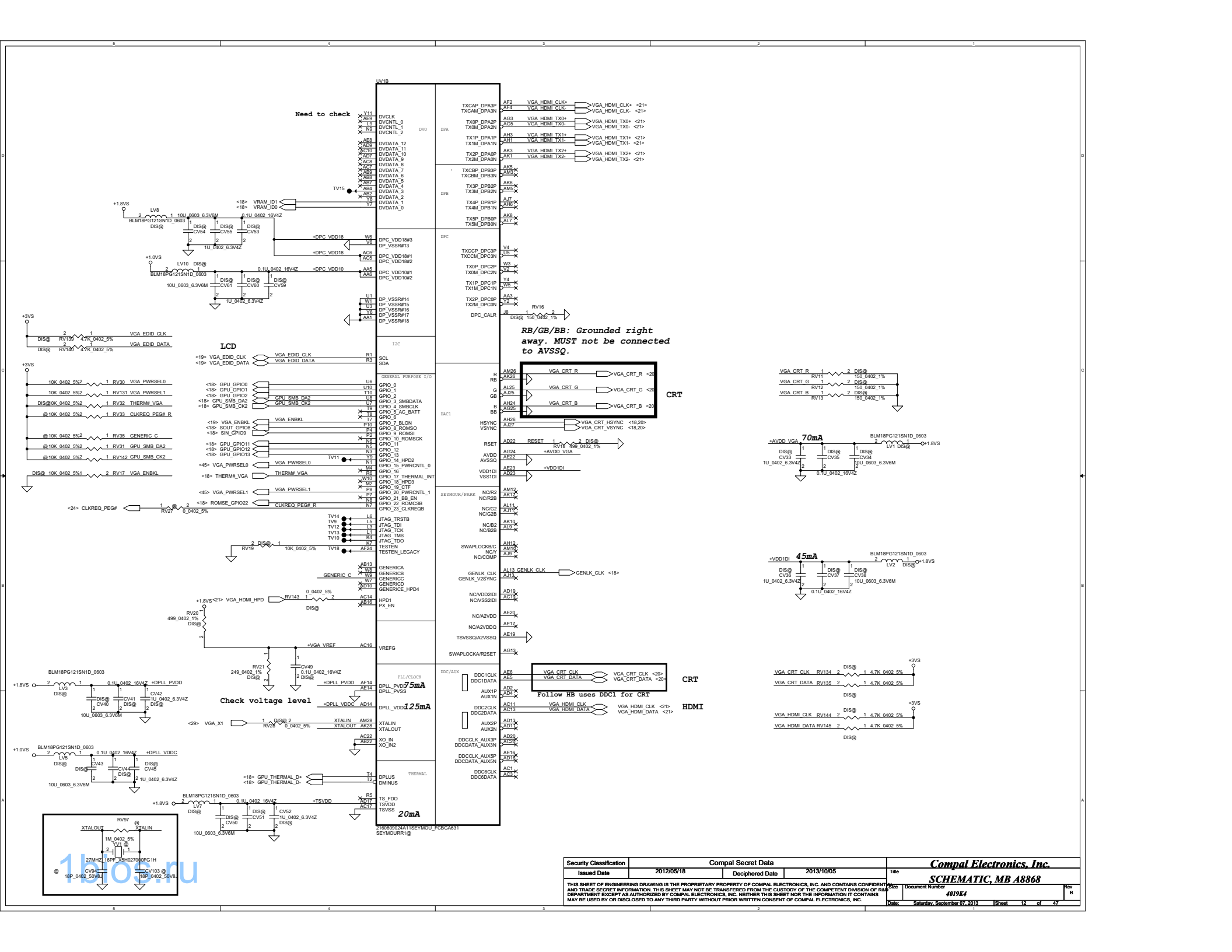


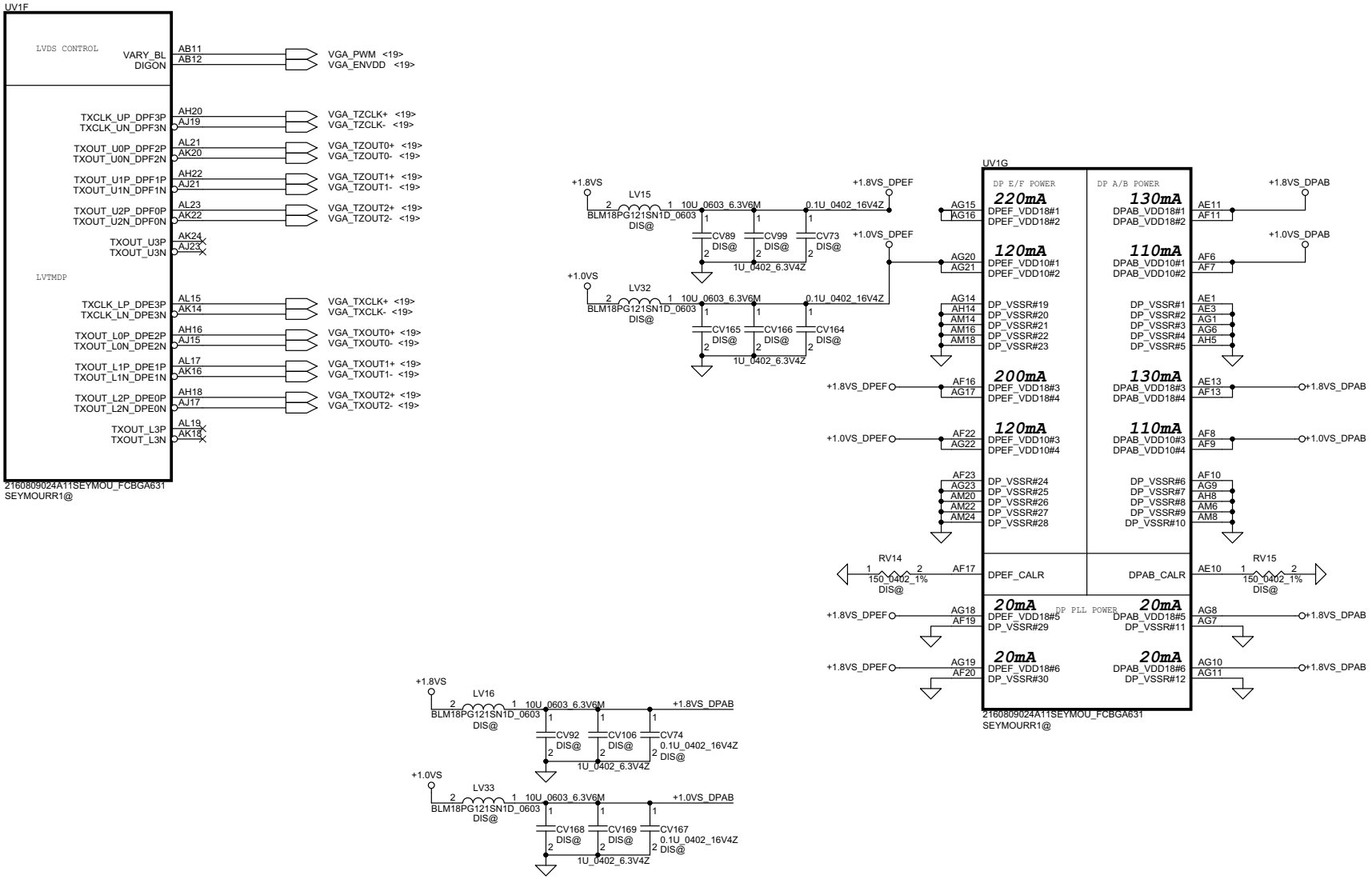
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Need to check
LV1B
LCD
CRT
HDMI
Follow HB uses DDC1 for CRT
RB/GB/BB: Grounded right away. MUST not be connected to AVSSQ.
Check voltage level
70mA
45mA
75mA
125mA
20mA
+3VS
+1.8VS
+1.0VS
+DPC_VDD18
+DPC_VDD10
+AVDD_VGA
+VDD1DI
+DPLL_PVDD
+DPLL_VDDC
+TSVDD
+VGA_VREF
VGA_EDID_CLK
VGA_EDID_DATA
VGA_CRT_R
VGA_CRT_G
VGA_CRT_B
VGA_CRT_HSYNC
VGA_CRT_VSYNC
VGA_HDMI_CLK+
VGA_HDMI_CLK-
VGA_HDMI_TX0+
VGA_HDMI_TX0-
VGA_HDMI_TX1+
VGA_HDMI_TX1-
VGA_HDMI_TX2+
VGA_HDMI_TX2-
VGA_HDMI_HPD
GENLK_CLK
CLKREQ_PEG#
VRAM_ID1
VRAM_ID0
GPU_SMB_DA2
GPU_SMB_CK2
VGA_ENBKL
THERM# VGA
VGA_PWRSEL0
VGA_PWRSEL1
GENERIC_C
ROMSE_GPIO22
GPU_THERMAL_D+
GPU_THERMAL_D-
RESET
DVCLK
DVCNTL_0
DVCNTL_1
DVCNTL_2
DVDATA_12
DVDATA_11
DVDATA_10
DVDATA_9
DVDATA_8
DVDATA_7
DVDATA_6
DVDATA_5
DVDATA_4
DVDATA_3
DVDATA_2
DVDATA_1
DVDATA_0
DPA
DPB
DPC
DVO
I2C
GENERAL PURPOSE I/O
DAC1
SEYMOUR/PARK
PLL/CLOCK
DDC/AUX
THERMAL
JTAG_TRSTB
JTAG_TDI
JTAG_TCK
JTAG_TMS
JTAG_TDO
TESTEN
TESTEN_LEGACY
GENERICA
GENERICB
GENERICC
GENERICD
GENERICE_HPD4
HPD1
PX_EN
VREFG
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DPLL_PVSS
DPLL_VDDC
XTALIN
XTALOUT
XO_IN
XO_IN2
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DPC_CALR
RSET
AVDD
AVSSQ
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VSS1DI
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NC/A2VDDQ
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DDC1CLK
DDC1DATA
DDC2CLK
DDC2DATA
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DDCDATA_AUX3N
DDCCLK_AUX5P
DDCDATA_AUX5N
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DDC6DATA
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AUX1N
AUX2P
AUX2N
NC/R2
NC/R2B
NC/G2
NC/G2B
NC/B2
NC/B2B
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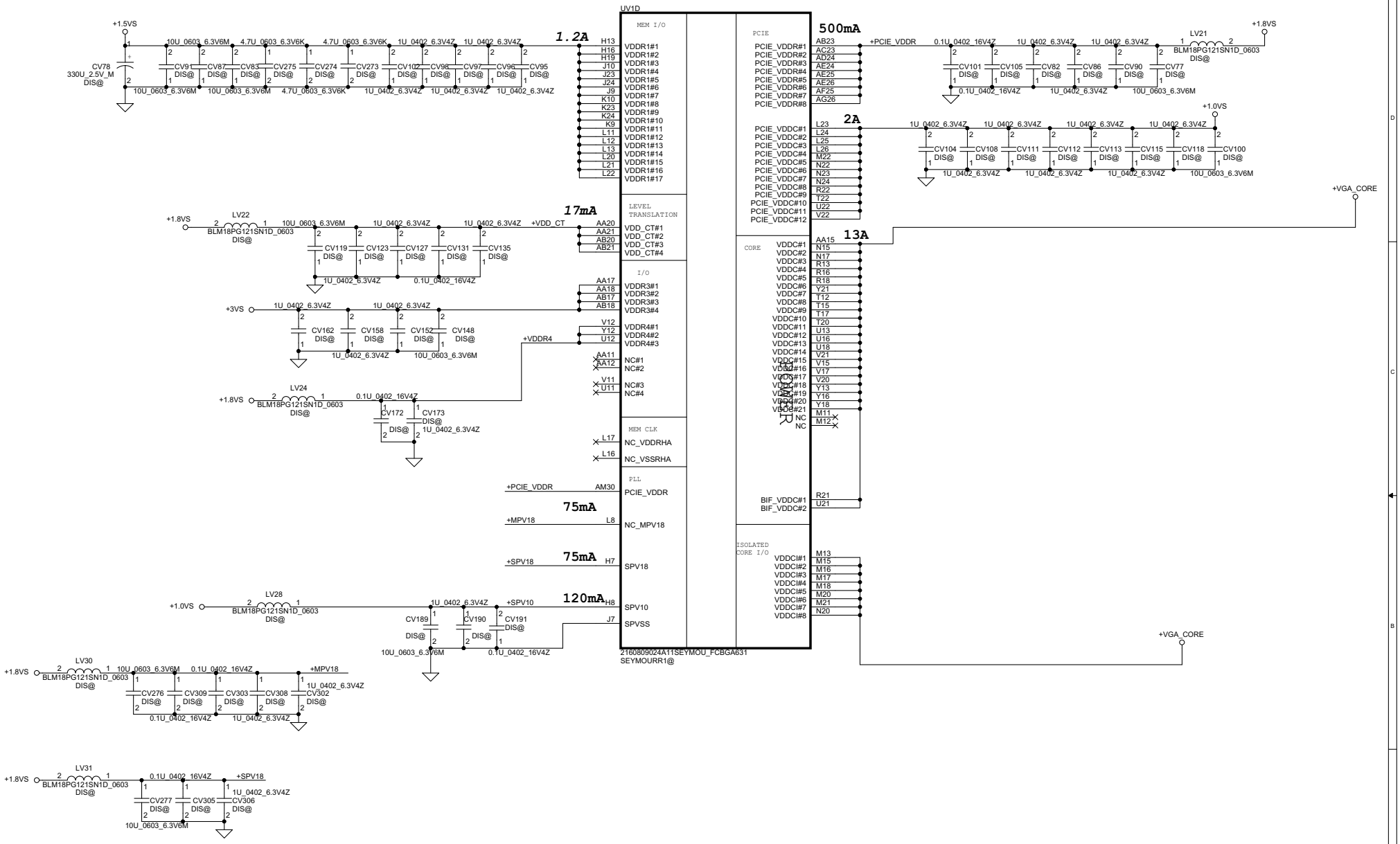
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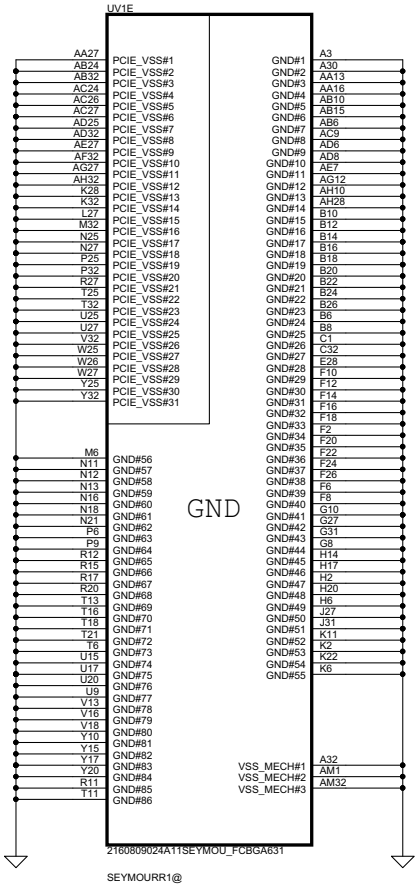
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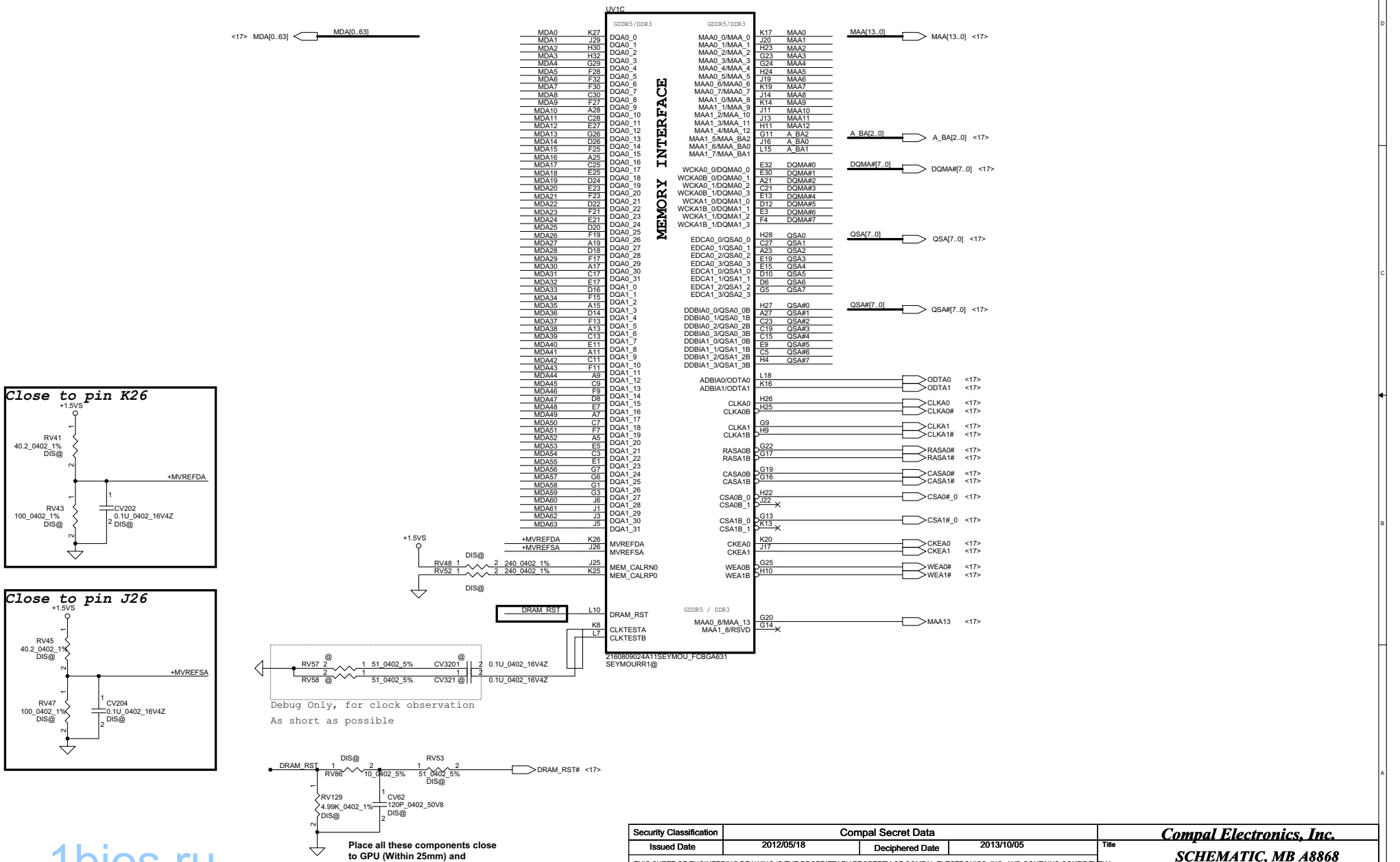
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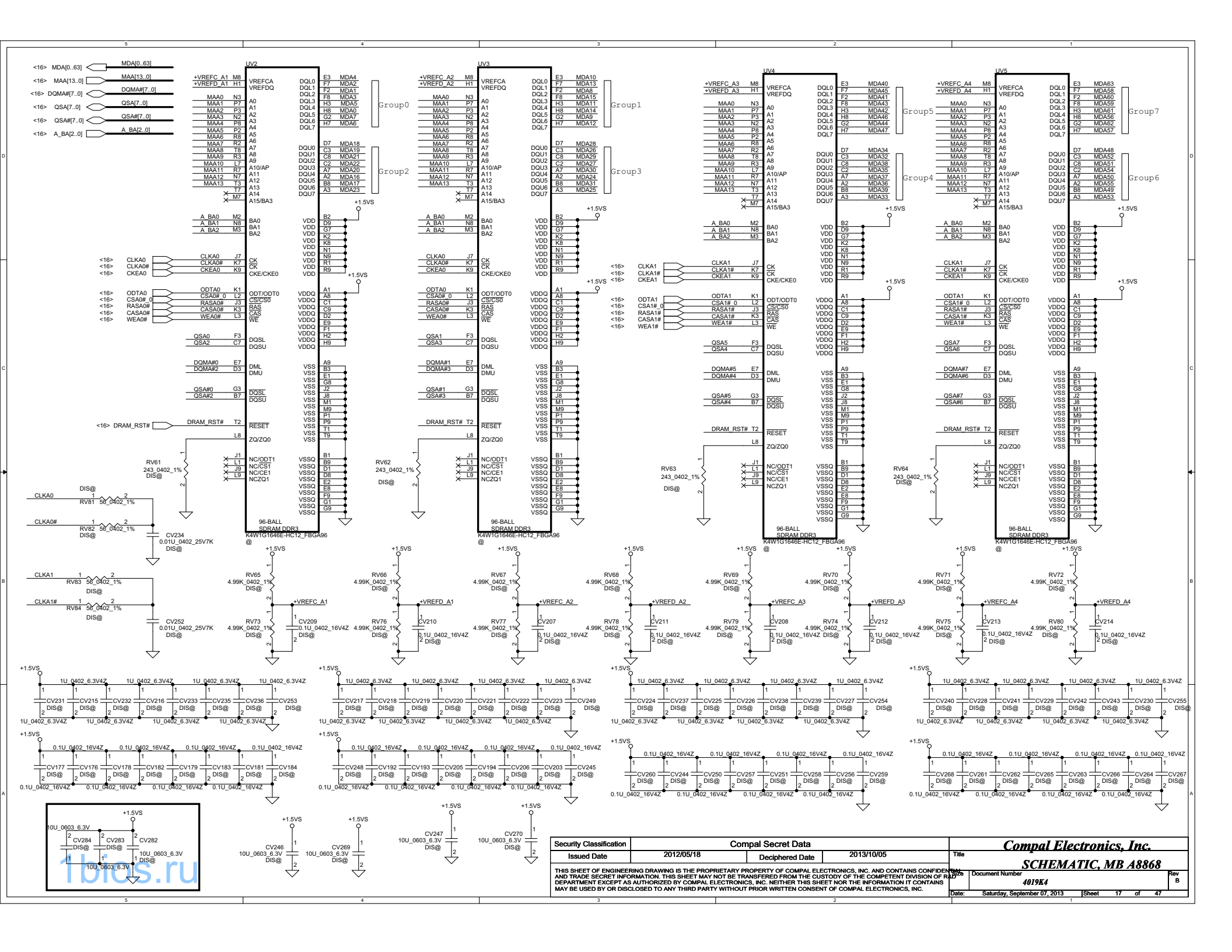


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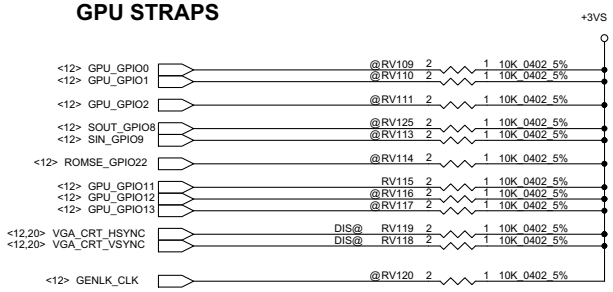
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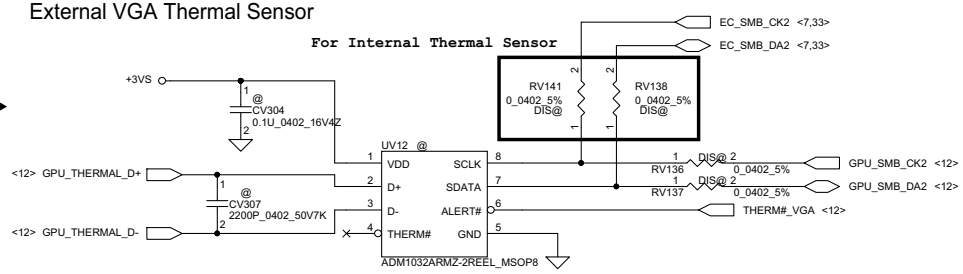
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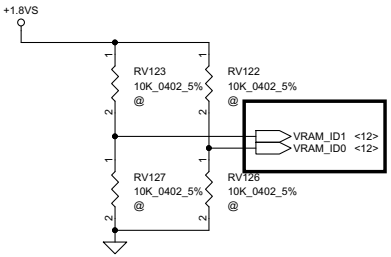
GPU STRAPS



GPU by the system BIOS		GPU by VBIOS
GPIO22 = 0 (BIOS_ROM_EN = 0		GPIO22 = 1 (BIOS_ROM_EN = 1
GPIO[13:11]	MEMORY SIZE	GPIO[13:11]
0 0 0	128MB	1 0 0 (M25P05A)
0 0 1	256MB	
0 1 0	64MB	

External VGA Thermal Sensor





CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOS ARE USED, THEY MUST NOT CONFLICT DURING RESET

Straps Name	Pin Name	Net Name	DESCRIPTION OF DEFAULT SETTINGS	RECOMMENDED SETTINGS
TX_PWRS_ENB	GPIO0	GPU_GPIO0	Transmitter Power Savings Enable 0: 50% Tx output swing 1: Full Tx output swing	0
TX_DEEMPH_EN	GPIO1	GPU_GPIO1	PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled 1: Tx de-emphasis enabled	0
BIF_GEN2_EN_A	GPIO2	GPU_GPIO2	PCIE GNE2 ENABLED 0 = Advertises the PCIe device as 2.5 GT/s capable at power-on 1 = Advertises the PCIe device as 5.0 GT/s capable at power-on.	0 5.0 GT/s capability will be controlled by software
RESERVED	GPIO_8_ROMSO	SOUT_GPIO8	RESERVED	0
RESERVED	GPIO_21_BB_EN	N.C	RESERVED	0 (Internal pulldown)
VGA_DIS	GPIO_9_ROMSI	SIN_GPIO9	VGA Controller 0: VGA Controller capacity enabled 1: The device will not be recognized as the system's VGA controller	0 (Enable)
BIOS_ROM_EN	GPIO_22_ROMCSB	ROMSE_GPIO22	Enable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
CONFIG(2:0)	GPIO[13:11]	GPU_GPIO11 GPU_GPIO12 GPU_GPIO13	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT a) If BIOS_ROM_EN = 1, then Config[2:0] defines the ROM type b) If BIOS_ROM_EN = 0, then Config[2:0] defines the primary memory aperture size.	0 0 1 (256M)
RESERVED	GENLK_CLK	GENLK_CLK		0
AUD[1] AUD[0]	HSYNC VSYNC	VGA_CRT_HSYNC VGA_CRT_VSYNC	AUD[1:0]: 00 - No audio function; 01 - Audio for DisplayPort only; 10 - Audio for DisplayPort and HDMI if dongle is detected; 11 - Audio for both DisplayPort and HDMI.	0 0

STRAPS	PIN	GPU	VRAM size	Vendor Part Number#	Compal Part Number#	VRAM_ID 1,0
VRAM_ID[1:0]	DVDATA (1,0)	Seymour-S3	512M 64Mx16 (x4)	SAM K4W1G1646G-BC11	SA00004GS00	1 0
			1G 128Mx16 (x4)	SAM K4W2G1646C-HC11	SA000047Q00	1 1

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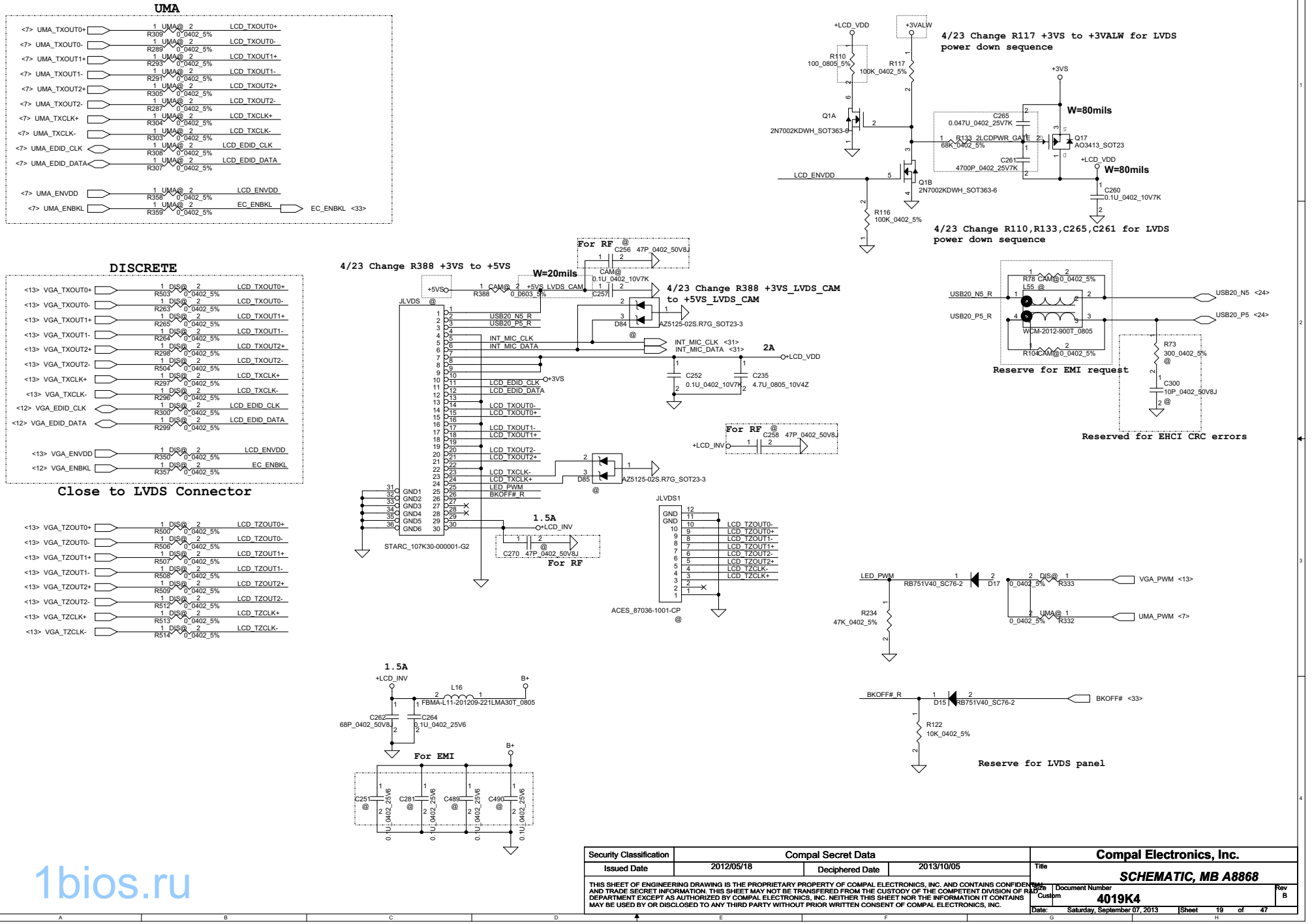
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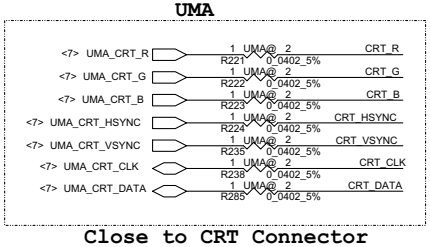
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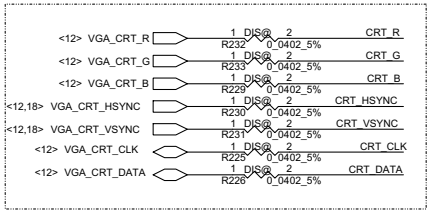
CRT CONNECTOR

UMA



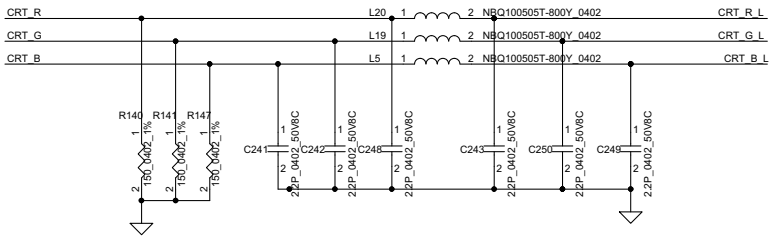
Close to CRT Connector

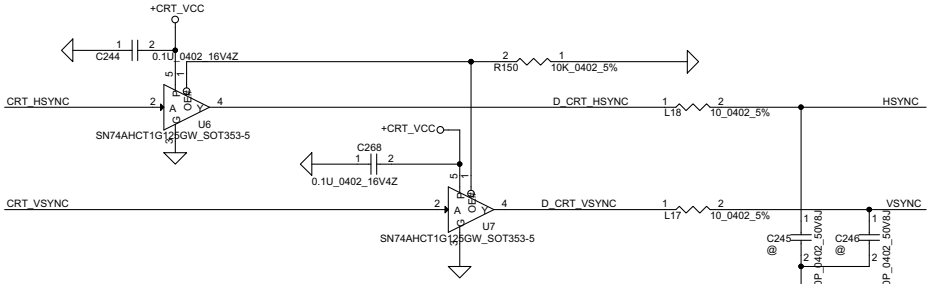
DISCRETE

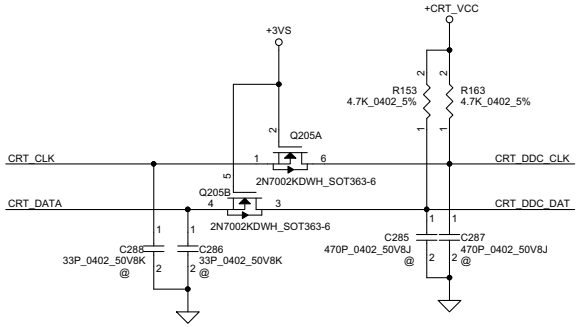


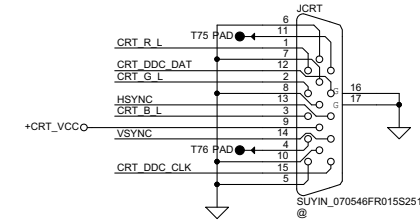
Close to CRT Connector

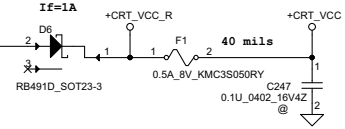
Remove D3~D5 on DVT

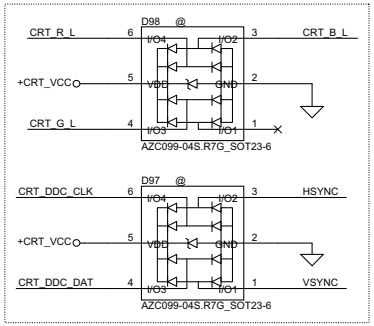












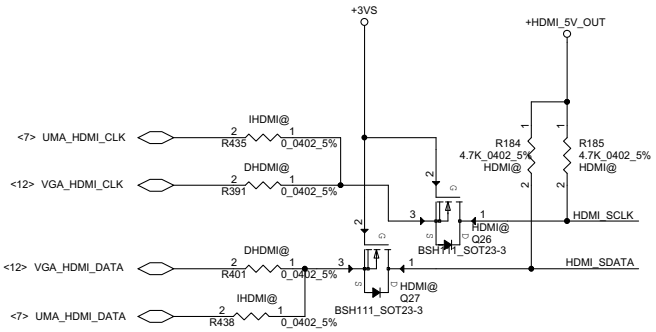
Reserve ESD for CRT connector on DVT

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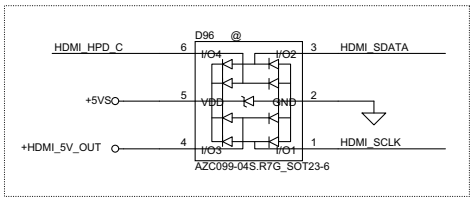


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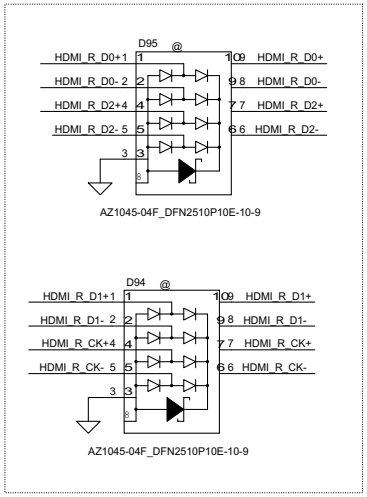
<12> VGA_HDMI_CLK+	CV296	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXC+
<12> VGA_HDMI_CLK-	CV293	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXC-
<12> VGA_HDMI_TX0+	CV294	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD0+
<12> VGA_HDMI_TX0-	CV297	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD0-
<12> VGA_HDMI_TX1+	CV299	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD1+
<12> VGA_HDMI_TX1-	CV298	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD1-
<12> VGA_HDMI_TX2+	CV295	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD2+
<12> VGA_HDMI_TX2-	CV300	1	2 0.1U_0402_16V7K	DHDMI@	VGA_DVI_TXD2-

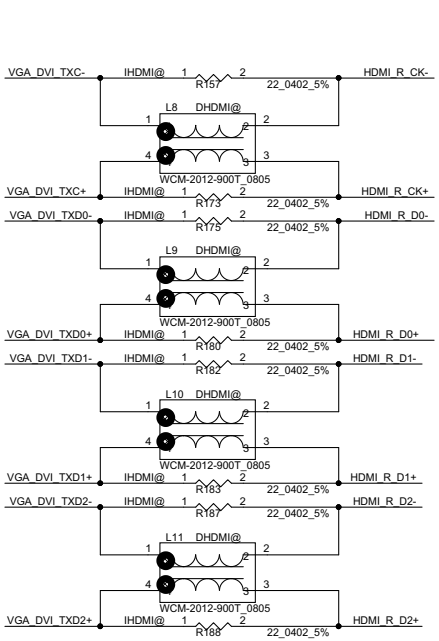
UMA

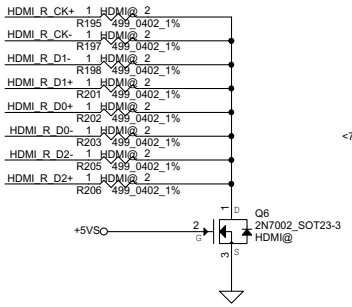
<7> UMA_HDMI_TXC+	C838	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXC+
<7> UMA_HDMI_TXC-	C837	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXC-
<7> UMA_HDMI_TX0+	C843	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD0+
<7> UMA_HDMI_TX0-	C844	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD0-
<7> UMA_HDMI_TX1+	C841	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD1+
<7> UMA_HDMI_TX1-	C842	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD1-
<7> UMA_HDMI_TX2+	C839	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD2+
<7> UMA_HDMI_TX2-	C840	1	2 0.1U_0402_16V7K	IHDMI@	VGA_DVI_TXD2-

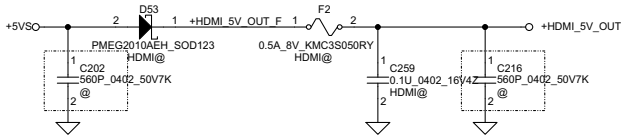


Reserve ESD for HDMI conn. on DVT



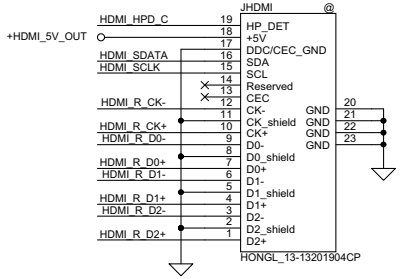


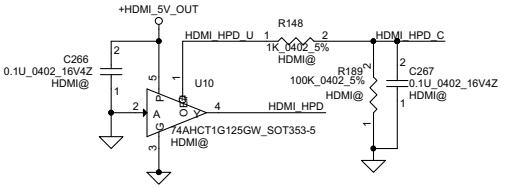


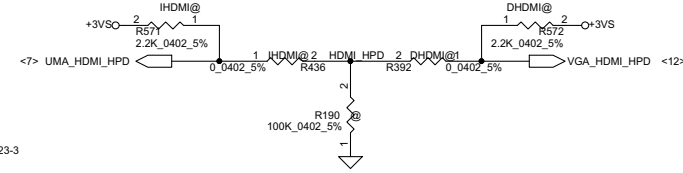


4/23 Add C202 and C216 for EMI request

HDMI Connector





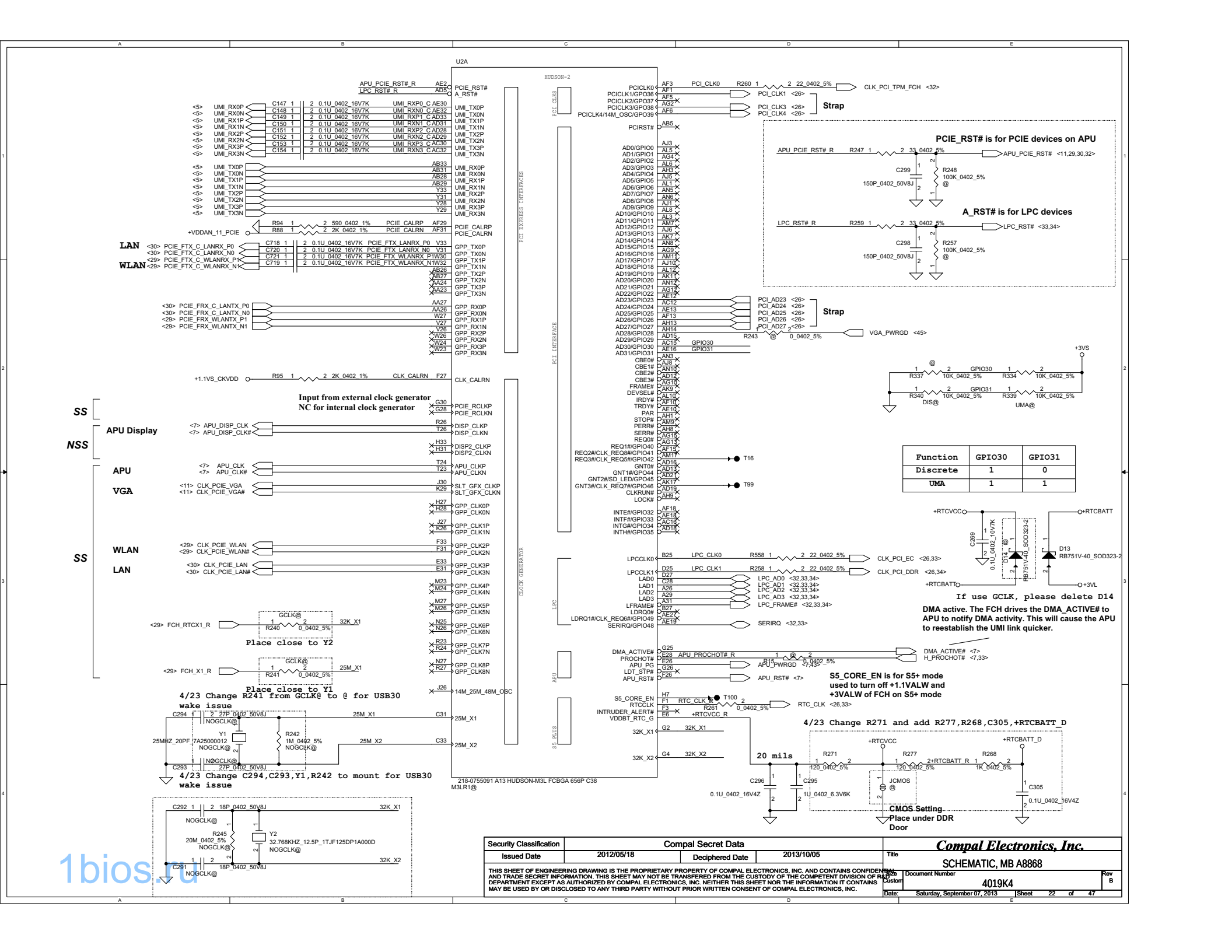


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HDMI_EN# (Internal 8.2K PU)

HDMI_EN#	H	L
SKU	Non-HDMI SKU	HDMI SKU

If an SPI ROM is shared between FCH and the Embedded Controller, a 10-k pull-up resistor to +3.3V_S5 is installed

Check

4MB SPI ROM & Non-shared ROM

SA00003K800

Socket: SP07000F500/SP07000H900

FCH-M3L NC pin

To avoid LED flashing

5/17 Add R113 and reserve C161 for EMI request

FCH-M3L NC pin

12/22 Follow the last Compal CRB board

Need to enable internal pull down to leave unconnected

218-0755091 A13 HUDSON-M3L FCBGA 656P C38

M3LR1@

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PCIE_RST2 : Reset PCIE device on Hudson 3

U2D

HUDSON-2

PCIE_RST2#/PCI_PME#/GEVENT4#
RI#/GEVENT22#
SPI_CS3#/GBE_STAT1/GEVENT21#
SLP_S3#
SLP_S5#
PWR_BTN#
PWR_GOOD
TEST0
TEST1/TMS
TEST2
GA20IN/GEVENT0#
KBRST#/GEVENT1#
LPC_PME#/GEVENT3#
LPC_SMI#/GEVENT23#
LPC_PD#/GEVENT5#
SYS_RESET#/GEVENT19#
WAKE#/GEVENT8#
IR_RX1/GEVENT20#
THRMTRIP#/SMBALERT#/GEVENT2#
WD_PWRGD
RSMRST#
CLK_REQ4#/SATA_IS0#/GPIO64
CLK_REQ3#/SATA_IS1#/GPIO63
SMARTVOLT1/SATA_IS2#/GPIO50
CLK_REQ0#/SATA_IS3#/GPIO60
SATA_IS4#/FANOUT3/GPIO55
SATA_IS5#/FANIN3/GPIO59
SPKR/GPIO66
SCL0/GPIO43
SDA0/GPIO47
SCL1/GPIO227
SDA1/GPIO228
CLK_REQ2#/FANIN4/GPIO62
CLK_REQ1#/FANOUT4/GPIO61
IR_LED#/LLB#/GPIO184
SMARTVOLT2/SHUTDOWN#/GPIO51
DDR3_RST#/GEVENT7#/VGA_PD
GBE_LED0/GPIO183
SPI_HOLD#/GBE_LED1/GEVENT9#
GBE_LED2/GEVENT10#
GBE_STAT0/GEVENT11#
CLK_REQG#/GPIO65/OSCIN/IDLEEXIT#
BLINK/USB_OC7#/GEVENT18#
USB_OC6#/IR_TX1/GEVENT6#
USB_OC5#/IR_TX0/GEVENT17#
USB_OC4#/IR_RX0/GEVENT16#
USB_OC3#/AC_PRES/TDO/GEVENT15#
USB_OC2#/TCK/GEVENT14#
USB_OC1#/TDI/GEVENT13#
USB_OC0#/SPI_TPM_CS#/TRST#/GEVENT12#
AZ_BITCLK
AZ_SDOUT
AZ_SDIN0/GPIO167
AZ_SDIN1/GPIO168
AZ_SDIN2/GPIO169
AZ_SDIN3/GPIO170
AZ_SYNC
AZ_RST#
PS2_DAT/SDA4/GPIO187
PS2_CLK/CEC/SCL4/GPIO188
SPI_CS2#/GBE_STAT2/GPIO166
PS2KB_DAT/GPIO189
PS2KB_CLK/GPIO190
PS2M_DAT/GPIO191
PS2M_CLK/GPIO192
KSO_0/GPIO209
KSO_1/GPIO210
KSO_2/GPIO211
KSO_3/GPIO212
KSO_4/GPIO213
KSO_5/GPIO214
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KSO_7/GPIO216
KSO_8/GPIO217
KSO_9/GPIO218
KSO_10/GPIO219
KSO_11/GPIO220
KSO_12/GPIO221
KSO_13/GPIO222
KSO_14/GPIO223
KSO_15/GPIO224
KSO_16/GPIO225
KSO_17/GPIO226

ACPI / WAKE UP EVENTS | GPIO | USB OC | HD AUDIO | EMBEDDED CTRL

USB MISC | USB 1.1 | USB 2.0 | USB 3.0

USBCLK/14M_25M_48M_OSC
USB_RCOMP
USB_FSD1P/GPIO186
USB_FSD1N
USB_FSD0P/GPIO185
USB_FSD0N
USB_HSD13P
USB_HSD13N
USB_HSD12P
USB_HSD12N
USB_HSD11P
USB_HSD11N
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USB_SS_RX0N
SCL2/GPIO193
SDA2/GPIO194
SCL3_LV/GPIO195
SDA3_LV/GPIO196
EC_PWM0/EC_TIMER0/GPIO197
EC_PWM1/EC_TIMER1/GPIO198
EC_PWM2/EC_TIMER2/WOL_EN/GPIO199
EC_PWM3/EC_TIMER3/GPIO200
KSI_0/GPIO201
KSI_1/GPIO202
KSI_2/GPIO203
KSI_3/GPIO204
KSI_4/GPIO205
KSI_5/GPIO206
KSI_6/GPIO207
KSI_7/GPIO208

FCH-M3L NC pin

Root

USB 3.0-Left2
USB 3.0-Left1
WLAN (BT)
Cardreader
Int. Camera

LP2
LP1

strap pin

218-0755091 A13 HUDSON-M3L FCBGA 656P C38

M3LR1@

For FCH internal debug use (Internal 10K pull-down)

+3VALW_FCH
R179 1 @ 2 2.2K 0402 5% TEST0
R181 1 @ 2 2.2K 0402 5% TEST1
RF1831 @ 2 2.2K 0402 5% TEST2

SM Bus 0-->S0 PWR domain==> SO-DIMM, WLAN
SM Bus 1-->S5 PWR domain==> no use

R282 100K 0402 5% EC_RSMRST#
R324 10K 0402 5% HDA_BITCLK
R325 10K 0402 5% AZ_SDIN0_HD
R326 10K 0402 5% AZ_SDIN1_HD
R327 10K 0402 5% AZ_SDIN2_HD
R329 10K 0402 5% AZ_SDIN3_HD

12/22 Reserve R326,R327,R329 but AMD have internal pull down

USB_OC1# is for left USB3.0 ports
USB_OC0# is for right USB2.0 ports

<28> USB_OC1#
<27> USB_OC0#

<31> AZ_BITCLK_HD R159 1 2 33 0402 5% HDA_BITCLK
<31> AZ_SDOUT_HD R160 1 2 33 0402 5% HDA_SDOUT
<31> AZ_SDIN0_HD
<31> AZ_SYNC_HD R161 1 2 33 0402 5% HDA_SYNC
<31> AZ_RST_HD# R162 1 2 33 0402 5% HDA_RST#

+3VALW_FCH

Internal Pull-up

R278 10K 0402 5% H_THERMTRIP#
R272 10K 0402 5% EC_LID_OUT#
R276 10K 0402 5% FCH_PCIE_WAKE#
R318 10K 0402 5% USB_OC0#
R319 10K 0402 5% USB_OC1#
R288 10K 0402 5% FCH_SCLK1
RF289 10K 0402 5% FCH_SDATA1

12/22 SMBus Not Implemented use 10k pull up to +3VALW_PCH

+3VS

RF287 2.2K 0402 5% FCH_SCLK0
RF291 2.2K 0402 5% FCH_SDATA0
R292 8.2K 0402 5% CLKREQ_WLAN#
R290 8.2K 0402 5% CLKREQ_LAN#
R320 10K 0402 5% WD_PWRGD

Place R425 and C363 close to FCH for ESD

+3VALW_FCH
+3VS
+3VS
R312 10K 0402 5%
R311 10K 0402 5%
Q32A
2N7002KDWH_SOT363-6
ODD_DA# FCH R425 0 0402 5%
2ODD_DA# Q
ODD_DA# <27>
C363 0.1U 0402 16V4Z

Q32B in page14

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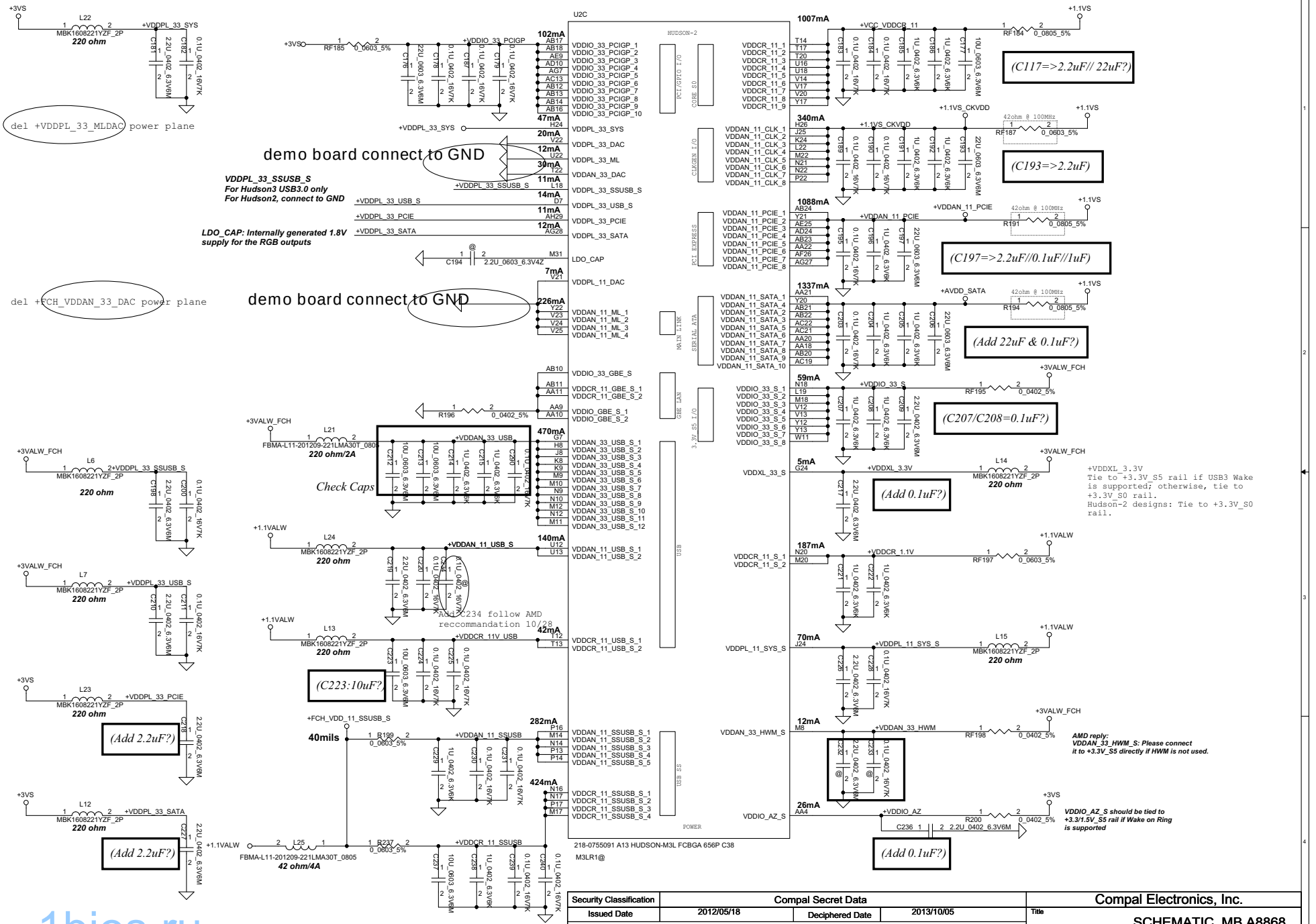
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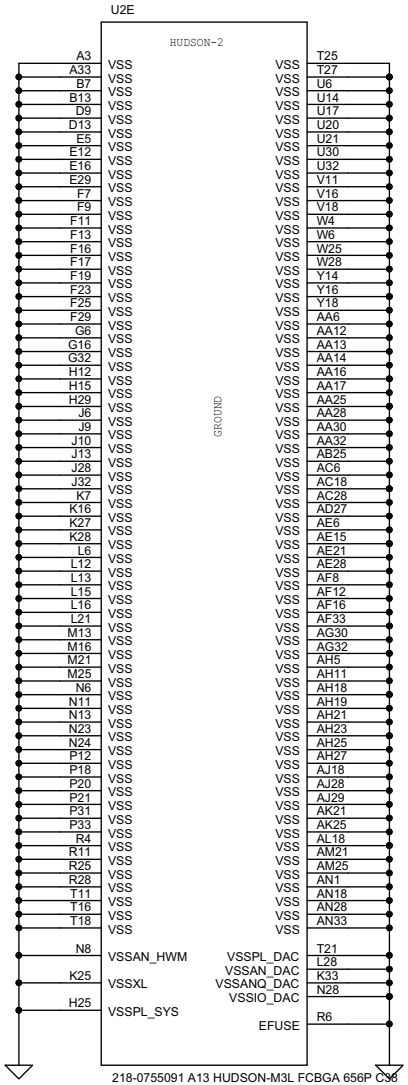
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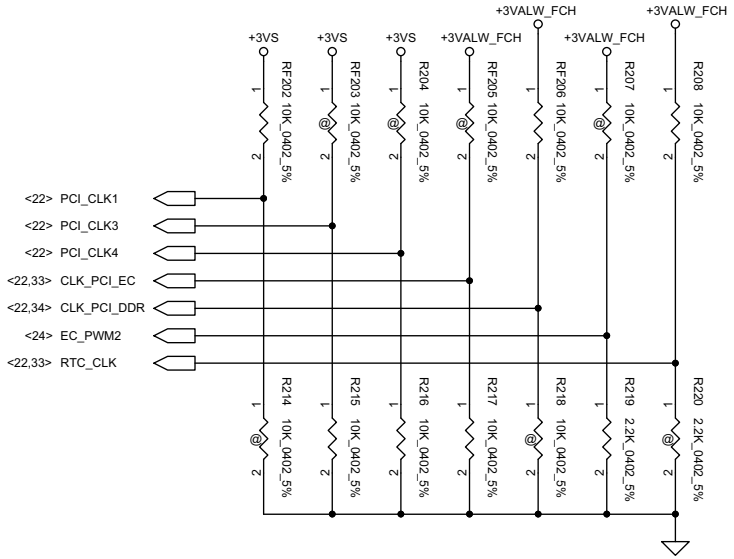
STRAP PINS

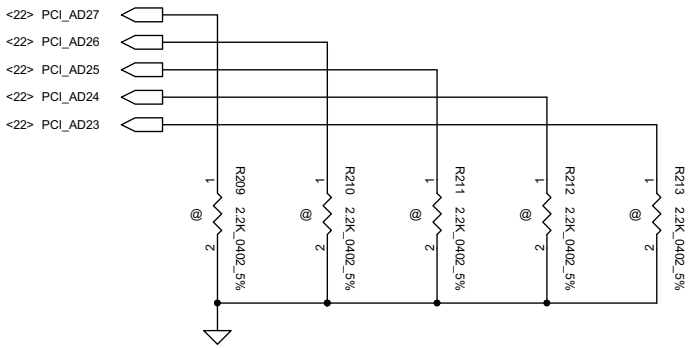
	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0_EC	LPC_CLK1	EC_PWM2	RTC_CLK
PULL HIGH	ALLOW PCIE GEN2 DEFAULT	USE DEBUG STRAPS	NON_FUSION CLOCK MODE	EC ENABLED	CLKGEN ENABLED DEFAULT	LPC ROM	S5 PLUS MODE DISABLED DEFAULT
PULL LOW	FORCE PCIE GEN1	IGNORE DEBUG STRAP DEFAULT	FUSION CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLE	SPI ROM DEFAULT	S5 PLUS MODE ENABLED

DEBUG STRAPS

FCH HAS 15K INTERNAL PU FOR PCI_AD[27:23]

	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

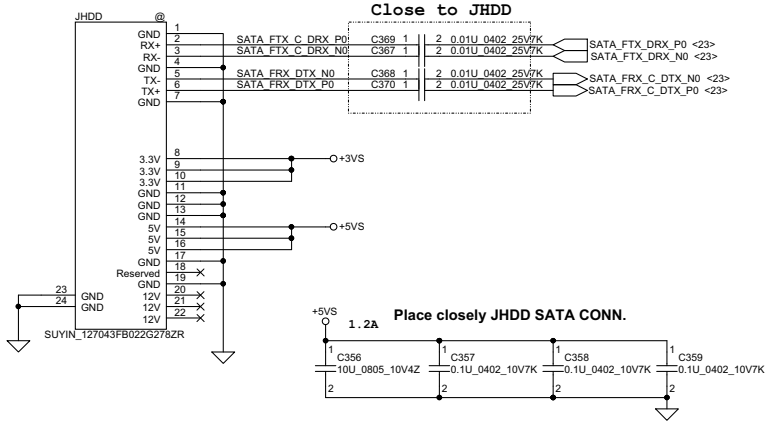




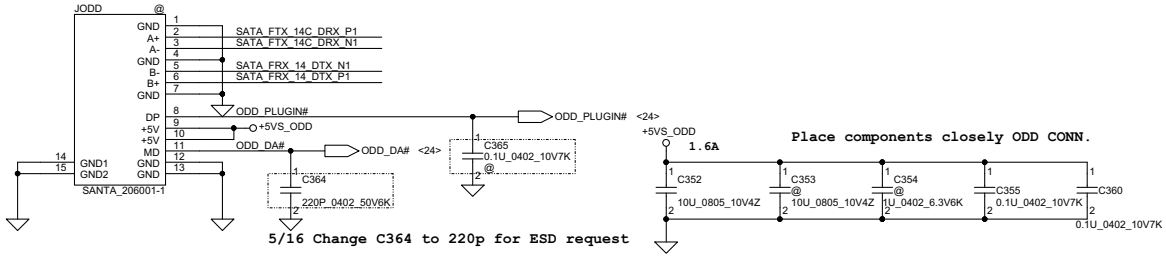
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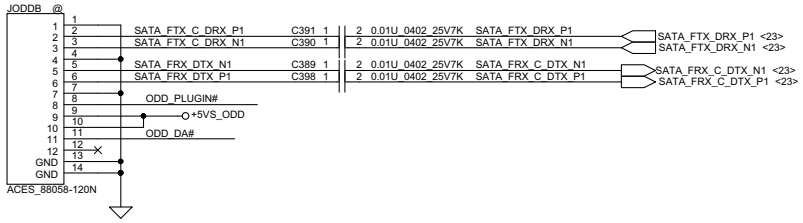
SATA HDD Conn.

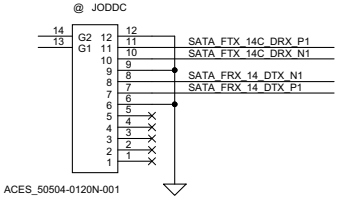


SATA ODD Conn (for 14")

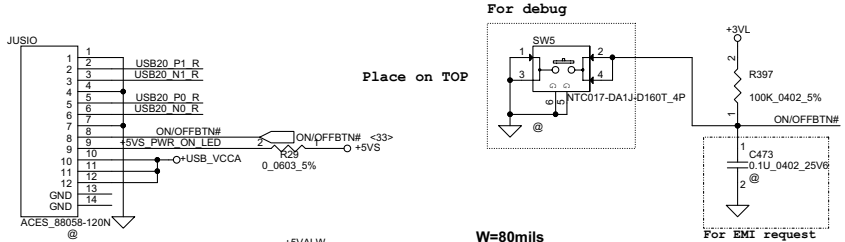


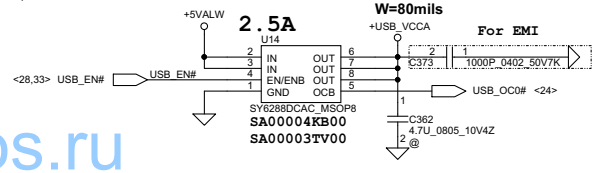
SATA ODD Conn (for 15")

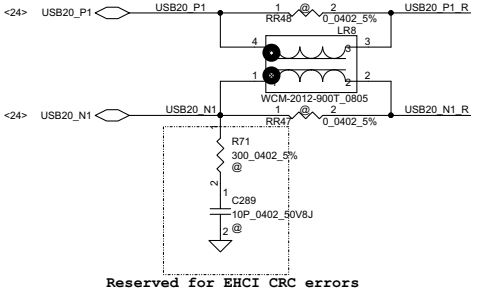


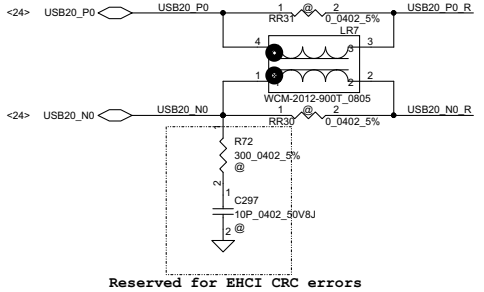


Power Button & RUSB connector







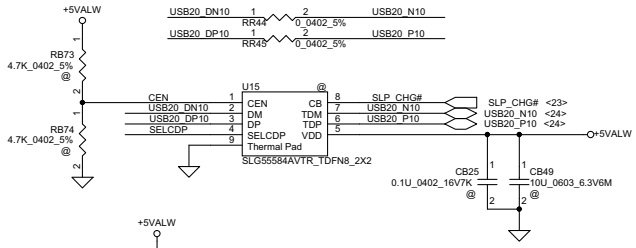


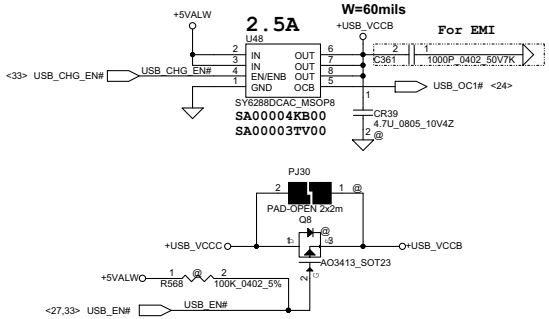
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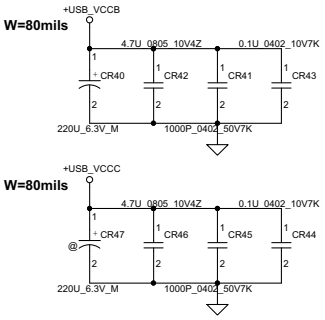
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Issued Date	2012/05/18	Deciphered Date	2013/10/05

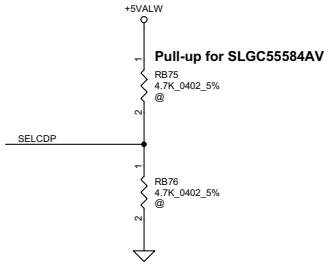
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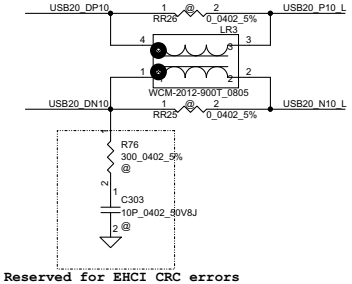


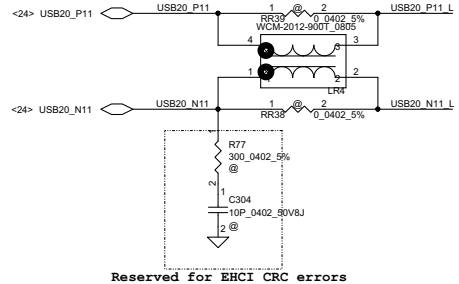


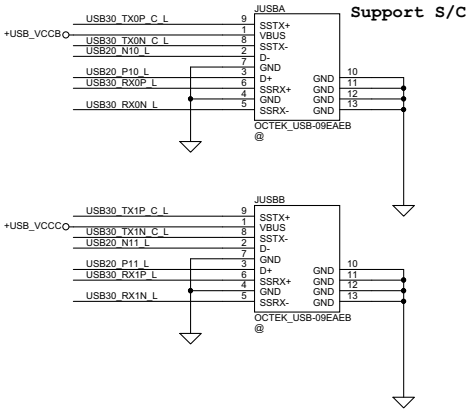


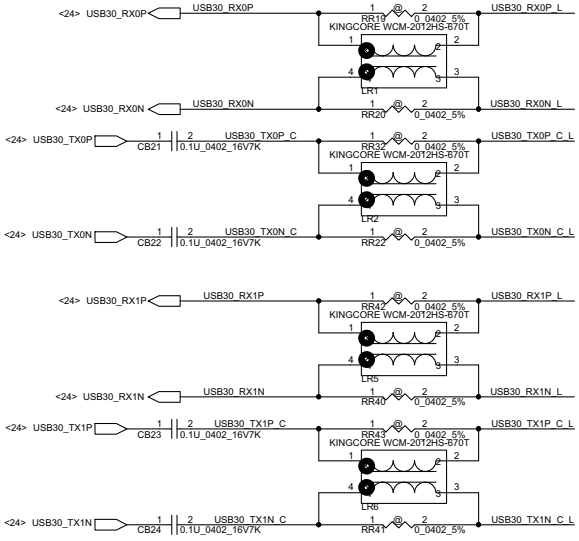


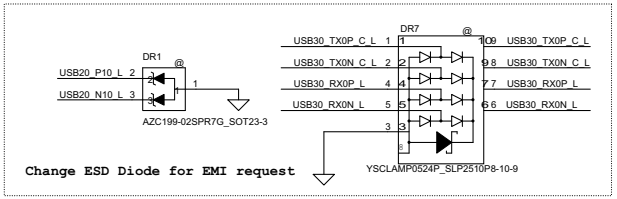
SLP_CHG#	SELCDP	Function
0	X	DCP autodetect with mouse/keyboard wakeup
1	0	S0 charging with SDP only
1	1	S0 charging with CDP or SDP only

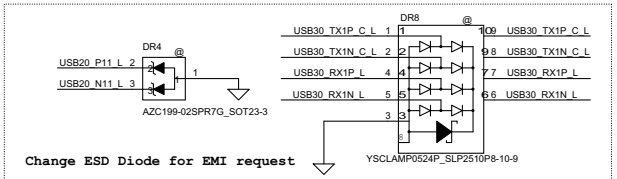












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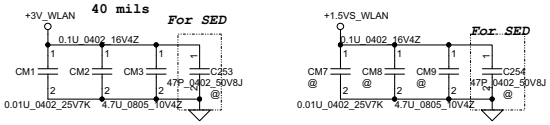
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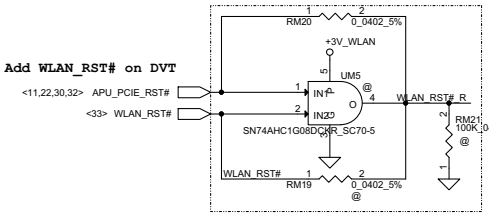
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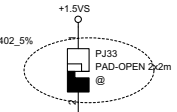
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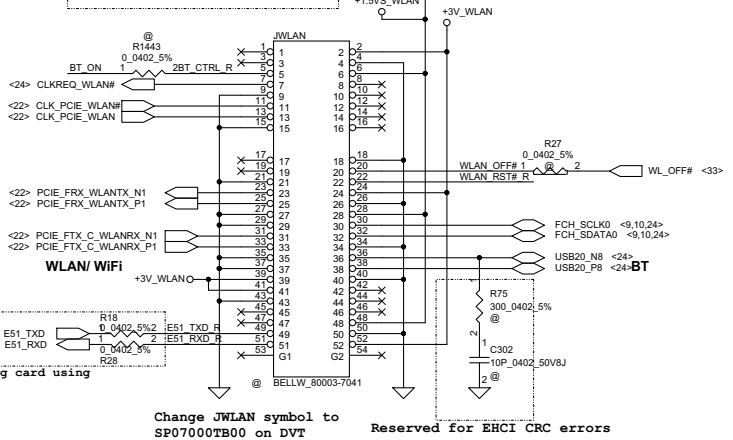
Slot 1 Half PCIe Mini Card-WLAN





4/23 Add RM20 and change UM5,RM21 to @



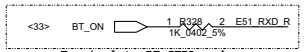


Change JWLAN symbol to SP07000TB00 on DVT

Reserved for EHCI CRC errors

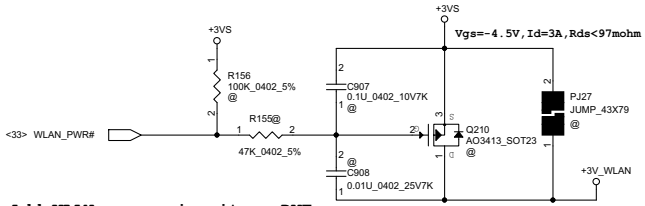
WLAN&BT Combo module circuits

	BT on module Enable	BT on module Disable
BT_ON	H	L



For isolate BT_CTRL and Compal Debug Card.

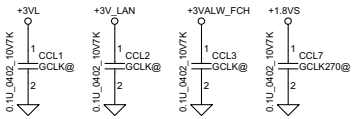
+3VALW TO +3V_WLAN

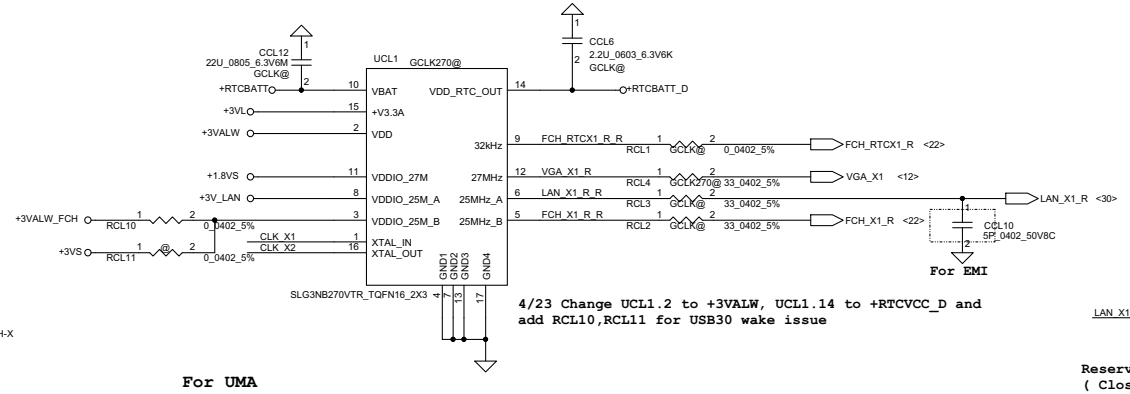


Add WLAN power circuit on DVT

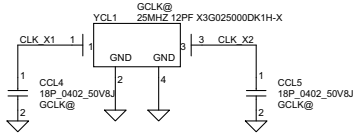
4/23 Change R156,R155,C907,C908,Q210 to @

Green Clock Generator

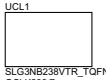


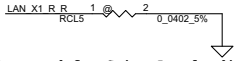


4/23 Change UCL1.2 to +3VALW, UCL1.14 to +RTCVCC_D and add RCL10,RCL11 for USB30 wake issue



For UMA





Reserved for Swing Level adjustment (Close GCLK side)

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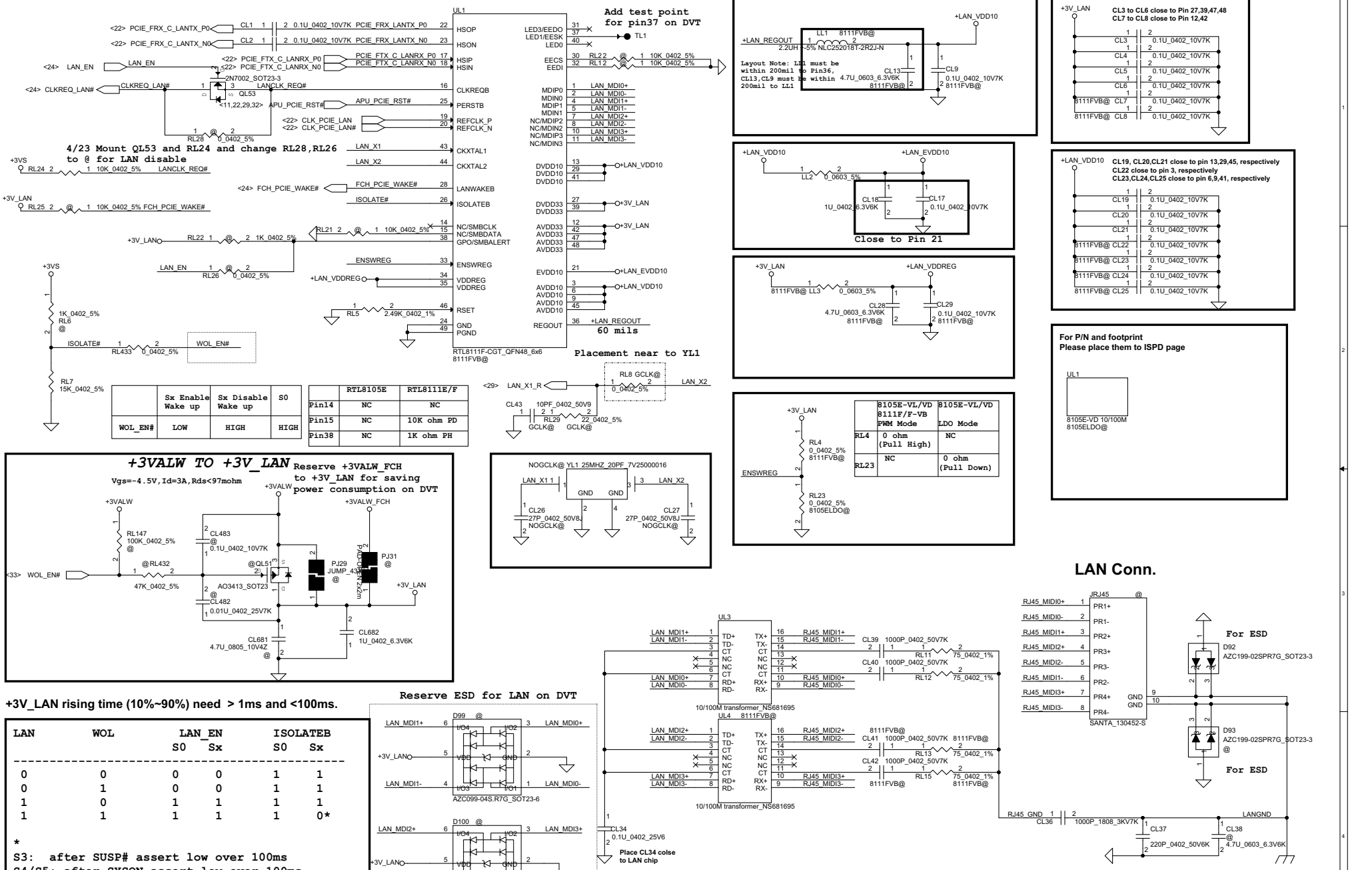
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	Sx Enable Wake up	Sx Disable Wake up	S0
WOL_EN#	LOW	HIGH	HIGH

	RTL8105E	RTL8111E/F
Pin14	NC	NC
Pin15	NC	10K ohm PD
Pin38	NC	1K ohm PH

	8105E-VL/VD 8111F/F-VB PWM Mode	8105E-VL/VD LDO Mode
RL4	0 ohm (Pull High)	NC
RL23	NC	0 ohm (Pull Down)

LAN	WOL	LAN_EN S0	LAN_EN Sx	ISOLATEB S0	ISOLATEB Sx
0	0	0	0	1	1
0	1	0	0	1	1
1	0	1	1	1	1
1	1	1	1	1	0*

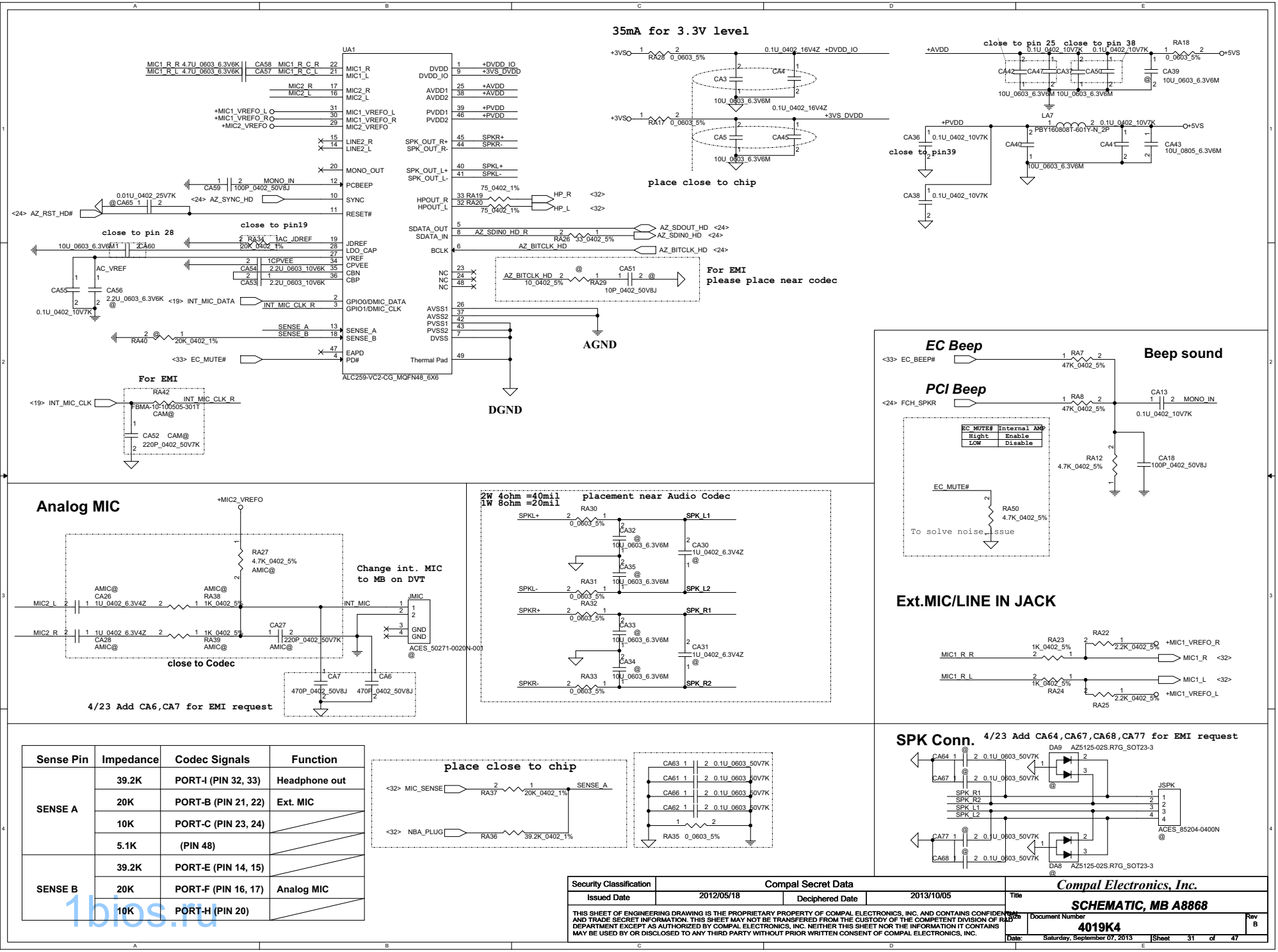
*
S3: after SUSP# assert low over 100ms
S4/S5: after SYSON assert low over 100ms

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Sense Pin	Impedance	Codec Signals	Function
SENSE A	39.2K	PORT-I (PIN 32, 33)	Headphone out
	20K	PORT-B (PIN 21, 22)	Ext. MIC
	10K	PORT-C (PIN 23, 24)	
	5.1K	(PIN 48)	
SENSE B	39.2K	PORT-E (PIN 14, 15)	
	20K	PORT-F (PIN 16, 17)	Analog MIC
	10K	PORT-H (PIN 20)	

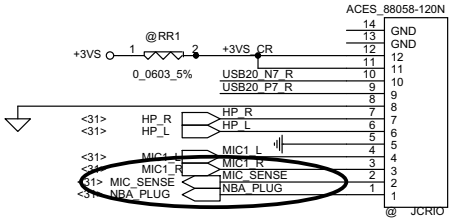
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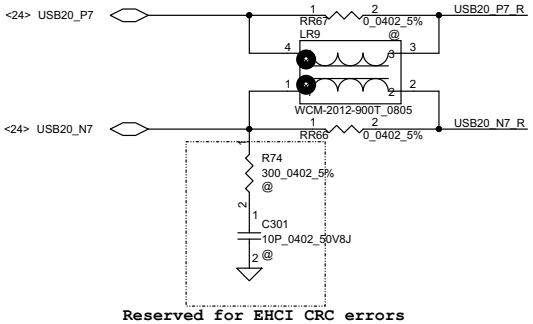
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CardReader Conn.

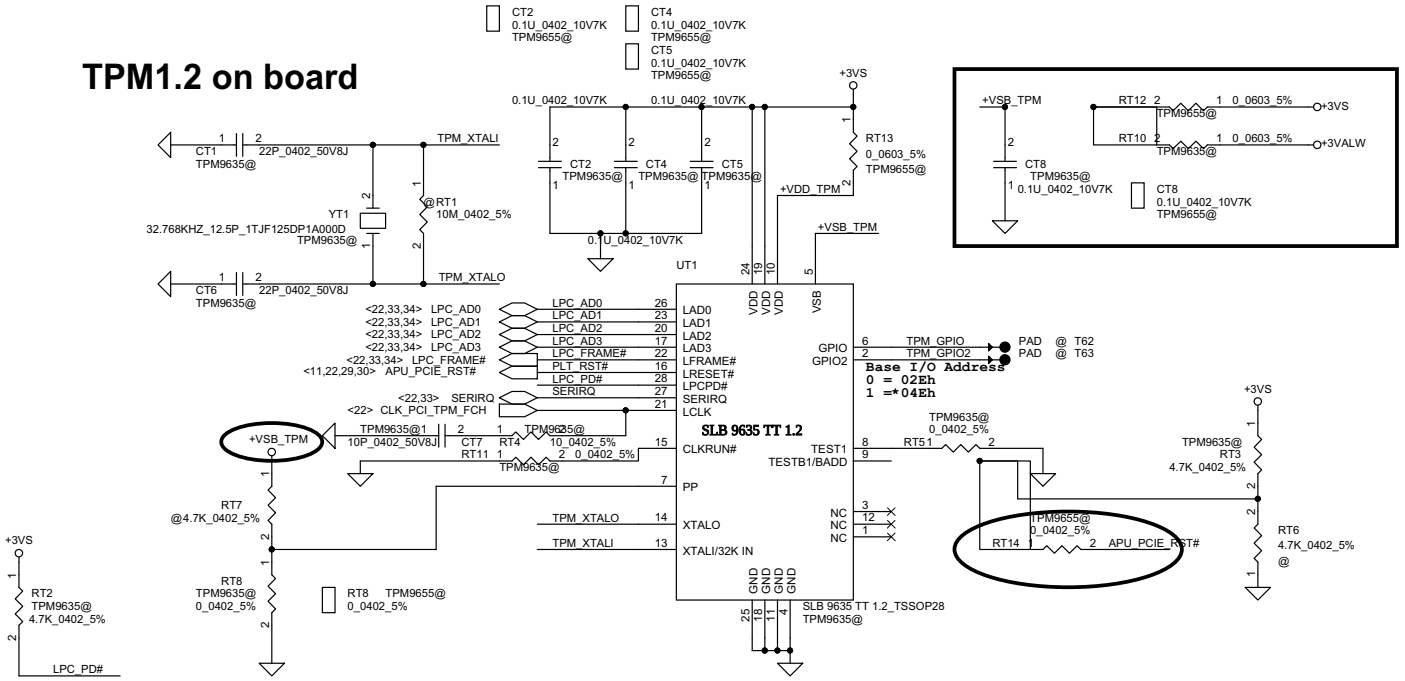


Update JCRIO pin definition on DVT



Reserved for EHCI CRC errors

TPM1.2 on board

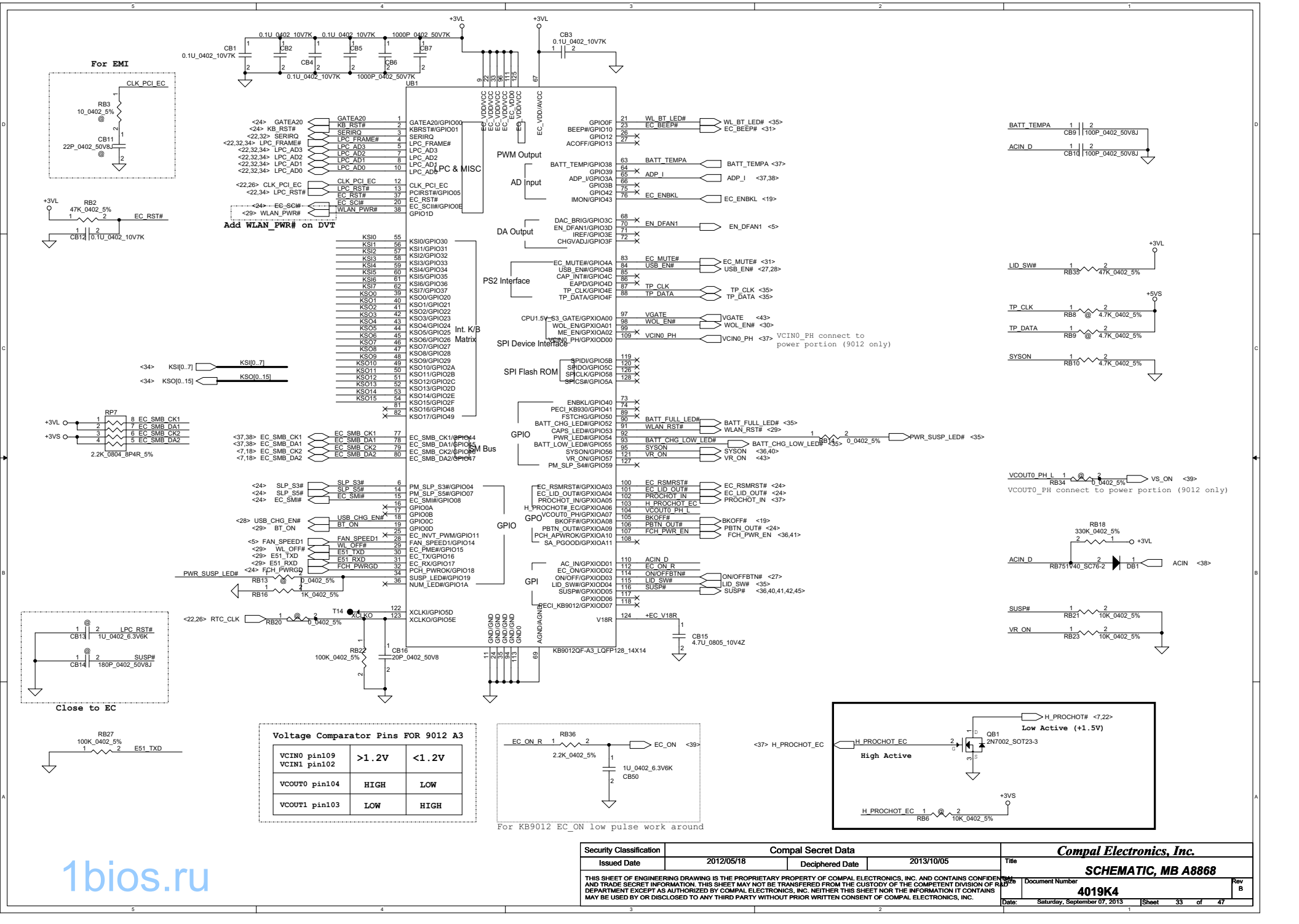


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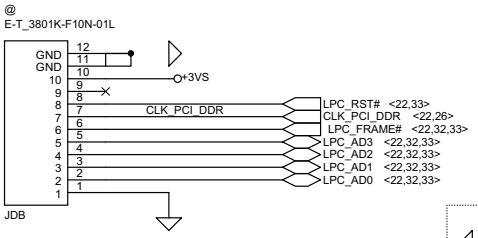
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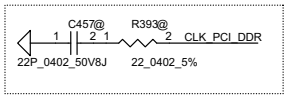
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LPC Debug Port

Place the PAD under DDR DIMM.

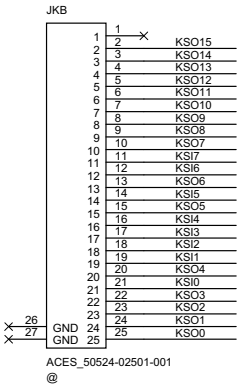




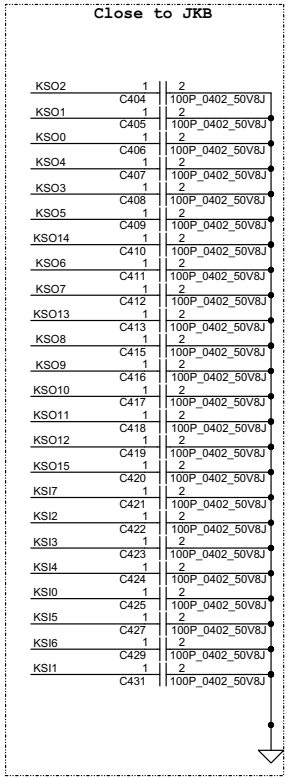
For EMI

KEYBOARD CONN.





For EMI
Close to JKB



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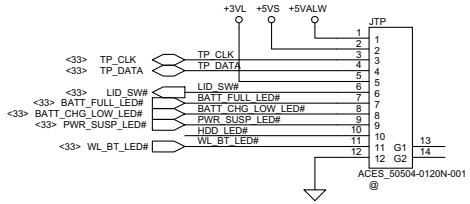
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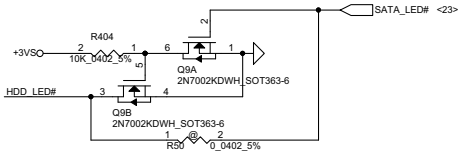
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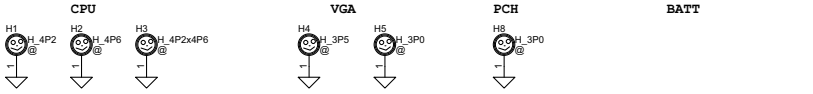
Touchpad Connector
(Reserve 7 pins for LED & Win8 TP)

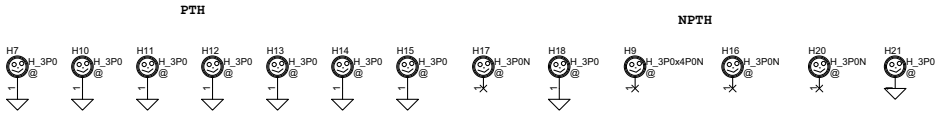


HDD LED



Screw Hole





PCB Fedical Mark PAD



ISPD

PCB ZZZ — PCB LA-8868P REV1

DC-IN PJP1 — PJP1 45@

FCH U2 — AMD A13 HUDSON-M3L R3 M3LR3@

CPU U1 — AMD E2-1800 R1_UNBW E2R1@

U1 — AMD E2-1800 R3 E2R3@

U1 — AMD E1-1200 R3 E1R3@

GPU UV1 — 216080902 4A11SEYMOU_FCBGA631 SEYMOURR3@

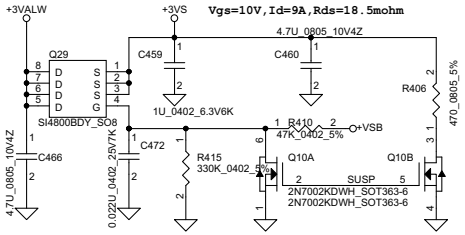
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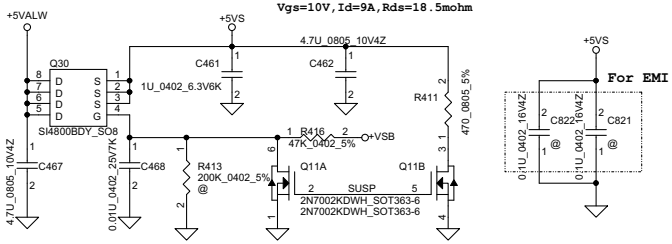
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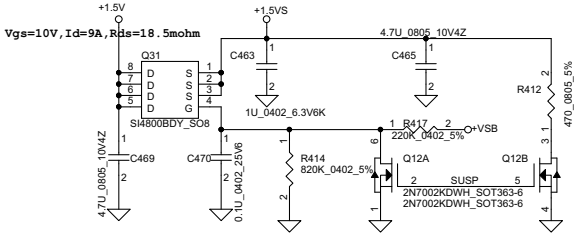
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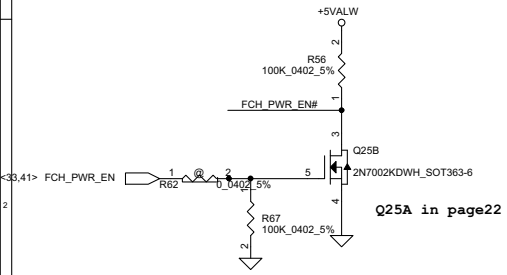


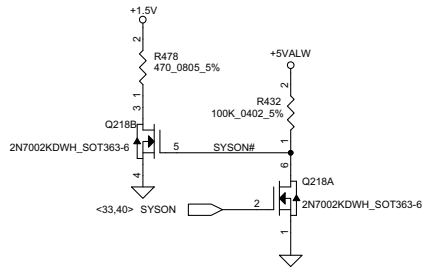
+5VALW TO +5VS

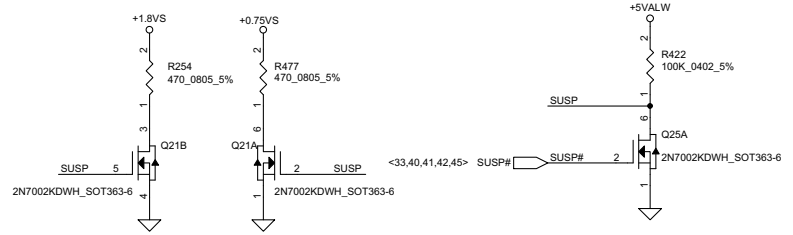


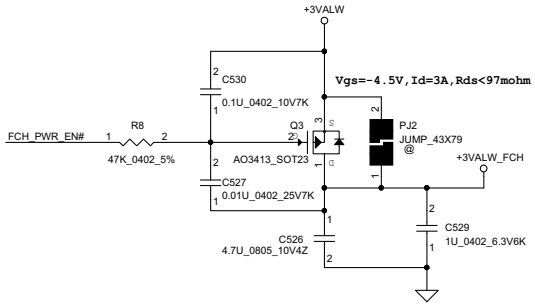
+1.5V to +1.5VS



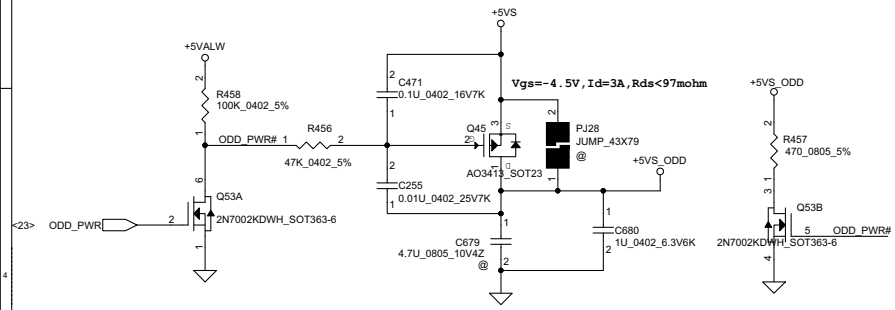




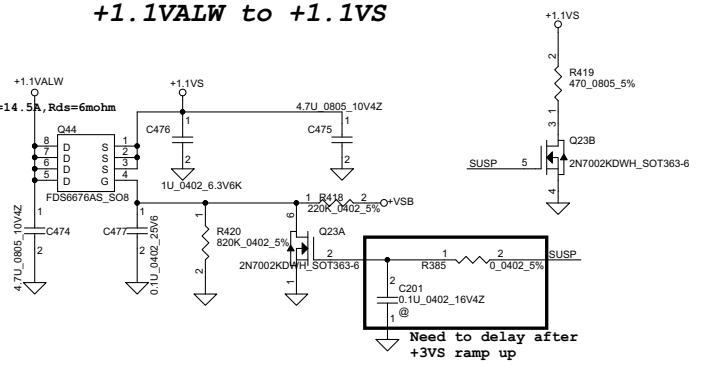




+5VS TO +5VS_ODD



+1.1VALW to +1.1VS



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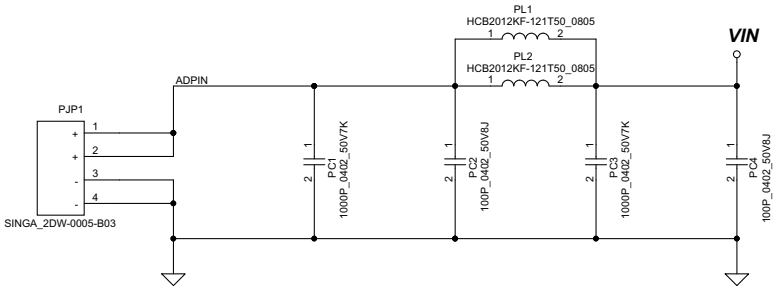
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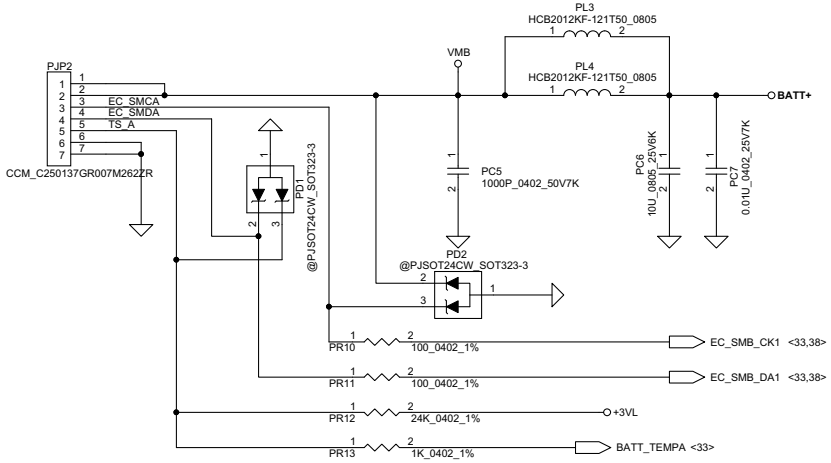
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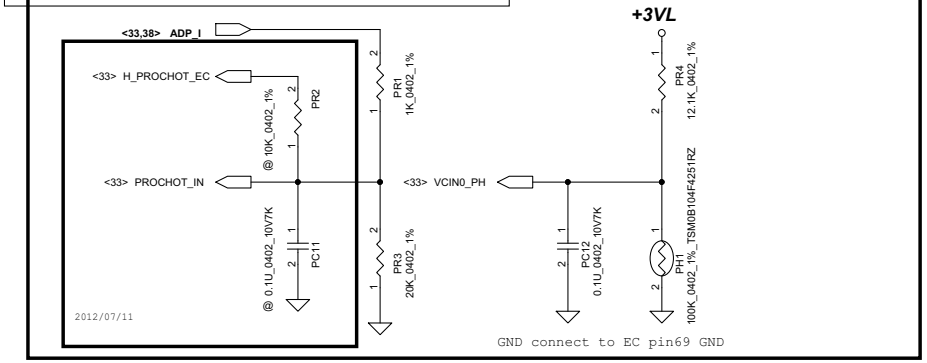
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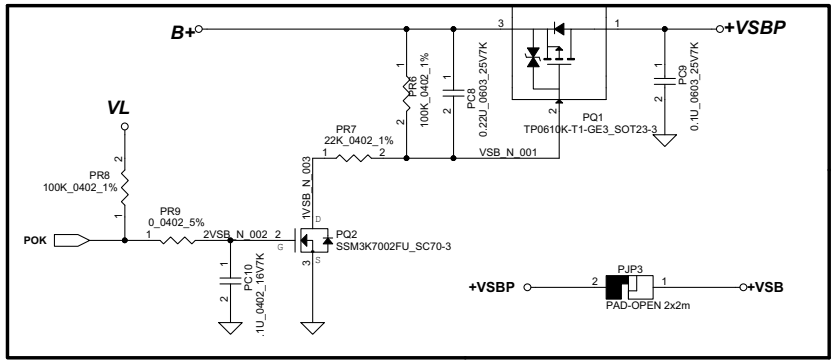




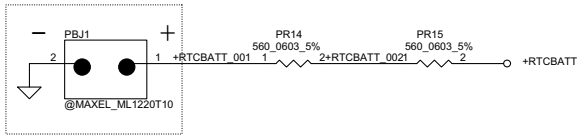
PH1 under CPU botten side :
CPU thermal protection at 93 +-3 degree C
Recovery at 56 +-3 degree C

Please locate these parts
Near EC chip





RTC Battery



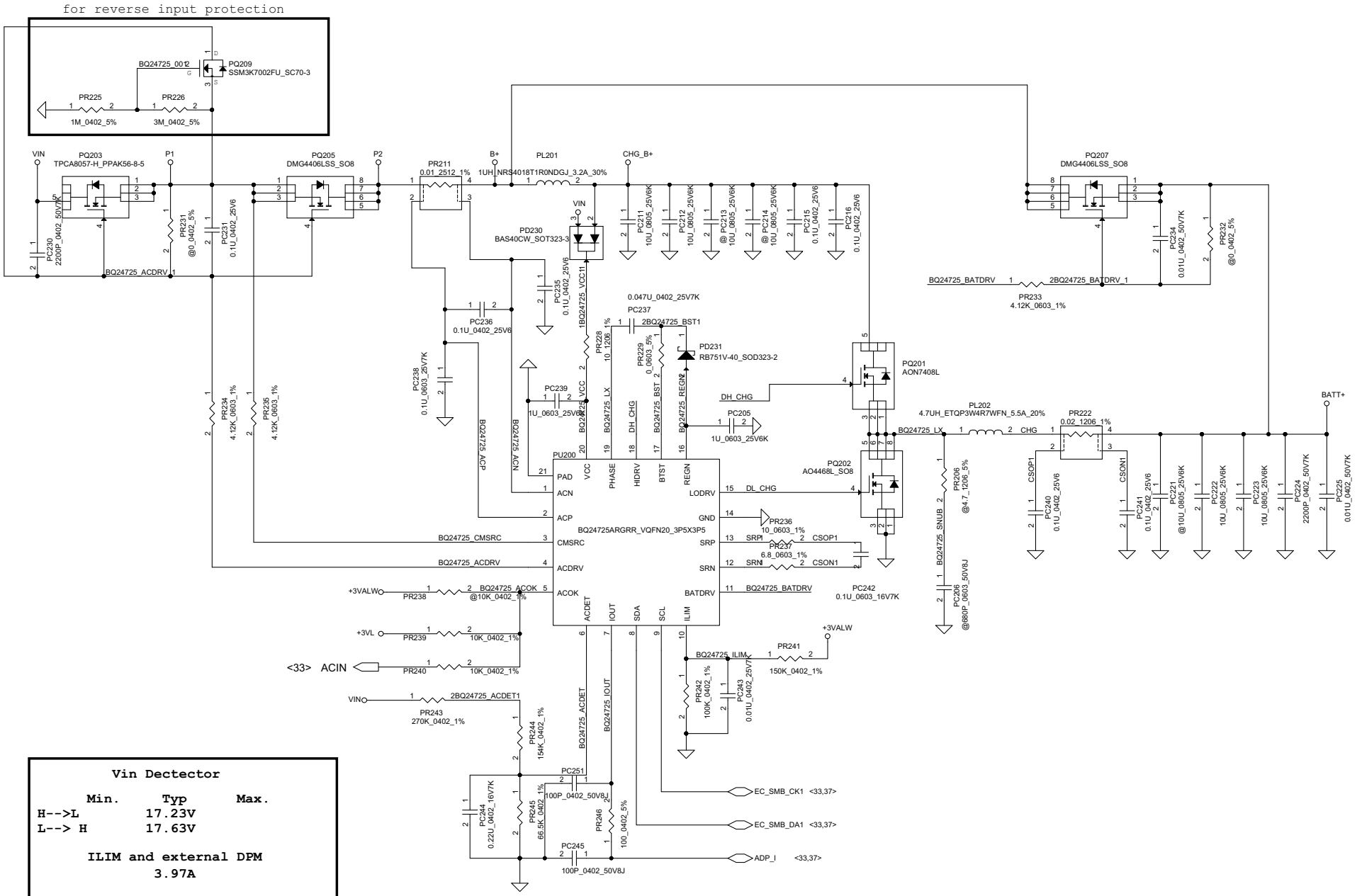
SP093MX0000

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Vin Dectector

	Min.	Typ	Max.
H-->L		17.23V	
L--> H		17.63V	

ILIM and external DPM 3.97A

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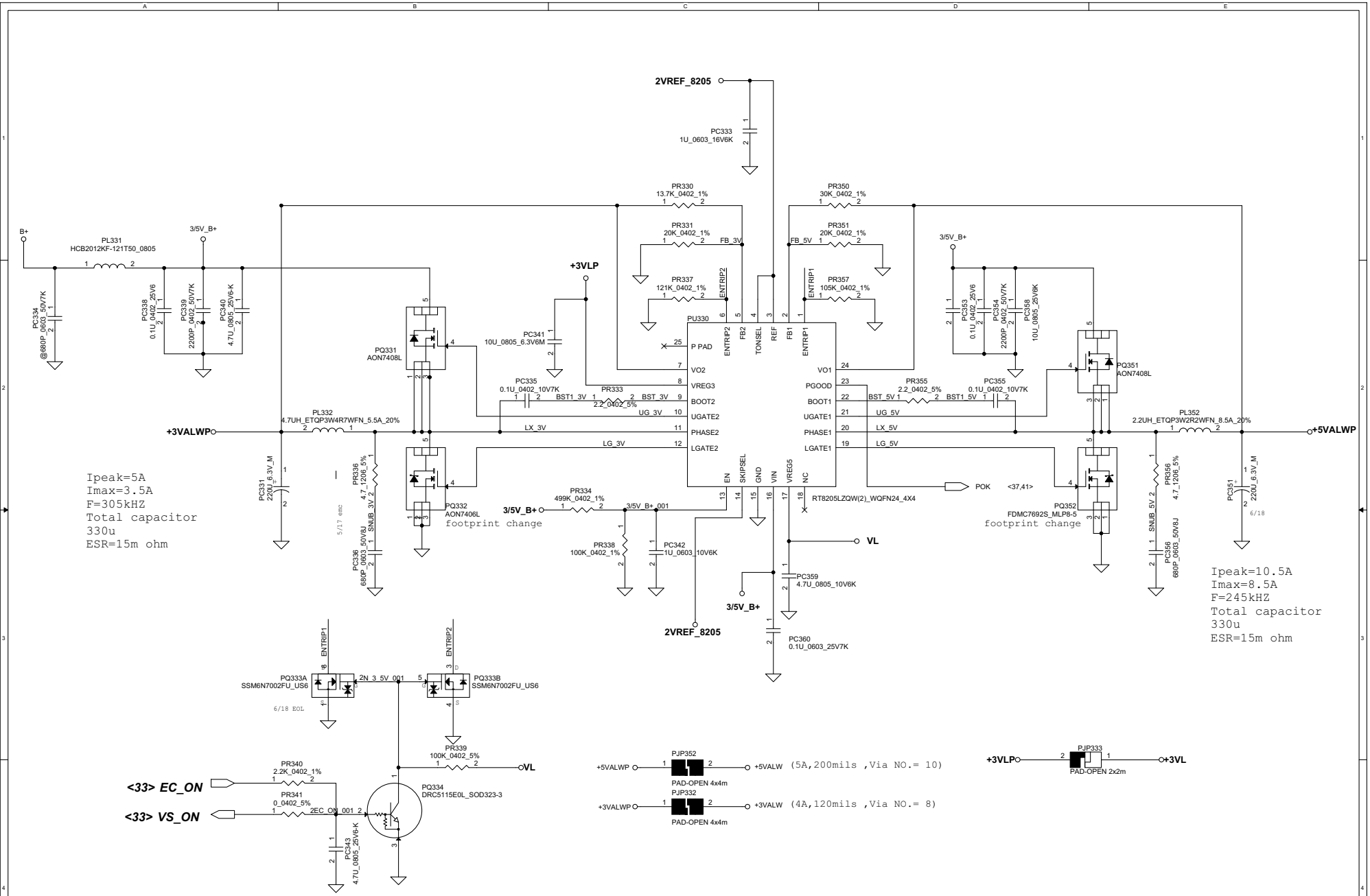
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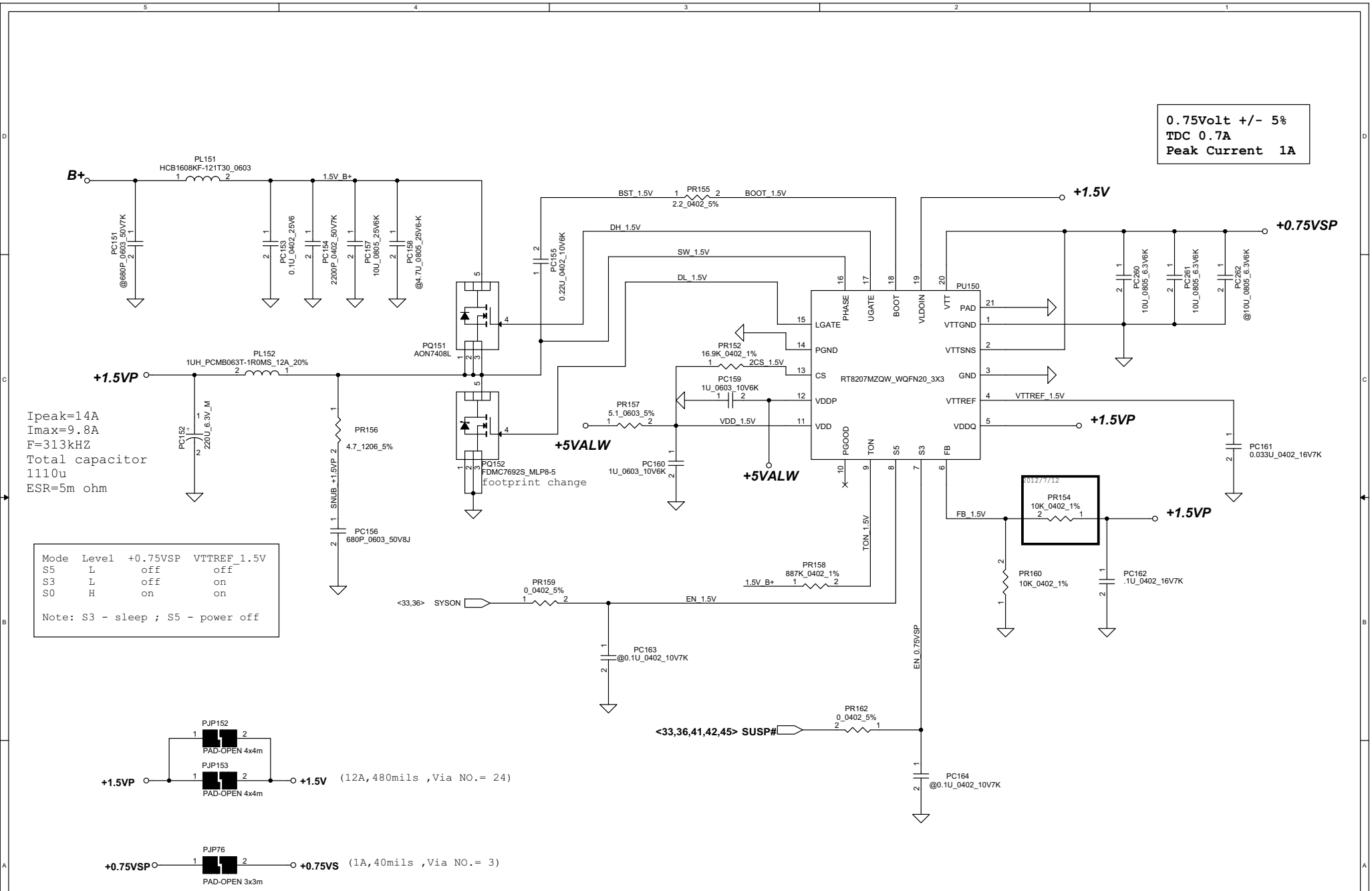
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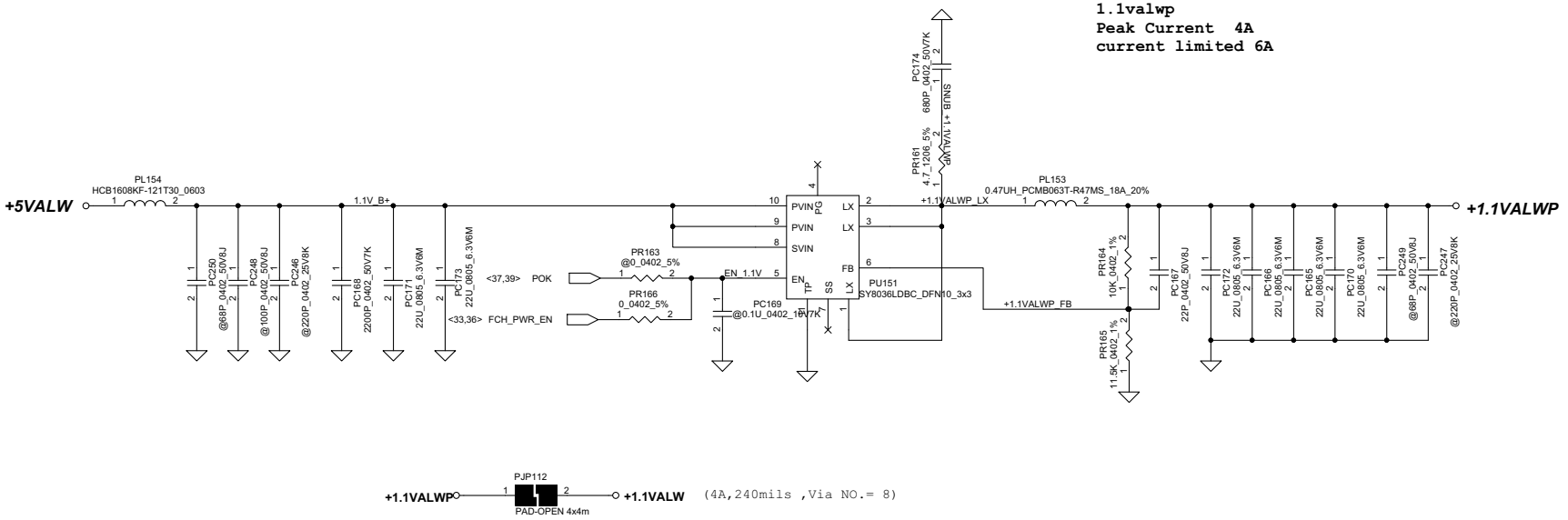
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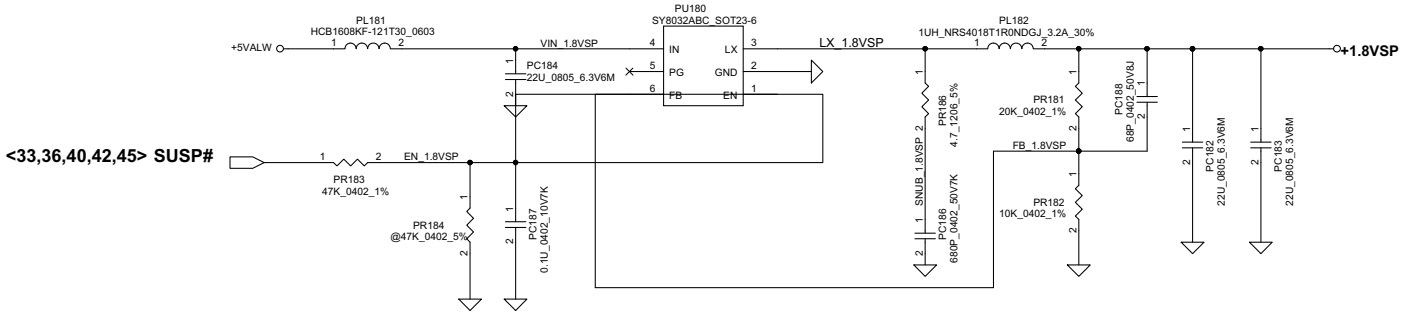
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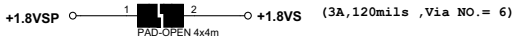
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(4A,240mils ,Via NO.= 8)





(3A,120mils ,Via NO.= 6)

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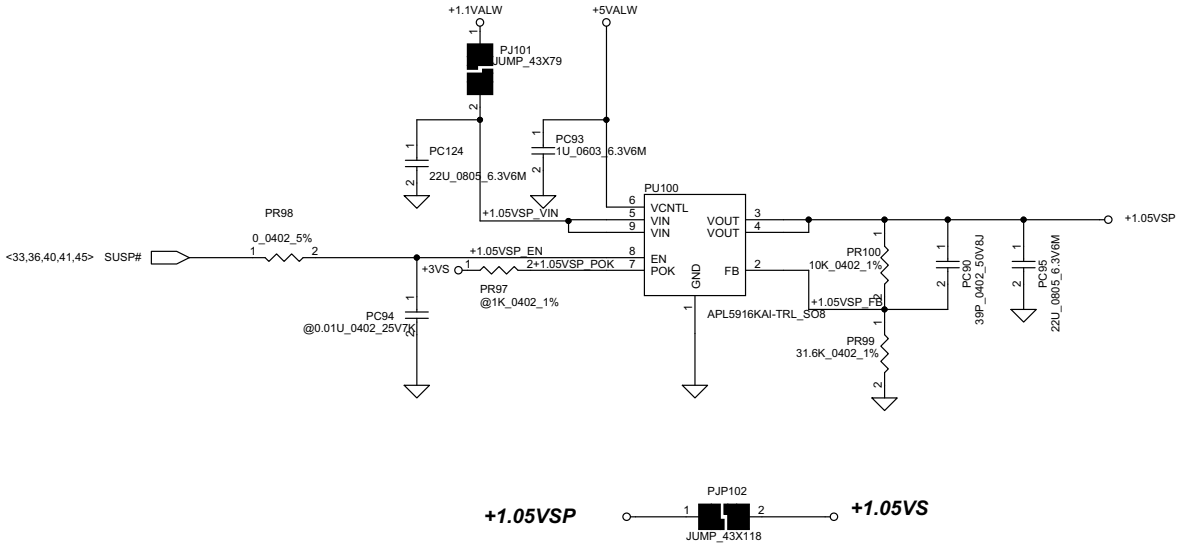
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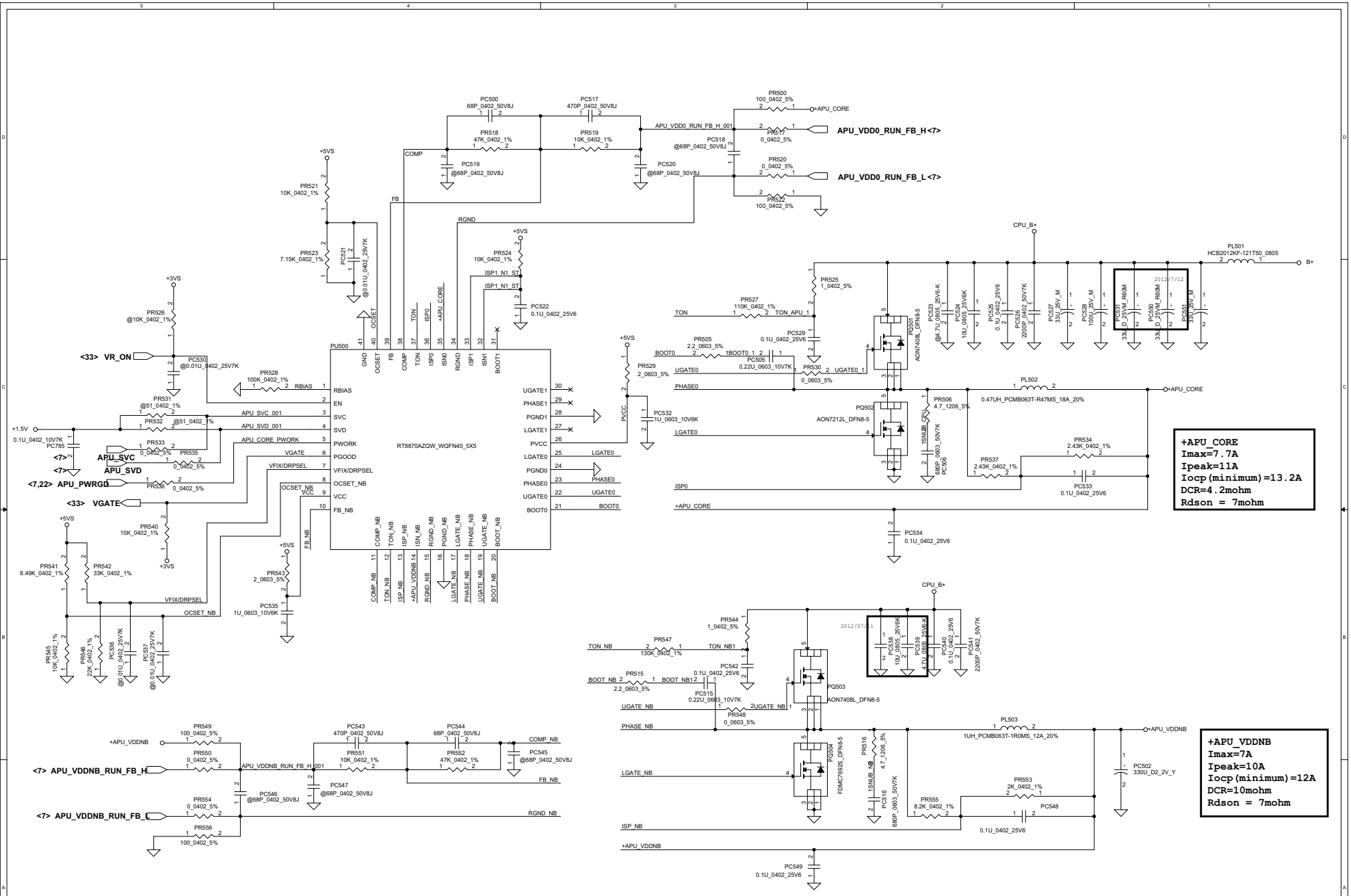
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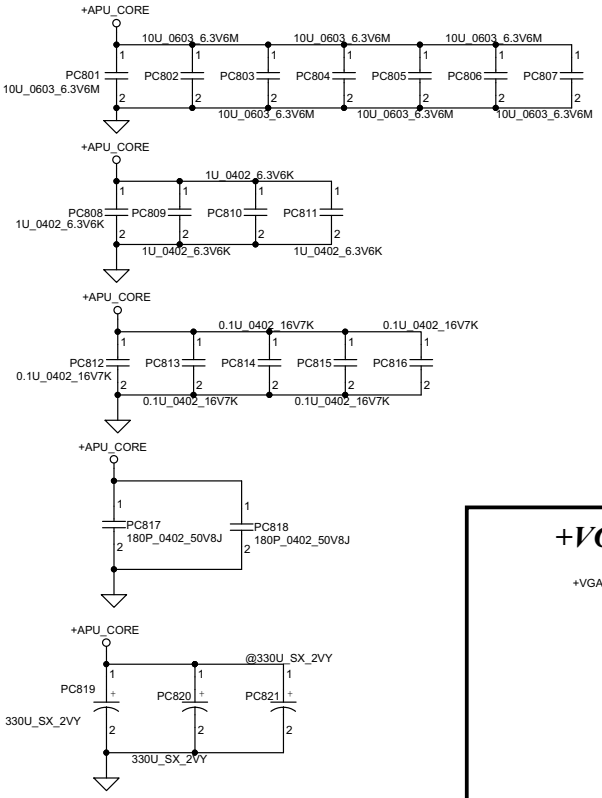
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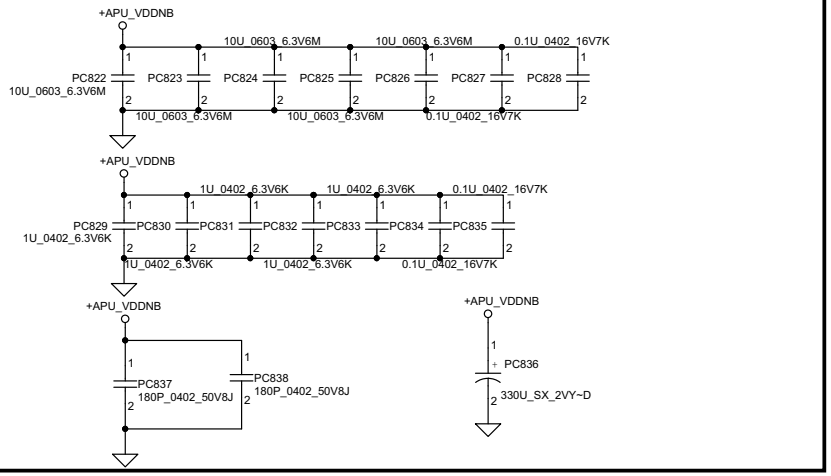
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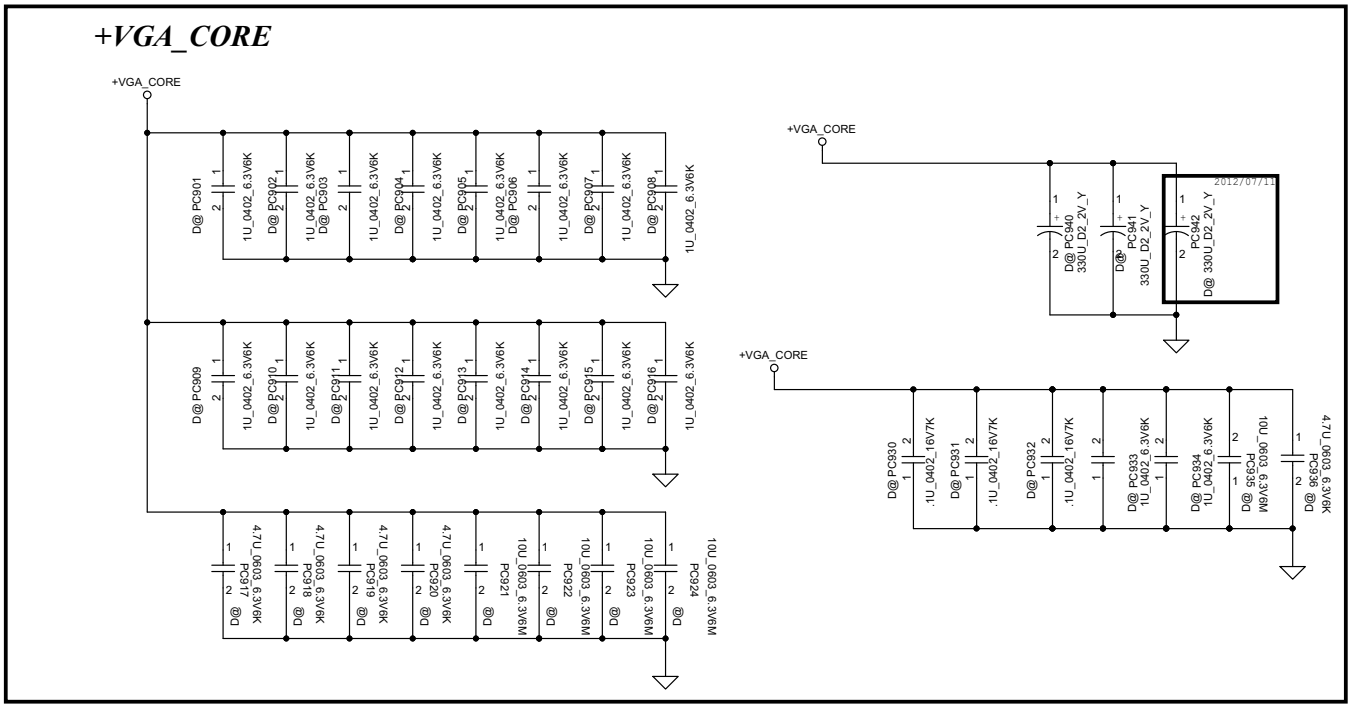
+APU_CORE



+APU_CORE_NB



+VGA_CORE

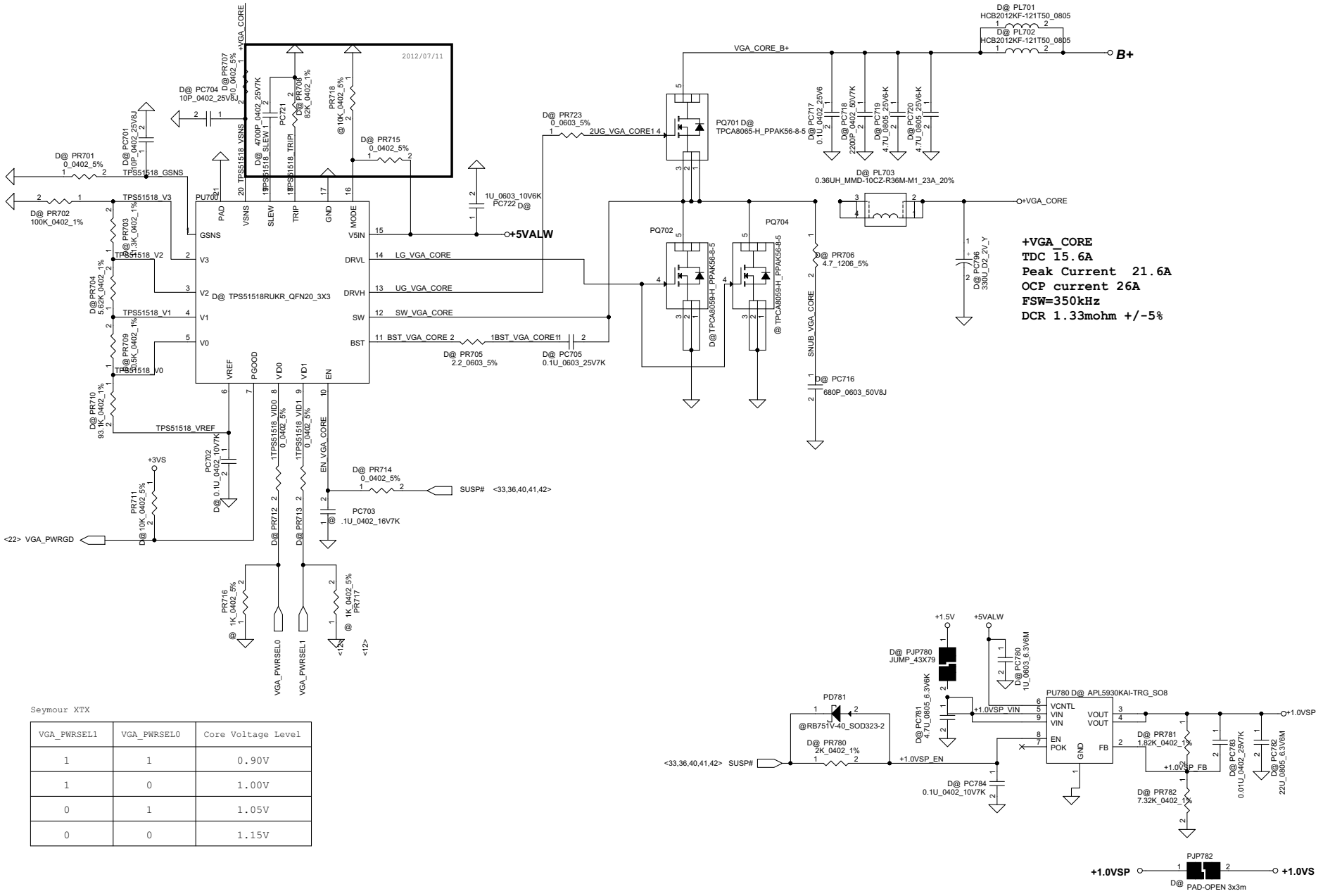


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Seymour XTX

VGA_PWRSEL1	VGA_PWRSEL0	Core Voltage Level
1	1	0.90V
1	0	1.00V
0	1	1.05V
0	0	1.15V

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