

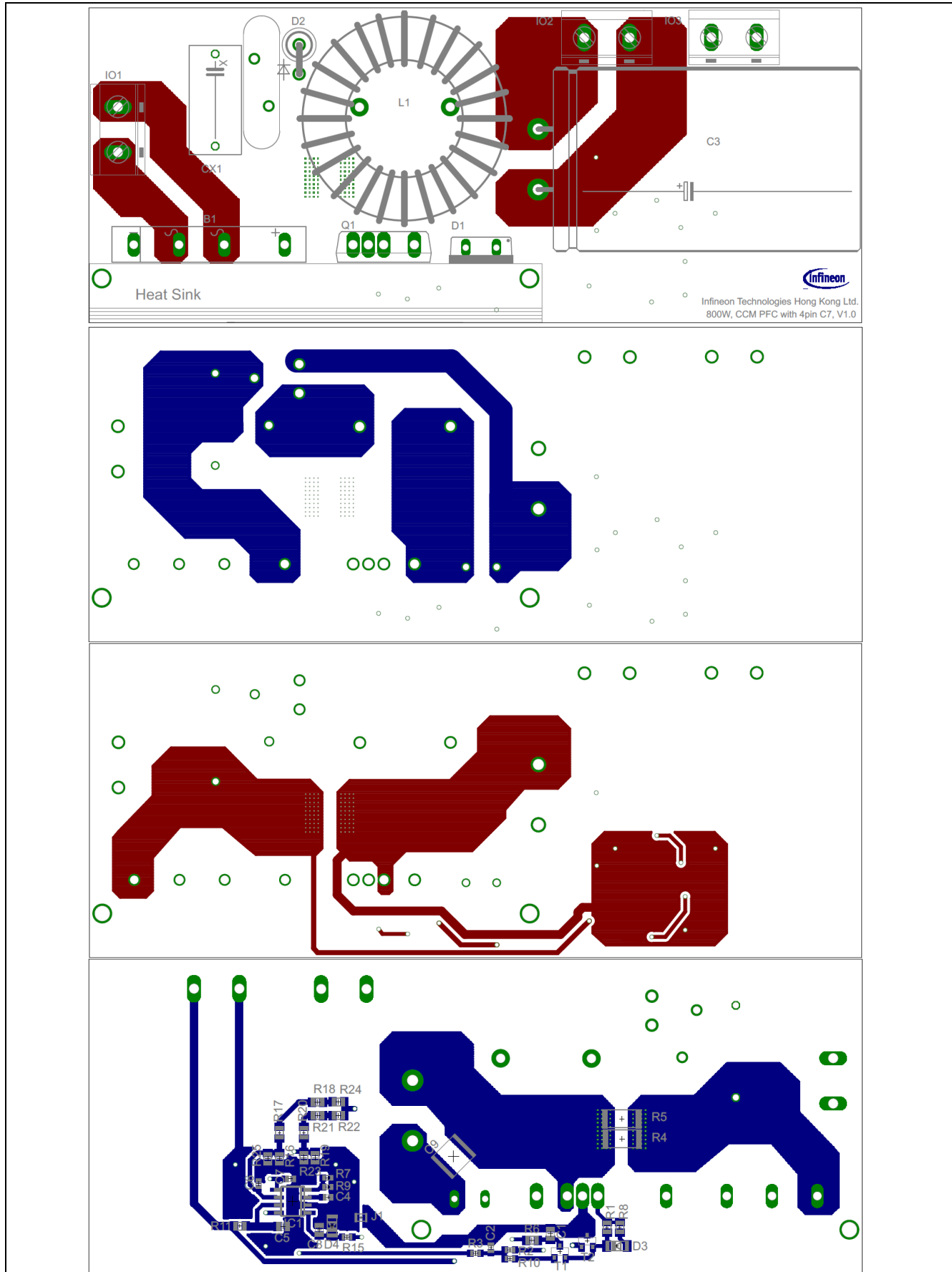


Figure 21 The 4 layers of the PCB layout for the reference design, including layer 1 (top side), layer 2(interlayer), layer 3(interlayer) and layer 4 (mirror of bottom side)