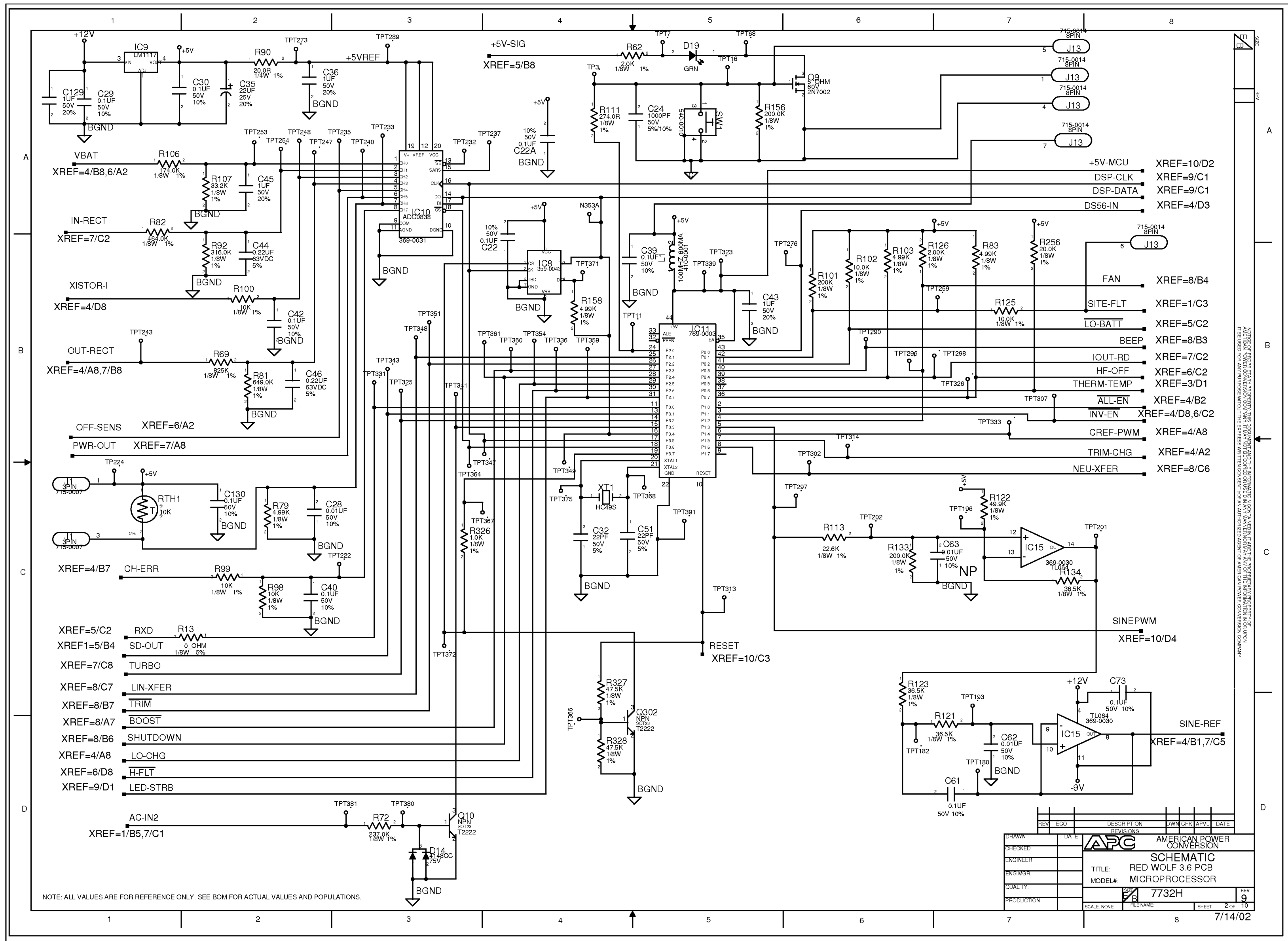
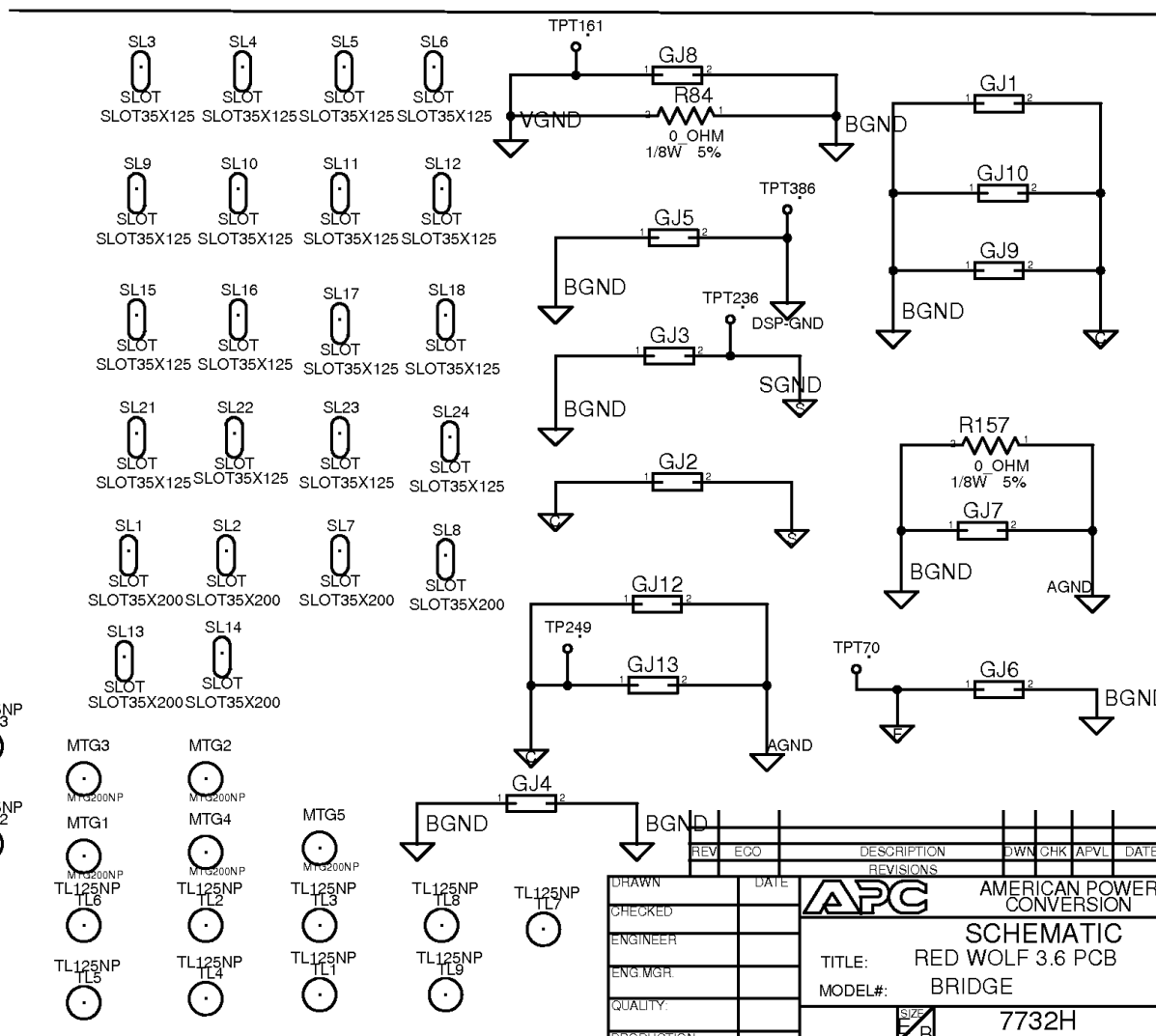
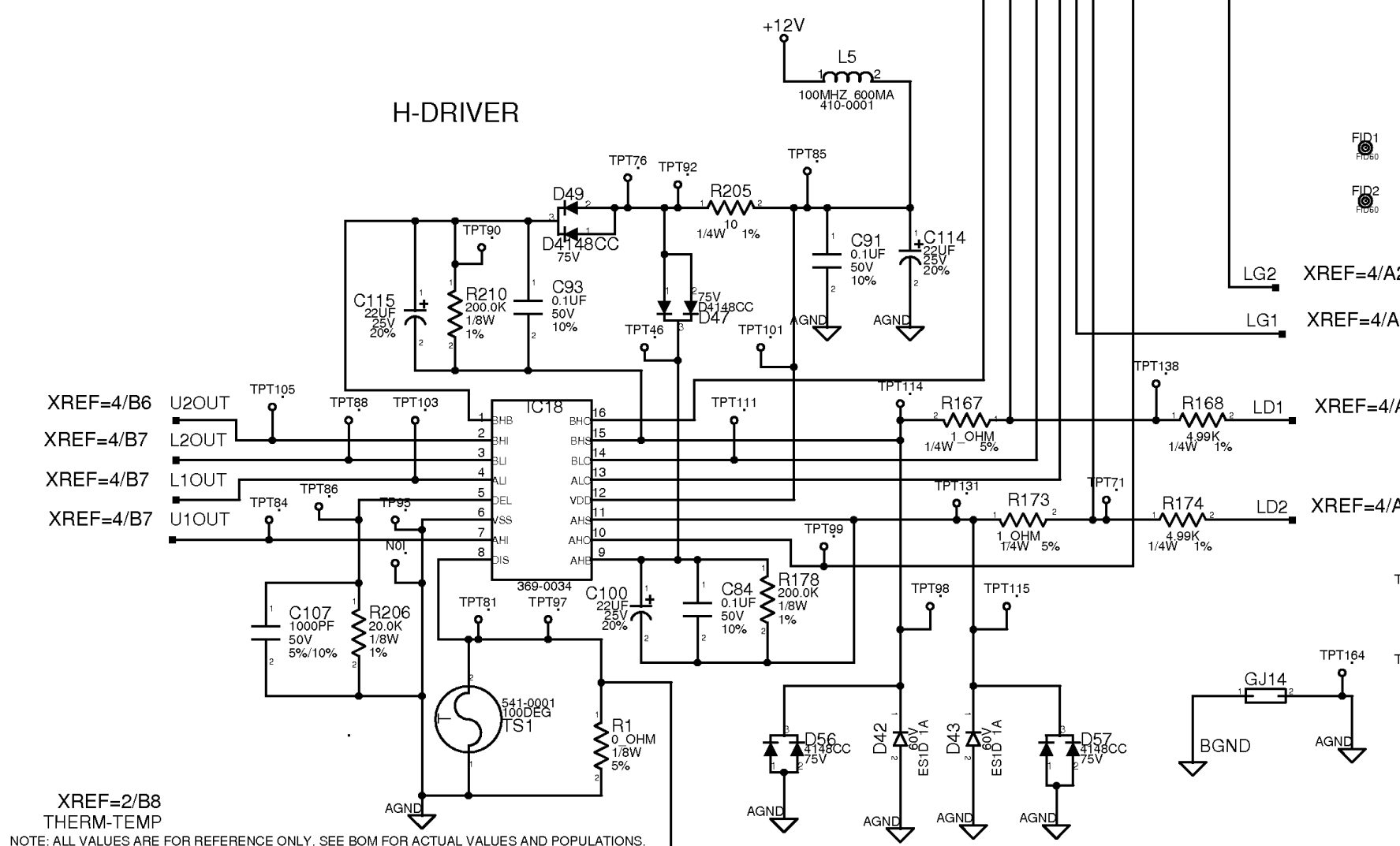
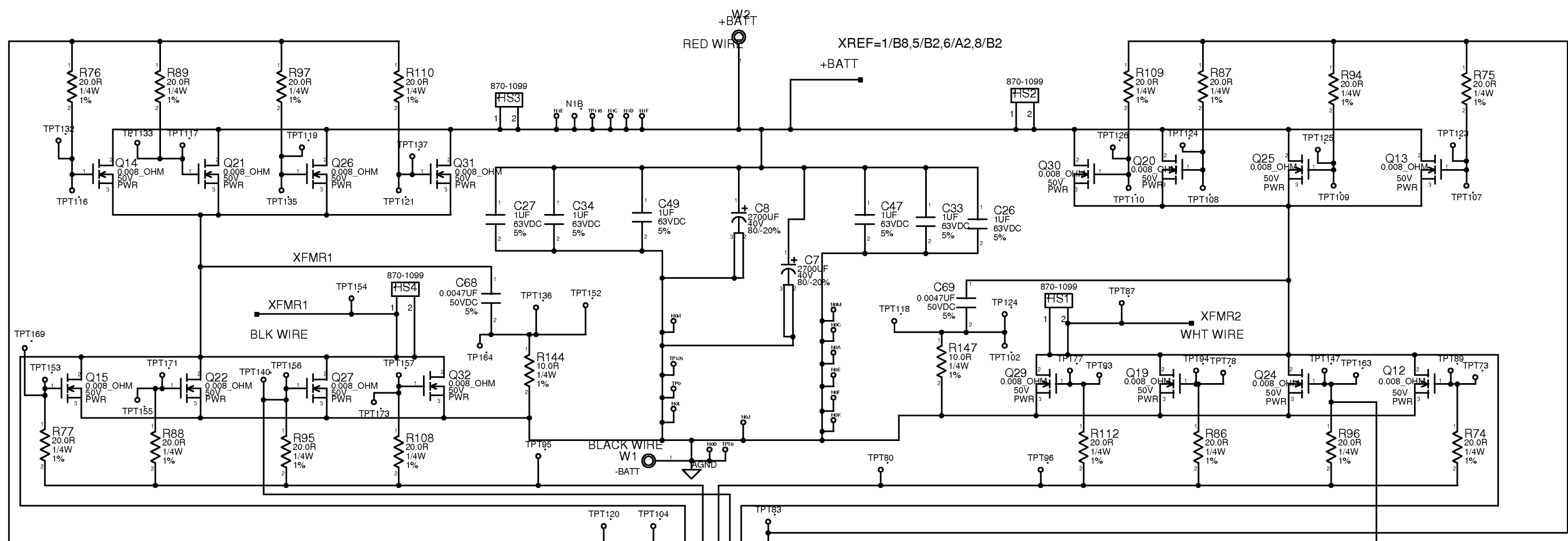




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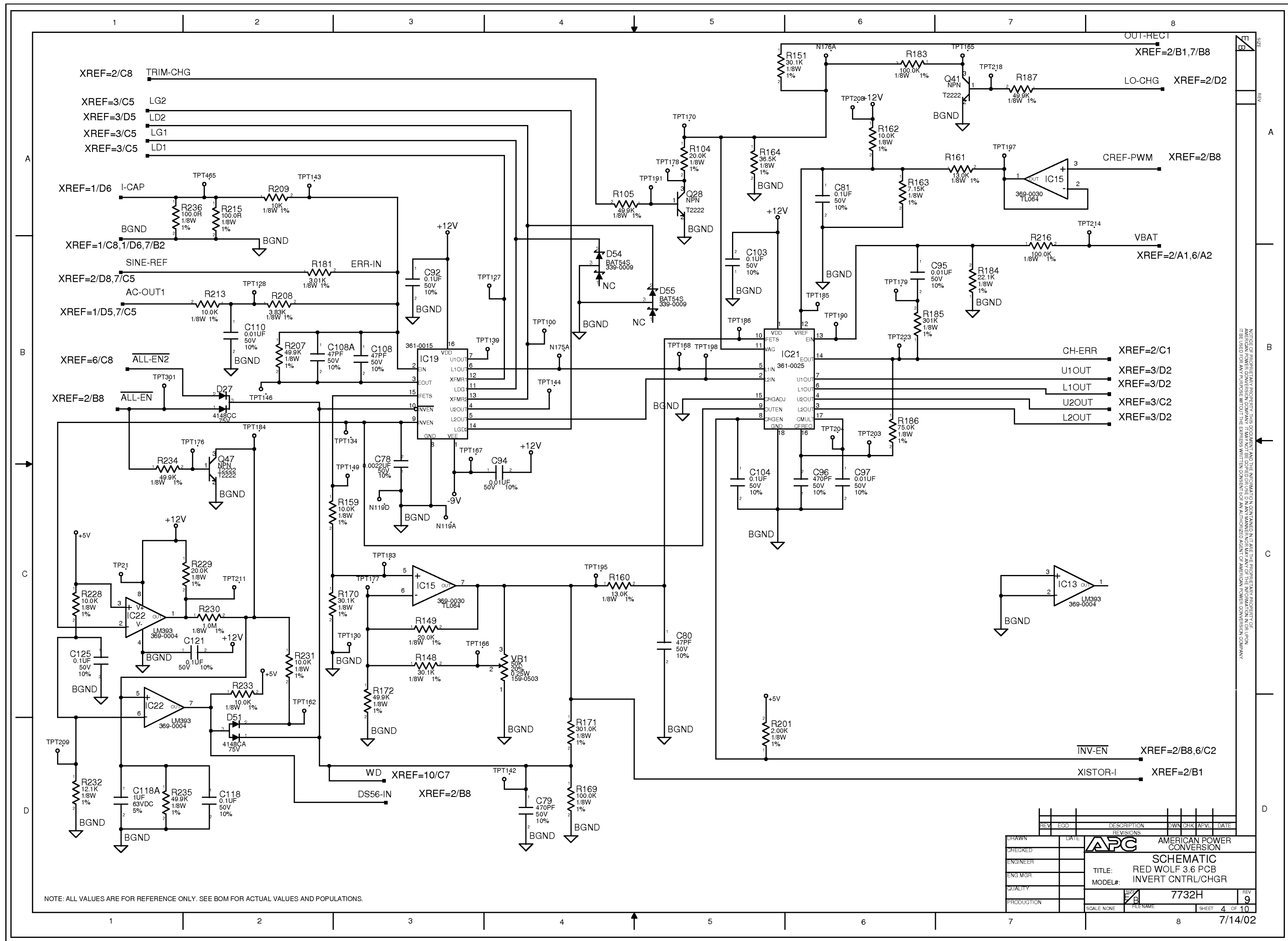


XREF=2/B8
THERM-TEMP
NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REVISIONS				DESCRIPTION			
REV	ECO	DATE	BY	CHK	APVL	DATE	
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DRAWN		DATE		CHECKED		ENGINEER		ENGR MGR		QUALITY		PRODUCTION	

TITLE		MODEL#		SCALE		FILE NAME		SHEET	
RED WOLF 3.6 PCB		BRIDGE		NONE				3 OF 10	



P	N	SIGNAL NAME
1	1	+12V
2	2	SGND
3	3	+24V-OUT
4	4	SGND
5	5	+5V
6	6	SDI-AS1
7	7	SDI-ACC1
8	8	SDI-OUT1
9	9	LO-BATT1
10	10	DIVERT1
11	11	SDO-ACC1
12	12	ON-SW
13	13	OFF-SW
14	14	+

XREF=5/A1
XREF=5/A7, 6/B2, 9/C1
XREF=5/B8, 6/B2, 9/D1

+BATT (SHORT to +BATT thru FUSE)
+BATT
XREF=1/B8, 3/A5, 6/A2, 8/B2

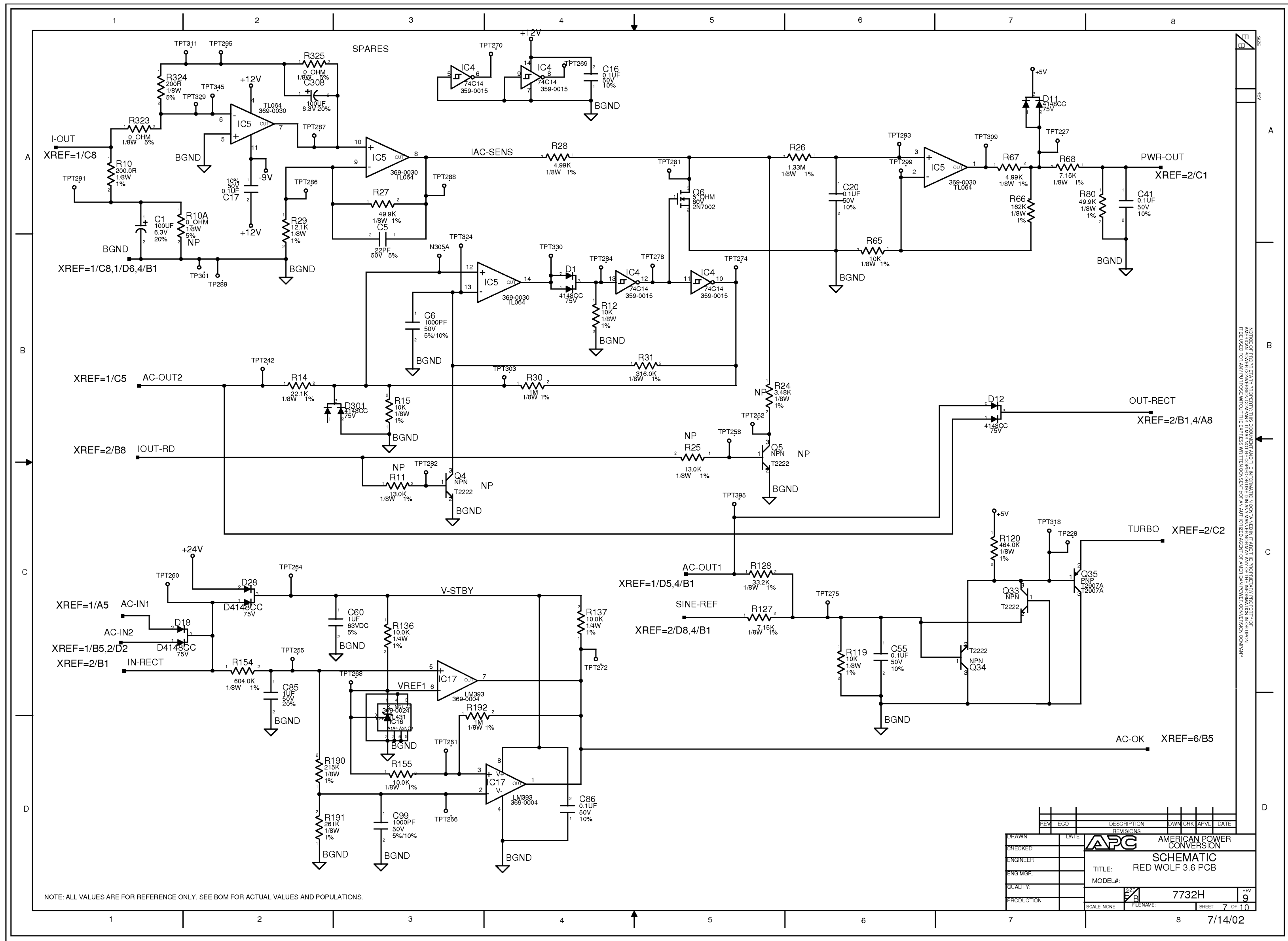
XREF=2/C2
SD-OUT
XREF=2/C2
RXD
XREF=2/B8
LO-BATT

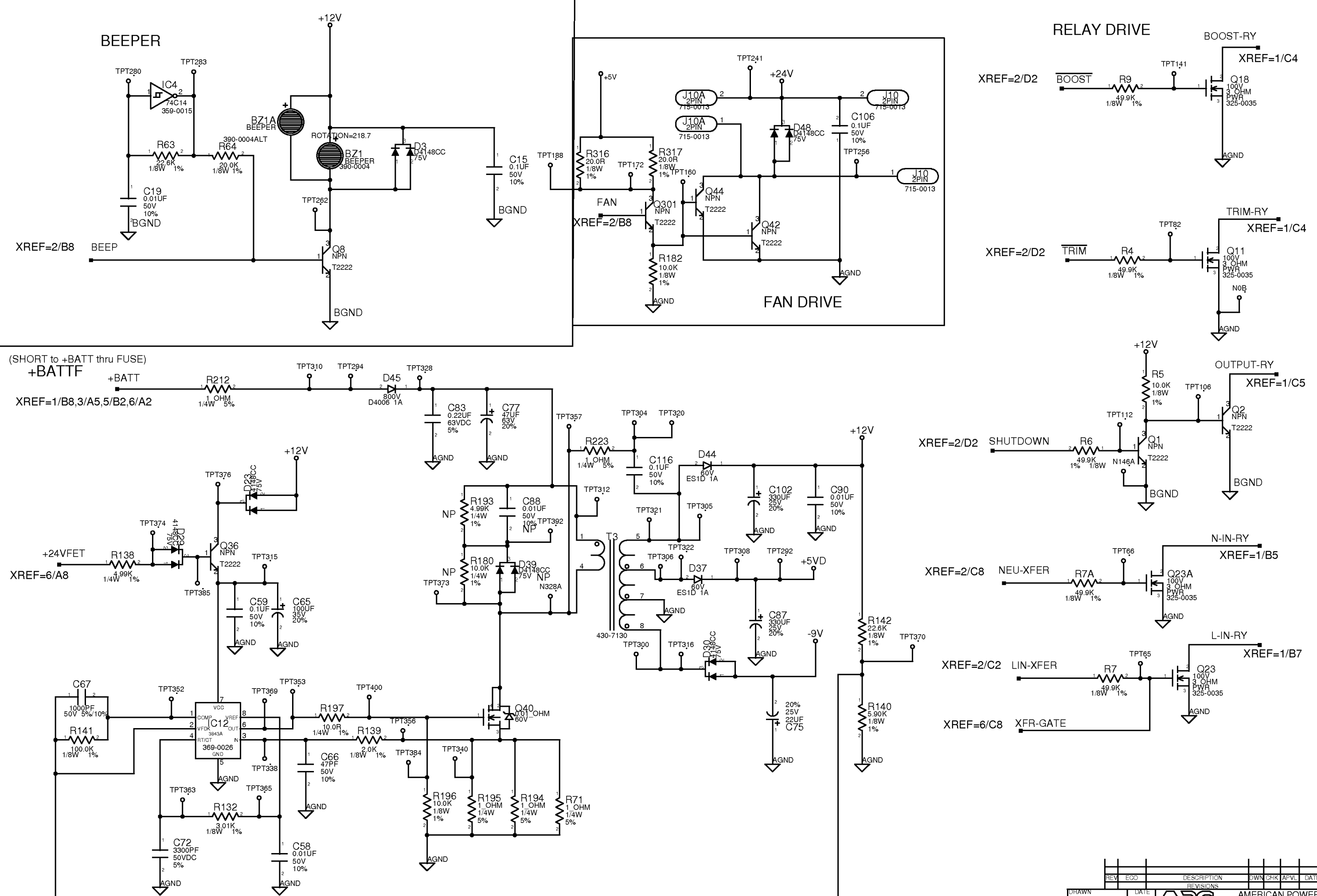
USB SERIAL
DATA OUT
J13
8PIN
715-0014
3
TPT267
TPT225
MUX CNTRL
J13
8PIN
715-0014
2
J13
8PIN
715-0014
8

NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV	ECO	DESCRIPTION	DWN	CHK	APVL	DATE
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AMERICAN POWER
CONVERSION
SCHEMATIC
RED WOLF 3.6 PCB
SMARTSLOT/DB9
MODEL#:
7732H
REV 9
SCALE: NONE
FILE NAME
SHEET 5 OF 10
7/14/02



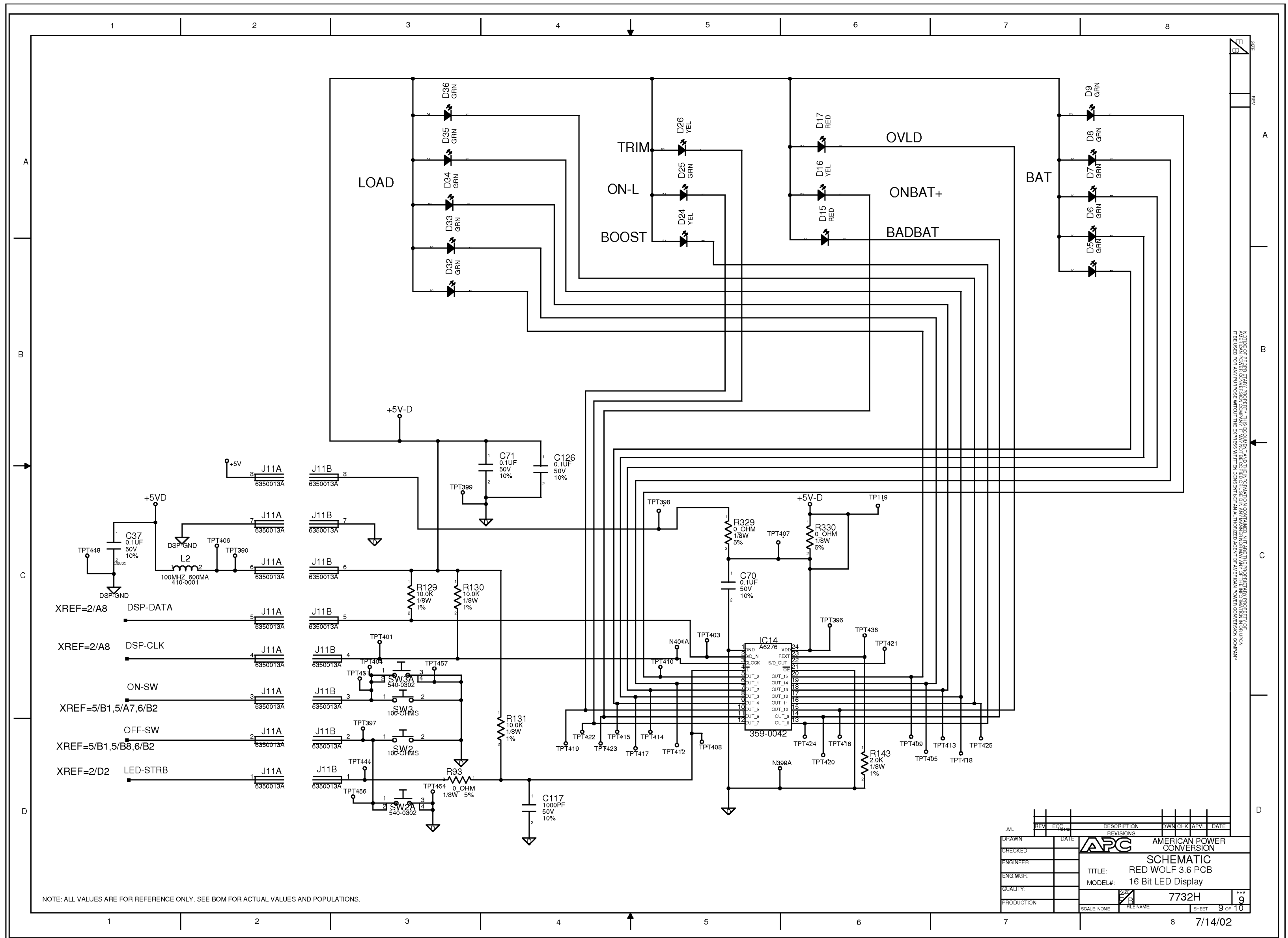


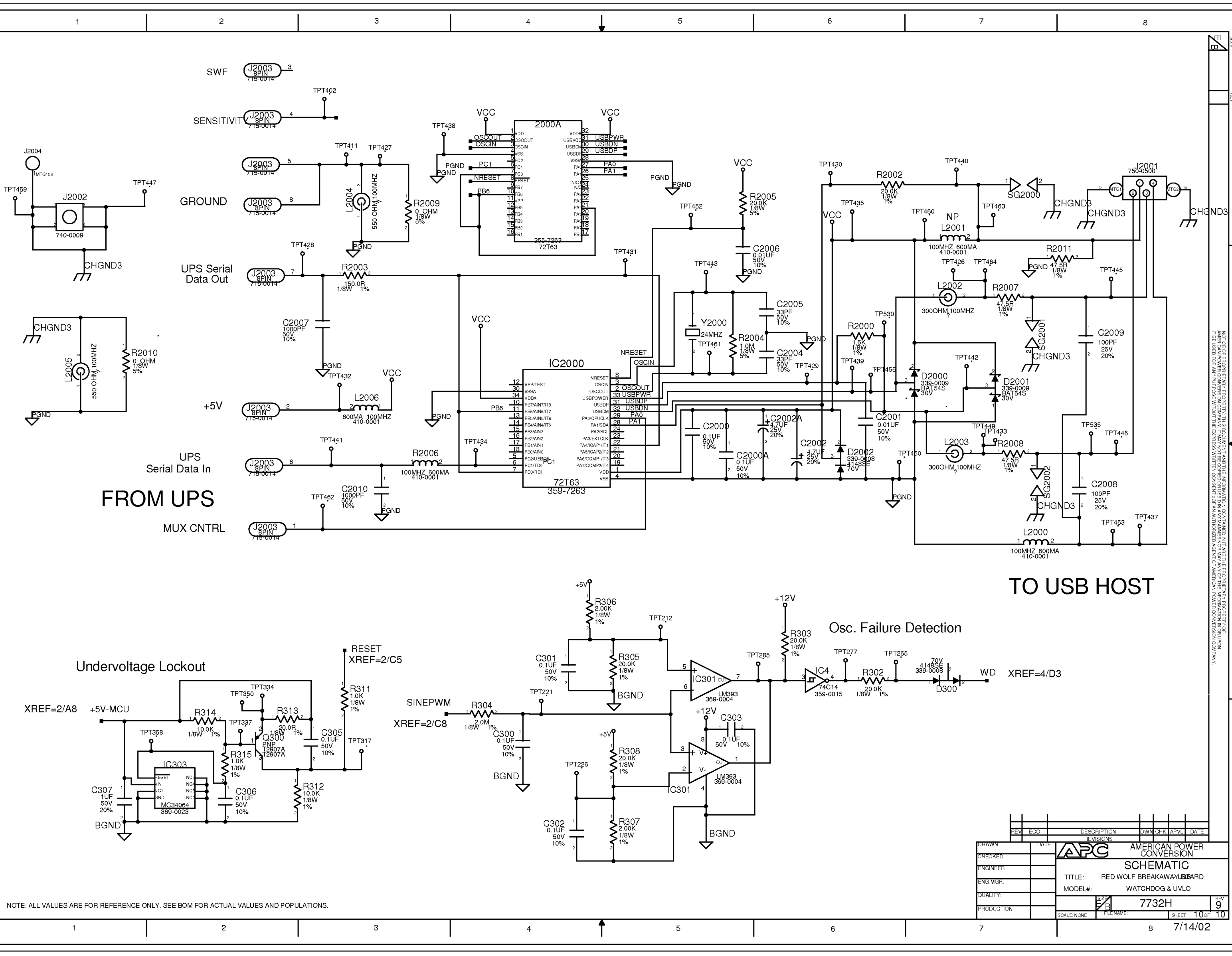
NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV	ECO	DESCRIPTION	OWN	CHK	APVL	DATE
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9						
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DRAWN	DATE	AMERICAN POWER CONVERSION
CHECKED		SCHEMATIC
ENGINEER		TITLE: RED WOLF 3.6 PCB
ENGR MGR		MODEL#:
QUALITY		7732H
PRODUCTION		REV 9
SCALE: NONE	FILE NAME	SHEET 8 OF 10

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REV		ECO	DESCRIPTION	OWN	CHK	APVL	DATE
1			SCHEMATIC				
2			RED WOLF BREAKAWAY USB				
3			WATCHDOG & UVLO				
4			7732H				
5			9				
6			10				
7			10				
8			10				
9			10				
10			10				

SCALE: NONE FILE NAME: SHEET 10 OF 10

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