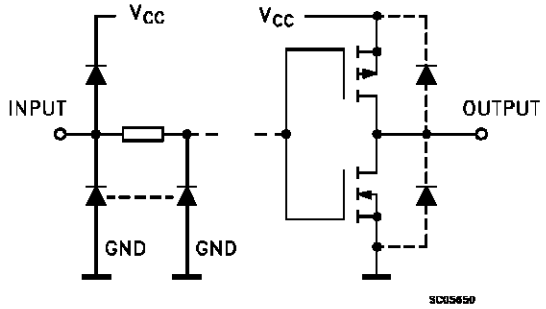
INPUT AND OUTPUT EQUIVALENT CIRCUIT



TRUTH TABLE

INPUTS					OUTPUT
SI	SCK	$\overline{SCLR}$	RCK	$\overline{G}$	
X	X	X	X	H	QA THRU QH OUTPUTS DISABLE
X	X	X	X	L	QA THRU QH OUTPUTS ENABLE
X	X	L	X	X	SHIFT REGISTER IS CLEARED
L	⌐	H	X	X	FIRST STAGE OF S.R. BECOMES "L" OTHER STAGES STORE THE DATA OF PREVIOUS STAGE, RESPECTIVELY
H	⌐	H	X	X	FIRST STAGE OF S.R. BECOMES "H" OTHER STAGES STORE THE DATA OF PREVIOUS STAGE, RESPECTIVELY
X	⌐	H	X	X	STATE OF S.R IS NOT CHANGED
X	X	X	⌐	X	S.R. DATA IS STORED INTO STORAGE REGISTER
X	X	X	⌐	X	STORAGE REGISTER STATE IS NOT CHANGED

X: DON'T CARE

LOGIC DIAGRAM

