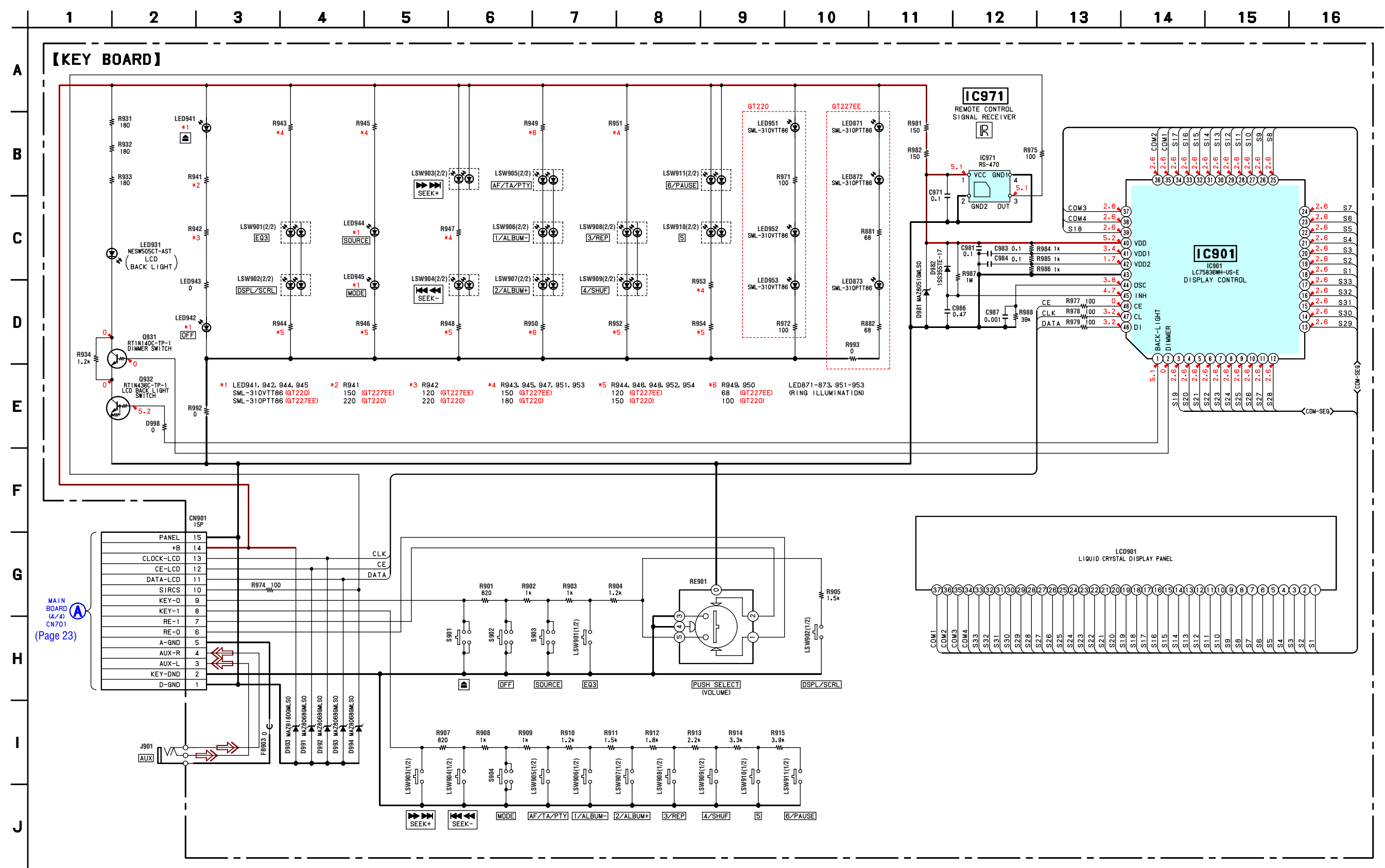
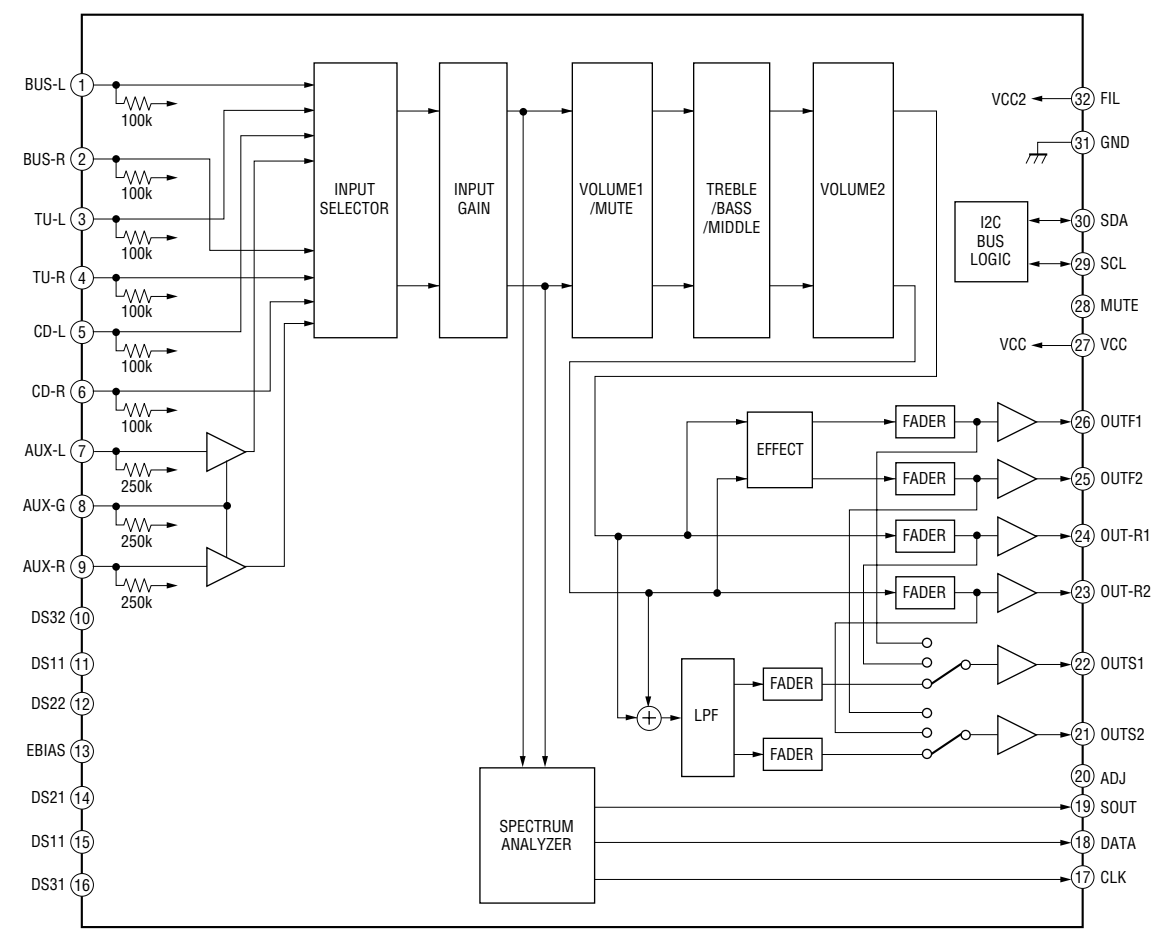


5-9. SCHEMATIC DIAGRAM — KEY SECTION —

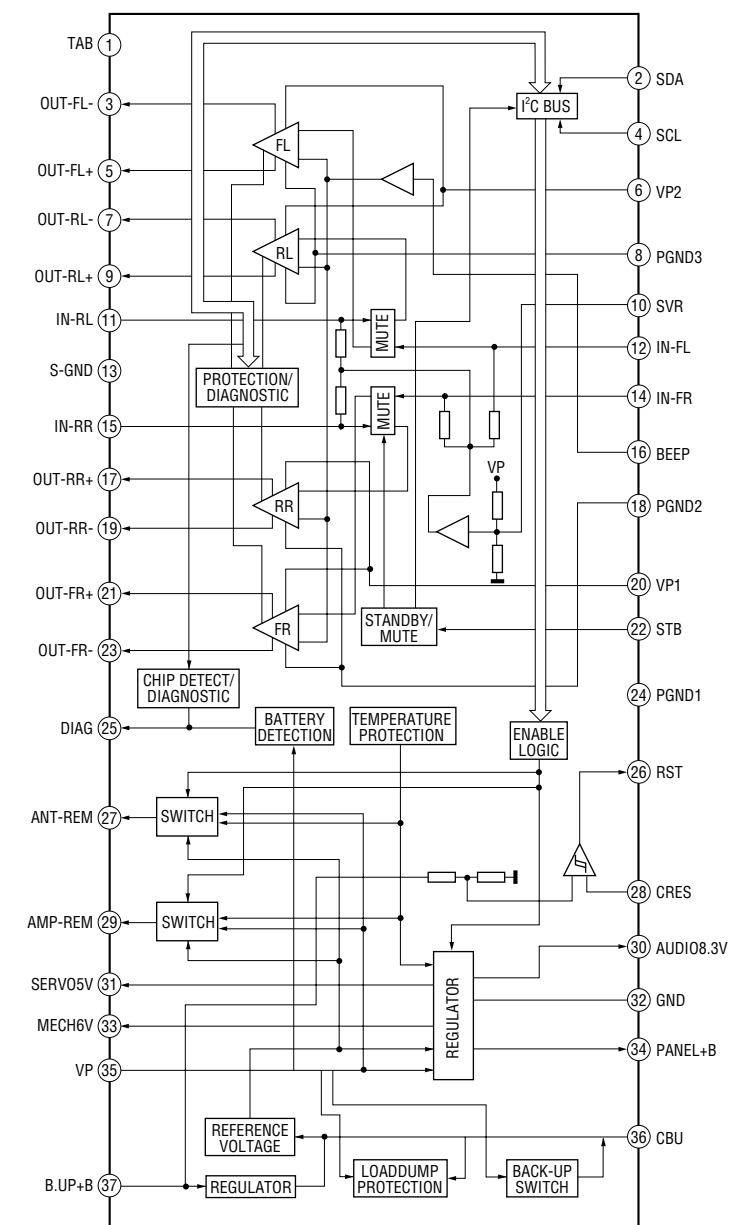


• IC Block Diagrams

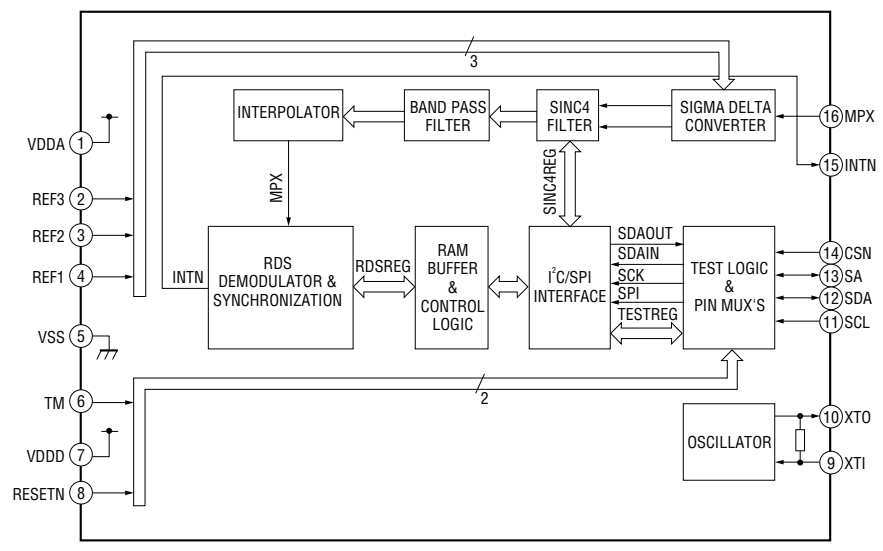
IC401 BD3442FS-E2 (MAIN Board (1/4))



IC750 TDA8589BJ/N2/R1 (MAIN Board (2/4))



IC51 TDA7333013TR (MAIN Board (1/4))



• IC PIN DESCRIPTIONS

• IC501 MB90F045PF-G-9065-SPE1 (SYSTEM CONTROL) (MAIN BOARD (3/4))

Pin No.	Pin Name	I/O	Pin Description
1	AREASEL0	I	Destination function setting pin 0
2	AREASEL1	I	Destination function setting pin 1
3	AREASEL2	I	Destination function setting pin 2
4	AREASEL3	I	Destination function setting pin 3
5	BEEP	O	BEEP signal output to power amplifier
6	CYRIL_SEL	I	Cyril correspondence discrimination signal input "L": No correspondence
7	NOSE_SW	I	Front panel attachment detect signal input "L": Panel on, "H": Panel off
8	DIAG	I	Status signal input from power amplifier
9	VOLATT	O	Electronic volume attenuate control signal output
10	NCO	O	Not used. (Open)
11	VSS	—	Ground pin
12	TUATT	O	Tuner mute control signal output
13	NSMASK	O	Noise mask signal output
14 to 22	NCO	O	Not used. (Open)
23	VCC5	—	Power supply pin (+3.3V)
24	EEP_SIO	I/O	EEPROM bus serial data signal input/output
25	EEP_CKO	O	EEPROM bus serial clock signal output
26	AMPSTB	O	Stand-by signal output to power regulation
27	LCD_CE	O	Chip enable signal output to LCD driver
28	LCD_SO	O	Serial data signal output to LCD driver
29	LCD_CKO	O	Serial clock signal output to LCD driver
30	RDS ON	O	RDS (radio data system) ON signal output
31	RE-IN0	I	Rotary encoder signal input 0
32	RE-IN1	I	Rotary encoder signal input 1
33	I2C_CKO	O	I2C bus serial clock signal output
34	I2C_SIO	I/O	I2C bus serial data signal input/output
35	DAVDD	—	A/D converter power supply pin (+3.3V)
36	AVRH	—	A/D converter external reference power supply pin (+3.3V)
37	DAVSS	—	A/D converter Ground pin
38	QUALITY	I	Noise detect signal input
39	VSM	I	S-meter voltage detect signal input
40	KEYIN1	I	Key signal input 1
41	KEYIN0	I	Key signal input 0
42	VSS	—	Ground pin
43	RC_IN0	I	Rotary commander key signal input Not used in this set. (Pull up)
44 to 48	NCO	O	Not used. (Open)
49	MD0	I	Operation mode setting pin 0 (Pull up)
50	MD1	I	Operation mode setting pin 1 (Pull up)
51	MD2	I	Operation mode setting pin 2 (Pull down)
52	KEYACK	I	Key acknowledgment detect signal input
53	TU_ATTIN	I	Tuner mute zero cross detect signal input
54	BUIN	I	Back-up power supply detect signal input
55	NCO	O	Not used. (Open)
56	DAVN	I	RDS data block synchronized detect signal input
57	NCO	O	Not used. (Open)
58	UNISI	I	S-BUS data signal input
59	UNISO	O	S-BUS data signal output
60	UNISCK	O	S-BUS clock signal output

Pin No.	Pin Name	I/O	Pin Description
61	CODEC_SEL	I	MP3 select signal input "H": MP3, "L": Non-MP3
62	ILLUMI_SEL	I	Illumination voltage setting signal input "H": 10.4 V, "L": 9.0 V
63	NCO	O	Not used. (Open)
64	SIRCS	I	Remote control signal input
65 to 67	NCO	O	Not used. (Open)
68	FLASH_W	I	Memory mode select signal input Not used in this set. (Pull up) Normally "H" input: Single chip mode, after reset "L": Flash write mode
69, 70	NCO	O	Not used. (Open)
71	RC_IN1	I	Rotary commander shift key signal input Not used in this set. (Pull up)
72	ACC_IN	I	Accessory power supply detect signal input
73	TESTIN	I	Test mode detect signal input
74	TELATT	I	Telephone attenuate detect signal input
75	NCO	O	Not used. (Open)
76	AD_ON	O	A/D converter power supply control signal output
77	RESET	I	System reset signal input
78	NCO	O	Not used. (Open)
79	XOUT	O	Low speed operation clock signal output (32.768 kHz)
80	XIN	I	Low speed operation clock signal input (32.768 kHz)
81	VSS1	—	Ground pin
82	OSCIN	I	High speed operation clock signal input (18.432 MHz)
83	OSCOUT	O	High speed operation clock signal output (18.432 MHz)
84	VCC3	—	Power supply pin (+3.3 V)
85	DEMOSEL	I	DEMO select signal input "H": DEMO on, "L": DEMO off
86	ATT	O	Audio mute control signal output
87	BUSON	O	BUS ON signal output
88	SYSRST	O	System reset signal output
89 to 92	NCO	O	Not used. (Open)
93	Z-MUTE	I	CD zero cross mute detect signal input
94	B OUT SEL	I	Black out with/without discrimination select signal input "H": Black out
95	INI RS	I	REAR/SUB INITIAL setting signal input "H": REAR INITIAL, "L": SUB INITIAL
96	MO-DISP	I	Motion display signal input "H": Motion display on, "L": Motion display off
97	DIM_SEL	I	Dimmer select signal input "H": Dimmer, "L": No dimmer Not used in this set. (Pull down)
98	CD_ON	I	CD mechanism servo power supply control request signal input
99	CDM_ON	I	CD mechanism deck power supply control request signal input
100	COL_SW	I	Illumination color select signal input Not used in this set. (Pull down) "H": Two colors selection/initial slave, amber, "L": Color/slave, amber