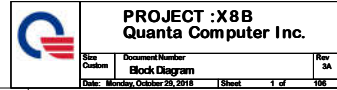
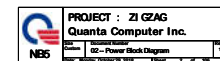


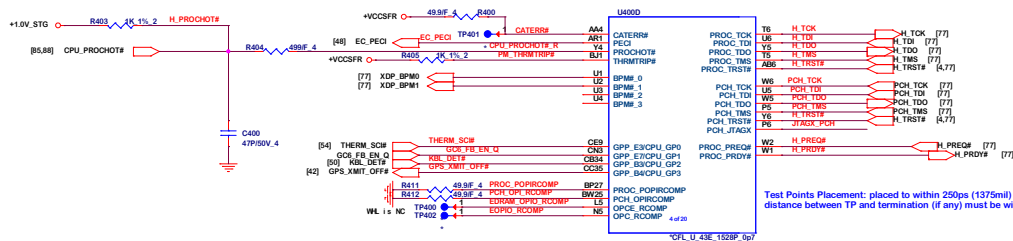
01



02

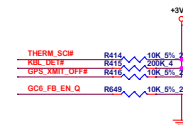


Need apply PN

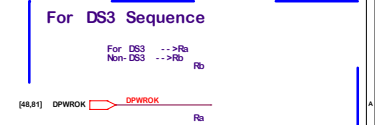
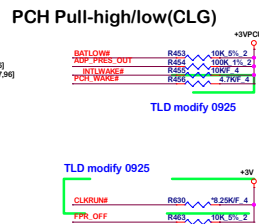
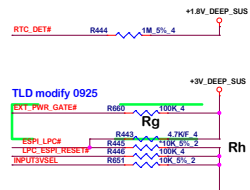
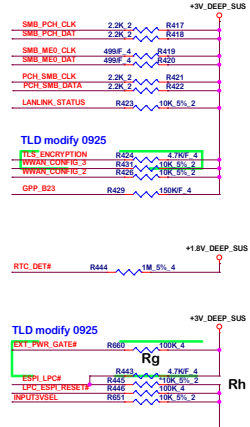
Maximum Trace length:
1ns (approx 6000 mil or 154mm)

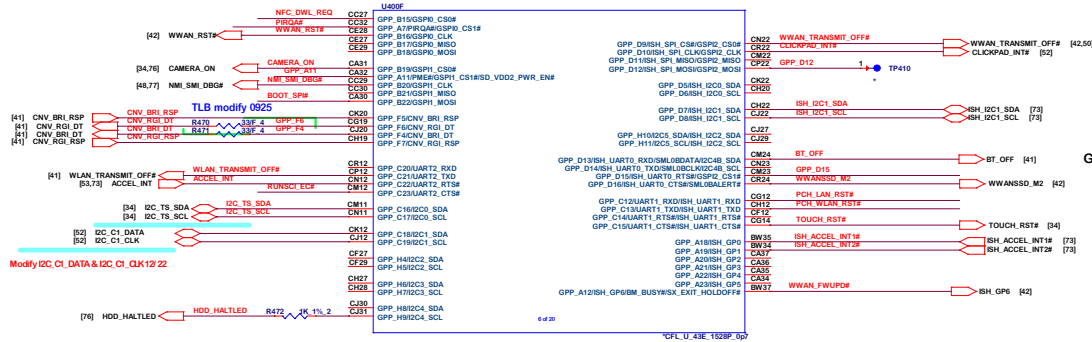
Placed R401 to within 200ps (1100 mil) or
PROC_TCK pin(561)

H_TCK
JTAGX_PCH R402 499F 4S



		PROJECT :X8B Quanta Computer Inc.	
		Document Number 04 - WH/ONL 4/20/MISC7 JTAG	Rev 3A
Size Custom	Date Monday, October 29, 2018	Sheet 4 of	Total 106





Si modify 0604
TLB modify 1004

GPIO Pull-up/Pull-down(CLG)

TLB modify 0925

Si modify 0521

Si modify 0524

TLB modify 0925



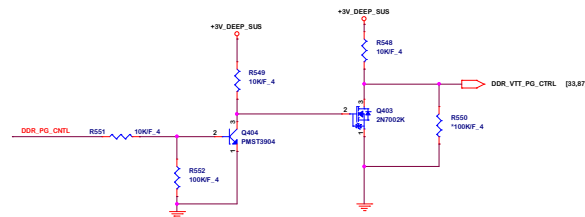
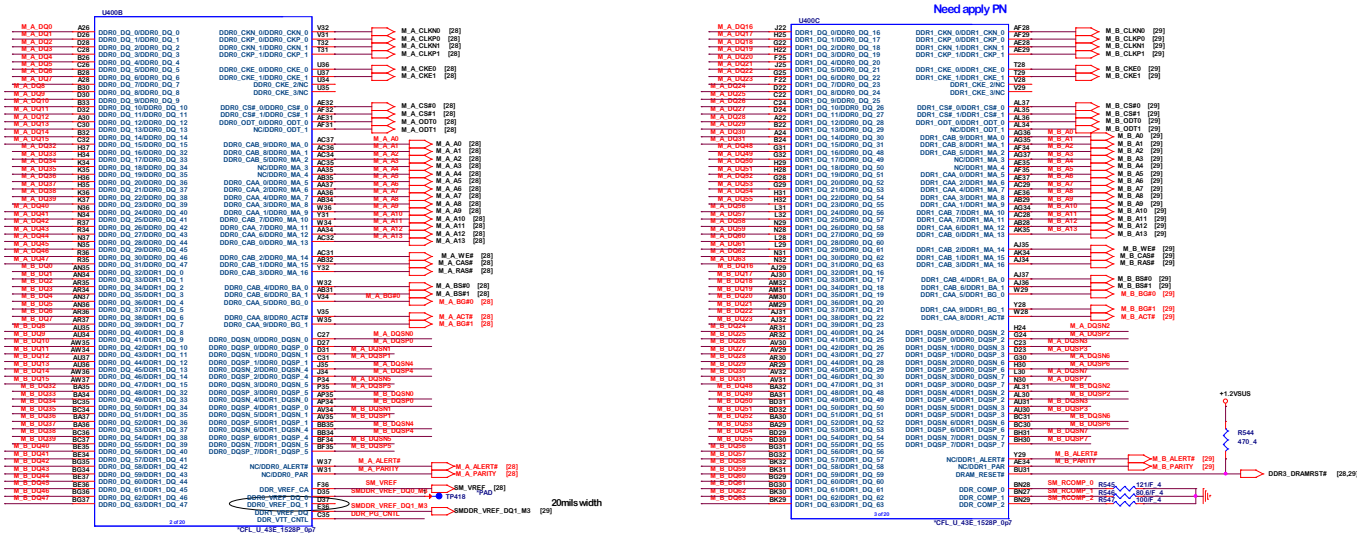
PROJECT :X8B
Quanta Computer Inc.

Rev	Document Number	Rev
06	06 - WHL/ CNL (GPI O)	06
Date: Monday, October 25, 2016	Sheet	6 of 66

[4,5,7,9,11,28,29,34,35,36,40,41,43,47,50,51,52,53,54,73,76,77,81,86,95]

+3V

Need apply PN

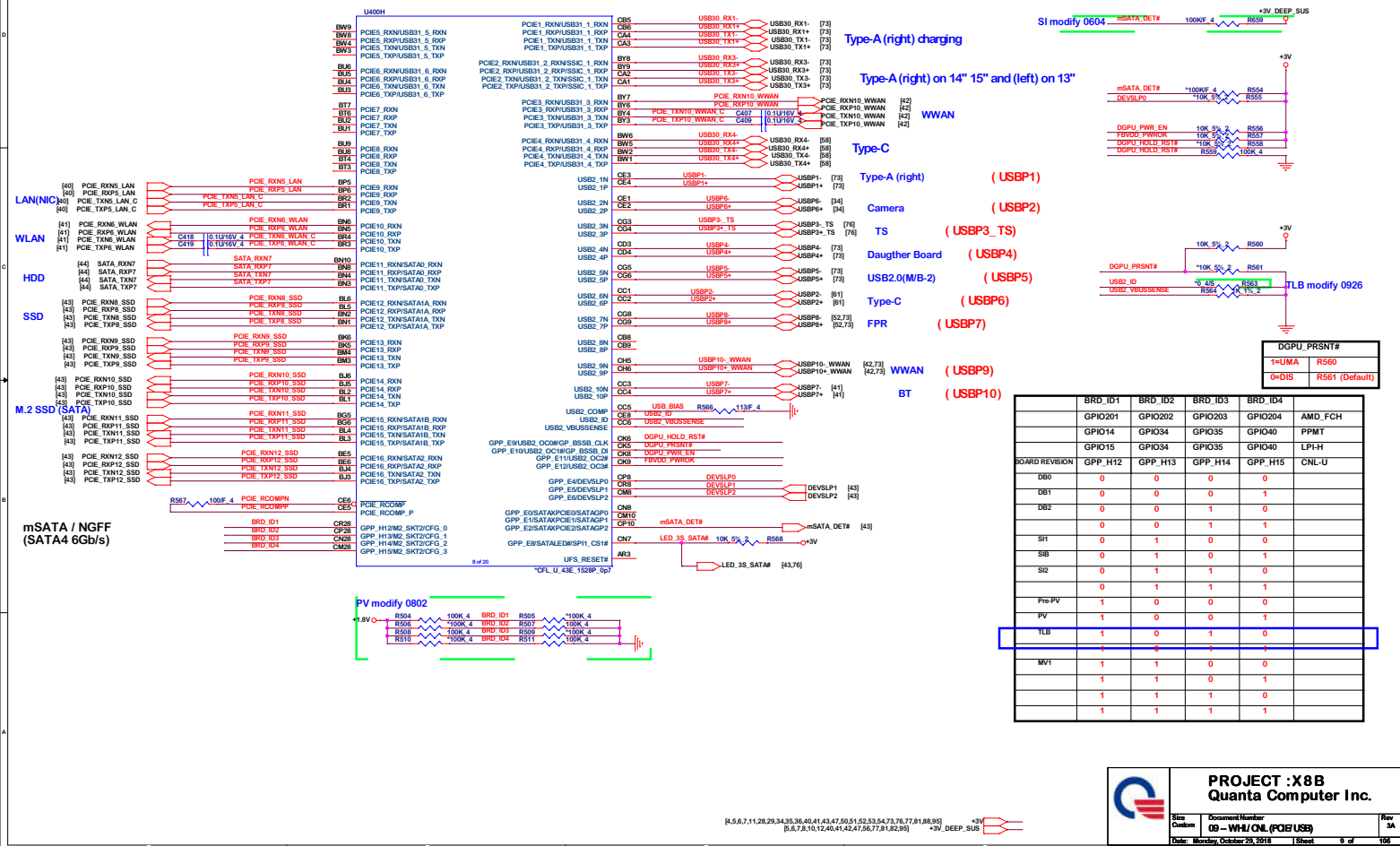


	WHL	CNL
RCOMP0	121ohm	100ohm
RCOMP1	80.6ohm	100ohm
RCOMP2	100ohm	100ohm

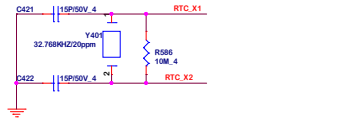


PROJECT :X8B
Quanta Computer Inc.

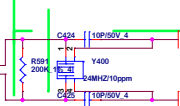
Rev	Customer	Document Number	Rev
01	08-WHL/CNL(DDR4-BV1F)	01	01
Date: Monday, October 26, 2015	1 Sheet	8	of 108



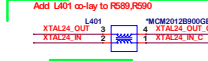
RTC Clock 32.768KHz



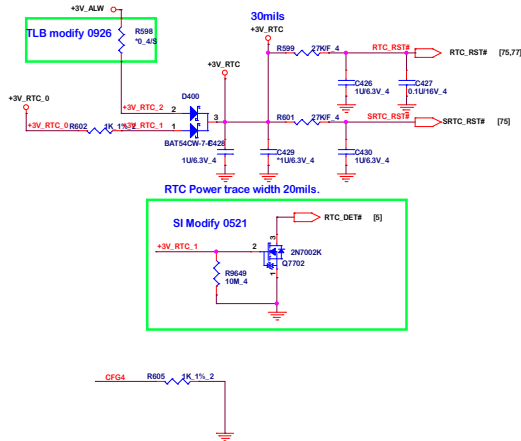
PV Modify 0727



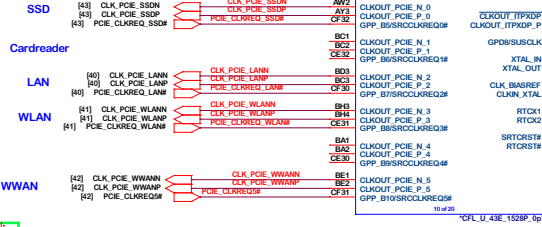
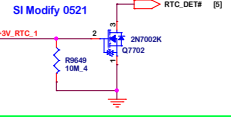
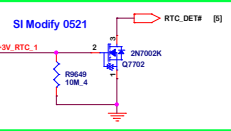
SI Modify 0521



RTC Circuitry(RTC)



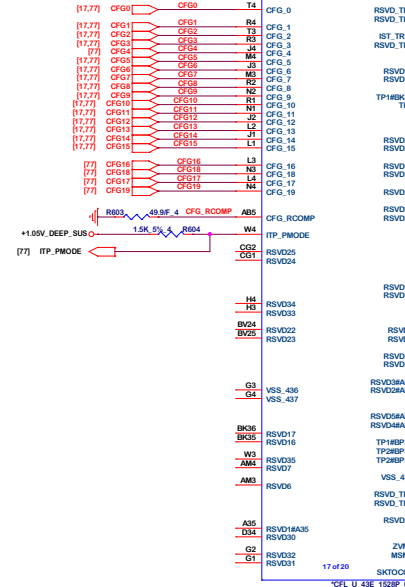
RTC Power trace width 20mils.



SI Modify 0530

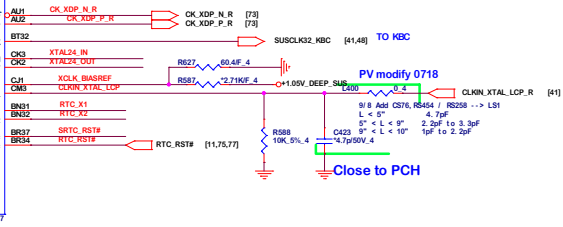


CFG-19 need Reserve TP



CPL_U_43E_1628P.qp

CPL_U_43E_1628P.qp



CLK_REQ/Strap Pin(CLG)



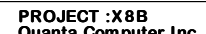
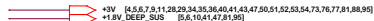
+3V

PROJECT :X8B
Quanta Computer Inc.

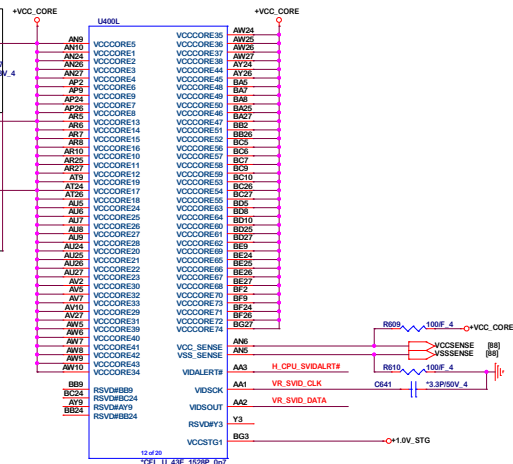
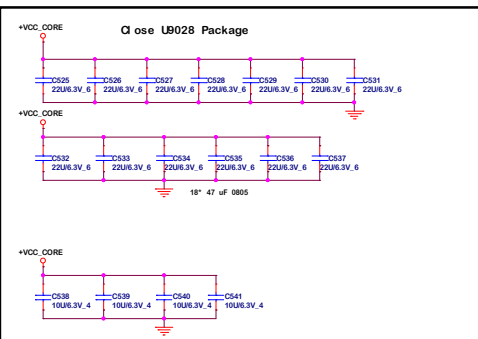
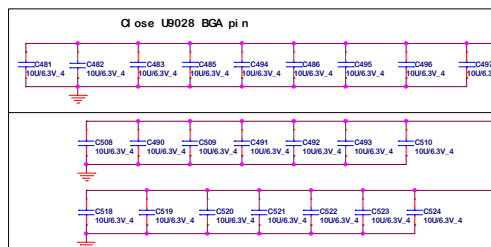
Rev	Document Number	Rev
14	WHU-016 (CLK_REQ/STRAP)	14
Date: Monday, October 26, 2015	1 Sheet	11 of 106



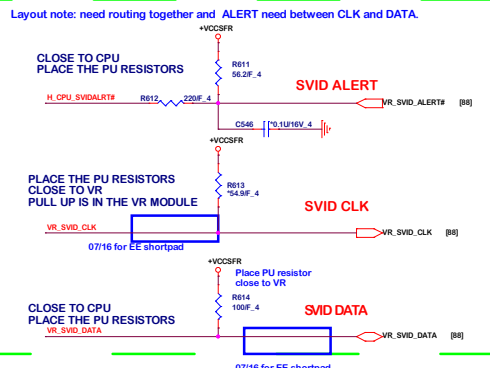
*CFL U 43E 1528P 0p2



Size Custom	Document Number 12 - WHI/ ONL (FCH POWER)	Rev 3A
Date: Monday, October 29, 2018	Sheet 12 of 106	



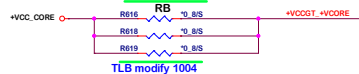
100- $\pm 1\%$ pull-up to VCC near processor.
Trace Length Match <25mil



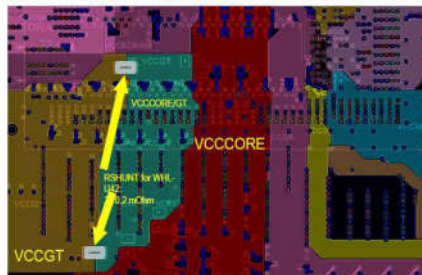
PROJECT :X8B
Quanta Computer Inc.

Rev	Document Number	Rev
Custom	13 - WHU OHL (POWER-1)	34
Date: Monday, October 28, 2013	Sheet	13 of 106

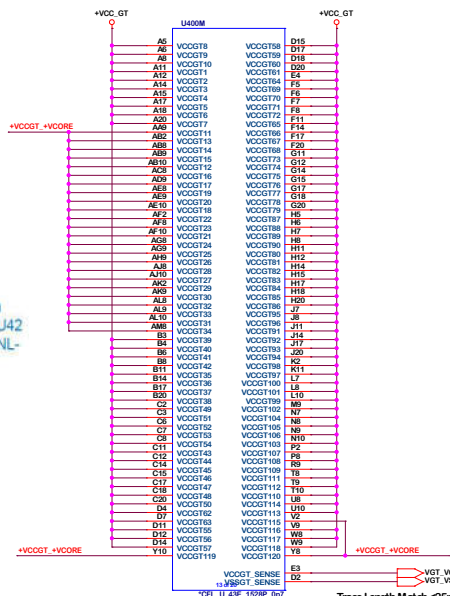
	WHL-U42f ES1 CNL-U22	WHL-U42 ES2
RA	STUFF	NI
RB	NI	STUFF



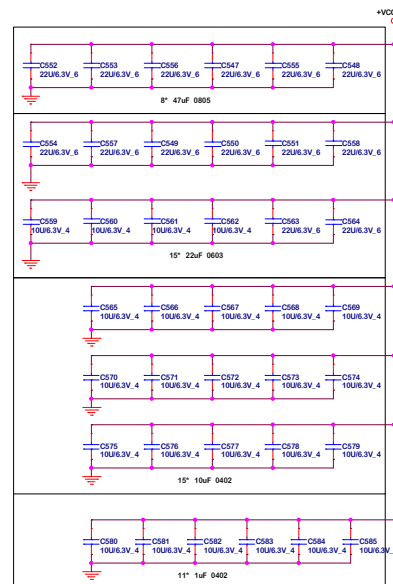
Routing guideline for RSHUNT placement for WHL ES1, CFL43e and CNLU22



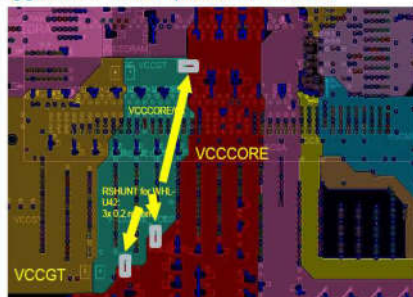
Place 2x 0.2 mOhm
RSHUNT for WHL-U42
(ES1)/CFL-U43e/CNL-
U22



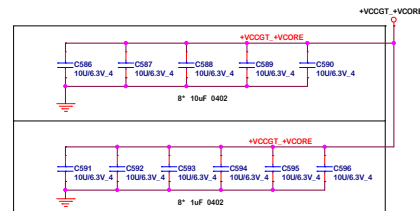
Trace Length Match <25mi



Routing guideline for RSHUNT placement for WHL ES2

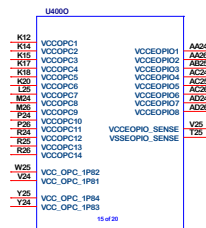


Place 3x 0.2 mOhm
RSHUNT for WHL-
U42 (ES2)



PROJECT :X8B
Quanta Computer Inc.

Size Custom	Document Number 14 - WHI/ ONL (POWER-2)	Rev 3A
Date: Monday, October 29, 2018	Sheet	14 of 108



U4000

CD4

BT9

VSS-2

VSS-3

VSS-4

VSS-5

CM7

CM7

BM8

VSS-9

VSS-10

VSS-11

VSS-12

VSS-13

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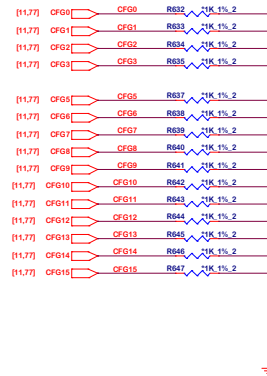
CM6

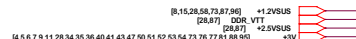
CM6

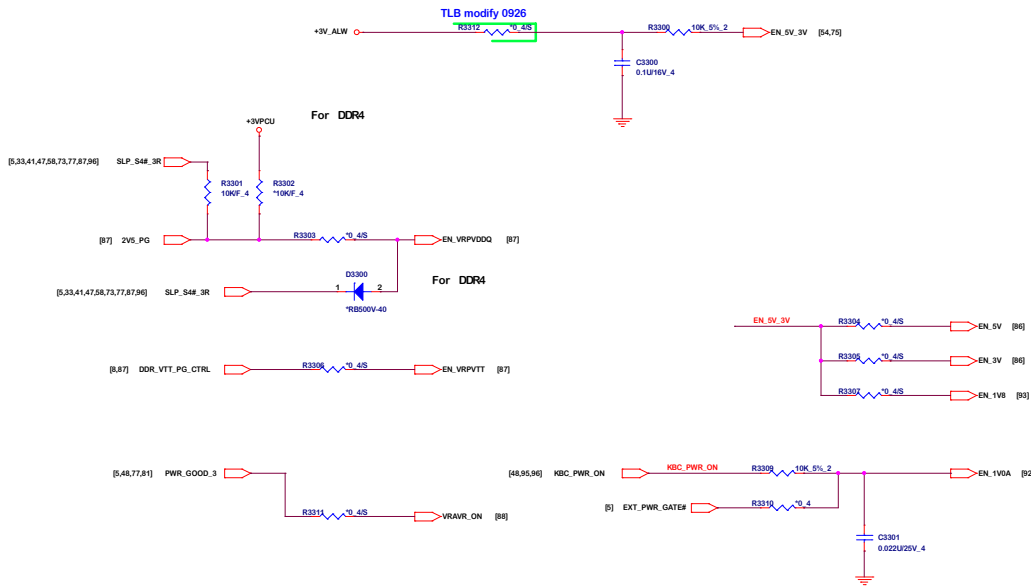
CM6

CM

[illegible]







PROJECT : X8B
Quanta Computer Inc.

Rev	Document Number	Rev
Custom	33 - POWER ENABLE	34
Date	Monday, October 26, 2015	Sheet 33 of 104

[11,41,73,75,81,85,86,87,93] +3V_ALW

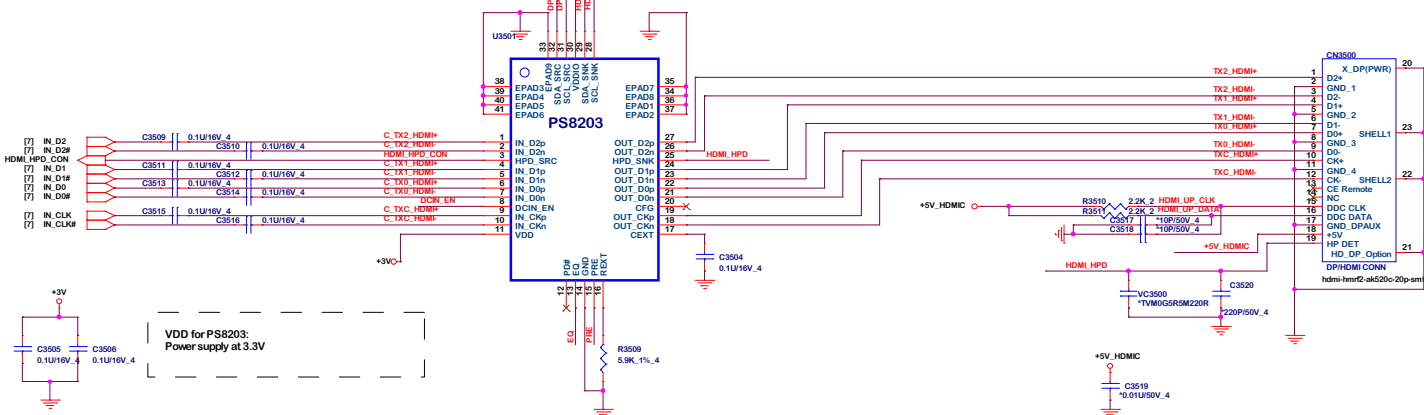
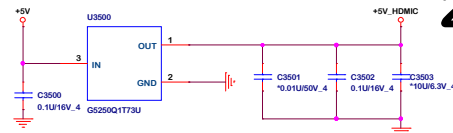
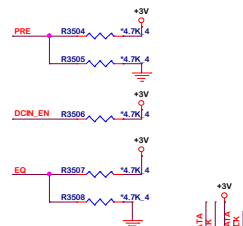
34

Size Custom	Document Number 34 -- LCD CONN/LID/CAM/D-MIC	Rev 3
Date: Monday, October 29, 2018	Sheet 34 of 106	

TX2_HDMI+	R3500	*150 5% 4	TX2_HDMI-
TX1_HDMI+	R3501	*150 5% 4	TX1_HDMI-
TX0_HDMI+	R3502	*150 5% 4	TX0_HDMI-
TXC_HDMI+	R3503	*150 5% 4	TXC_HDMI-

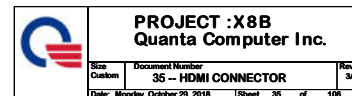
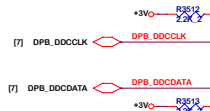
Receiver equalization setting: Internal pull down at ~150k Ω , 3.3V I/O.

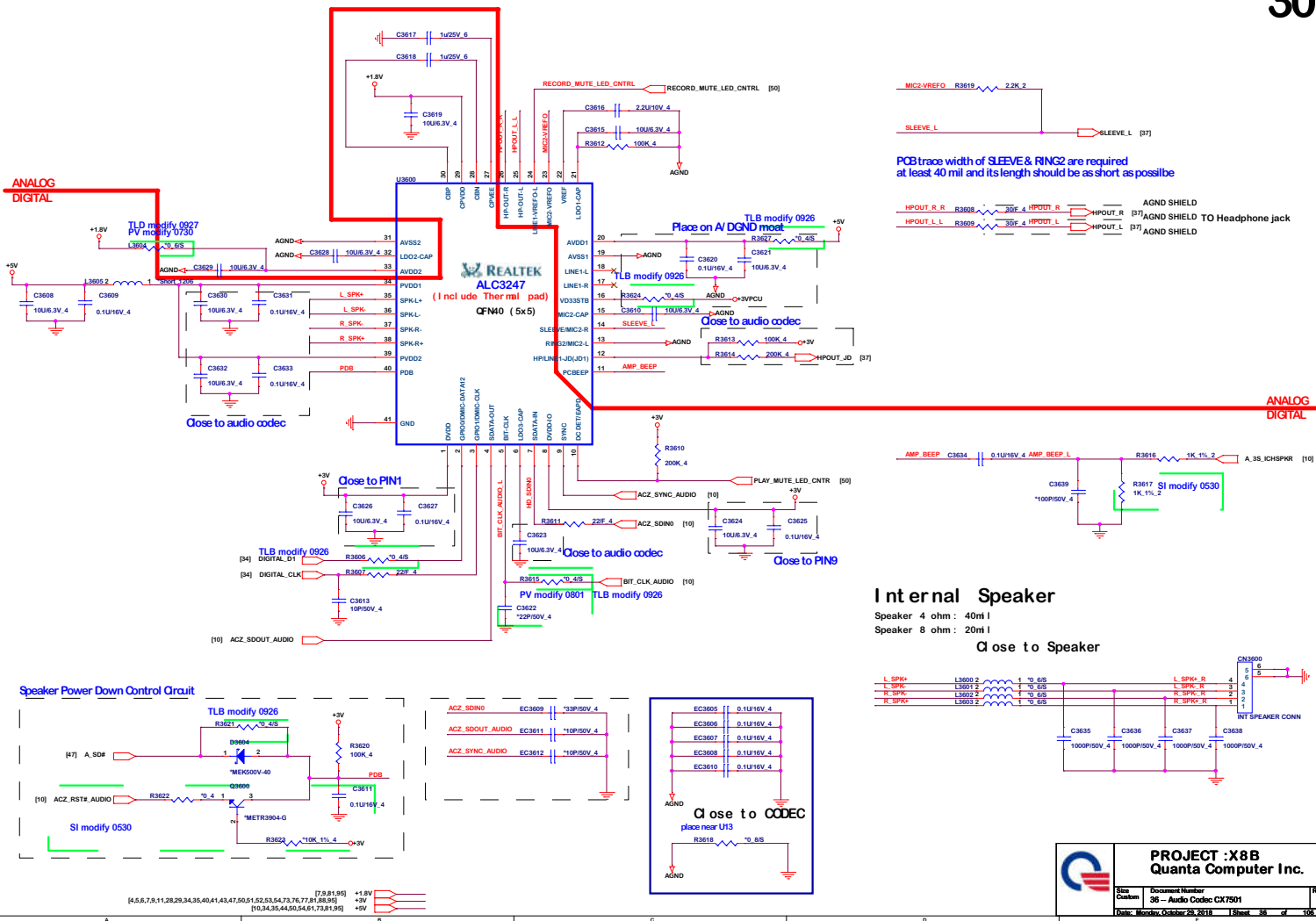
L	programmable EQ for channel loss up to 12.4dB @ 3Gbps
H	programmable EQ for channel loss up to 4.3dB @ 3Gbps
M	non-programmable EQ for channel loss up to 8.6dB @ 3Gbps



for EM request

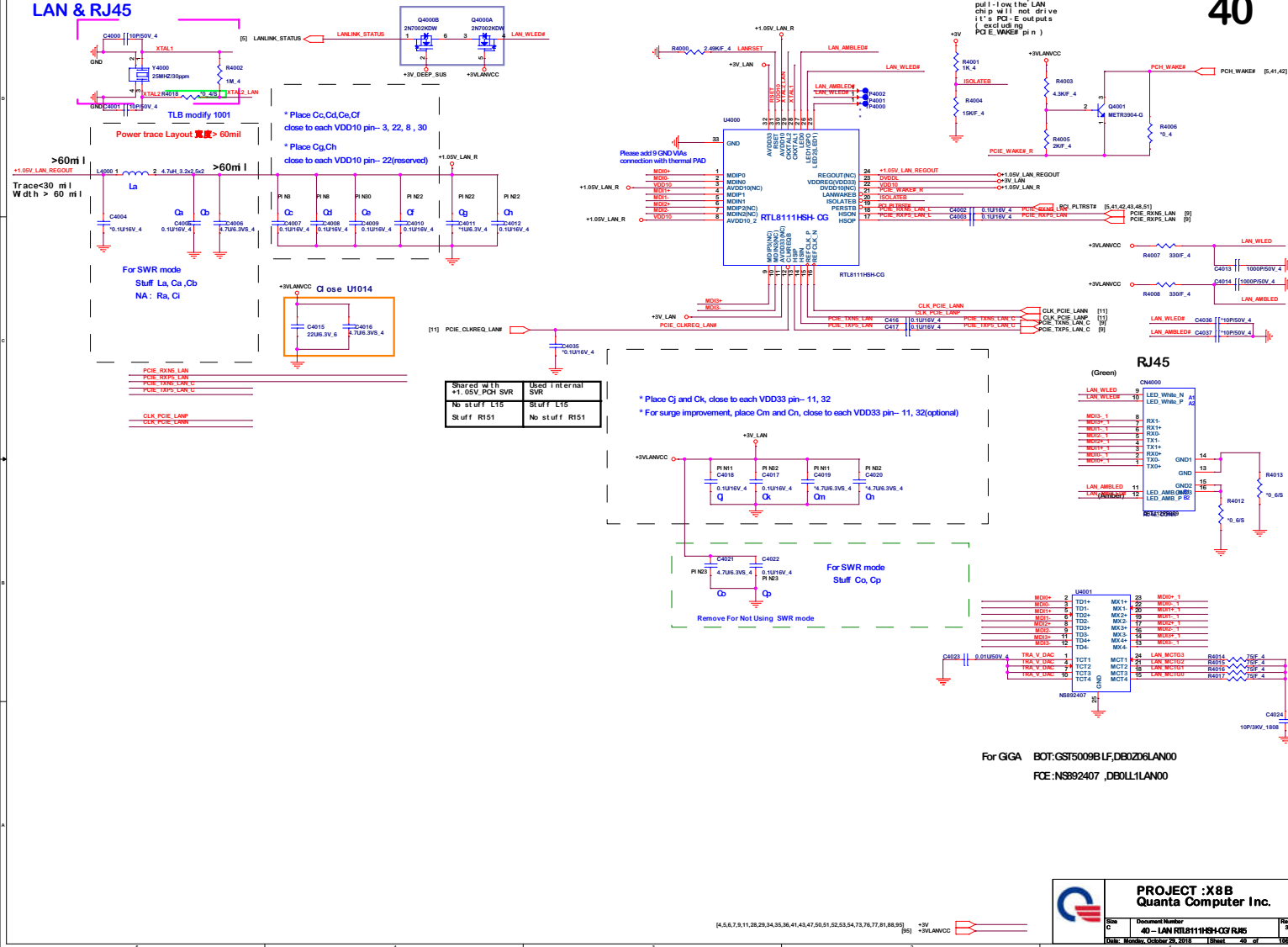
HDMI SMBus Isolation





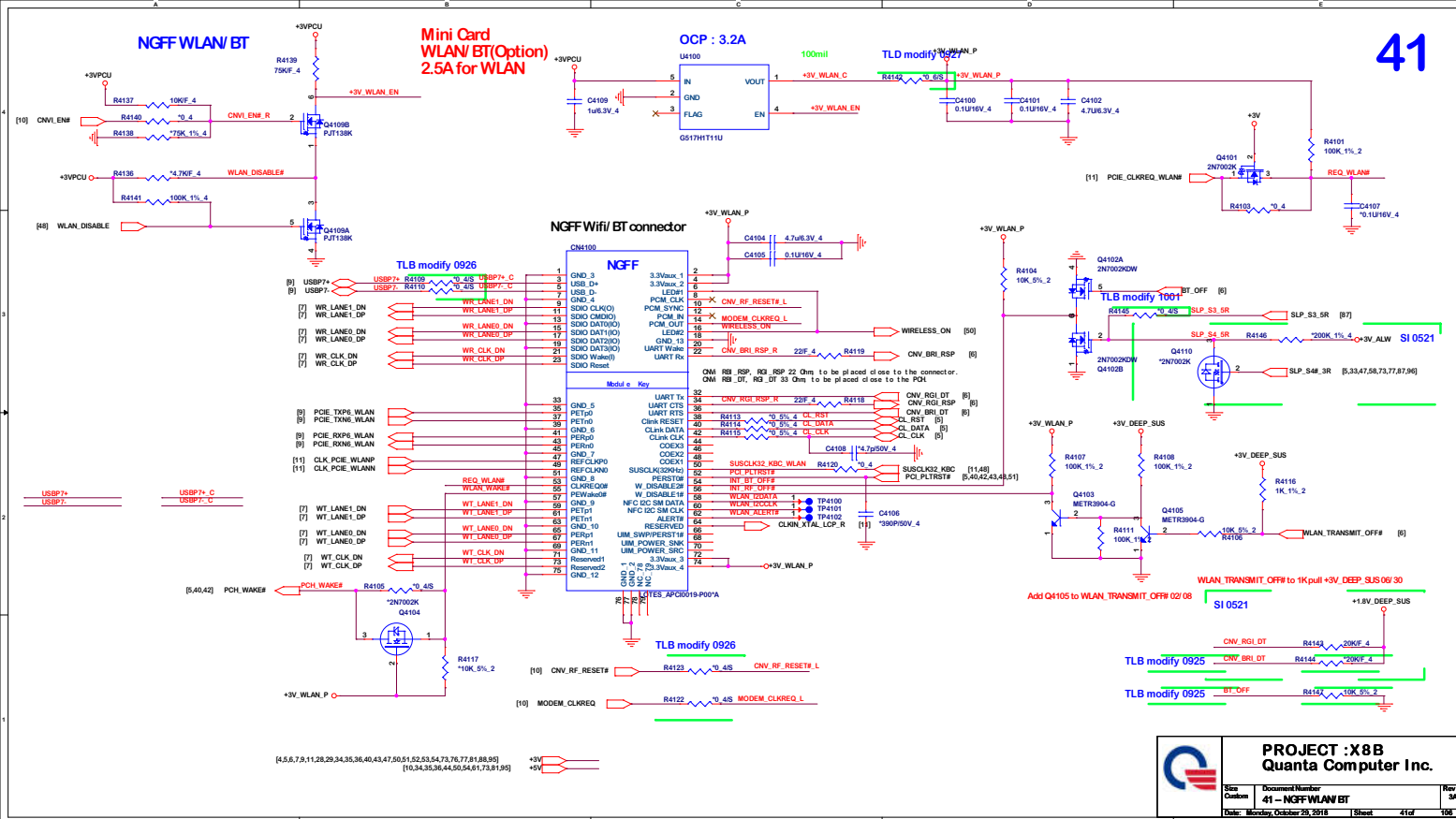


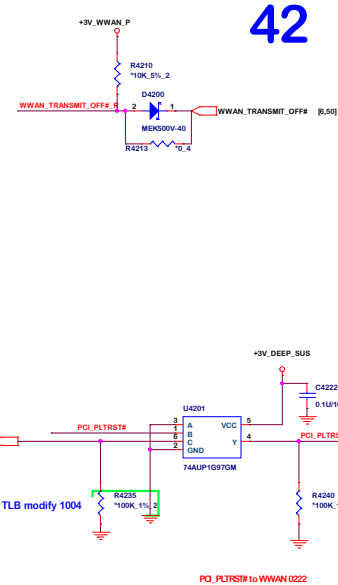
 PROJECT :X8B Quanta Computer Inc.	
Size Custom	Document Number 37 – Audio JACK
Date: Monday, October 26, 2015	Sheet 37 of 106



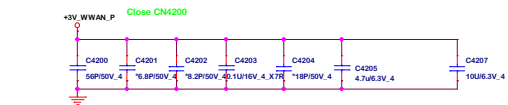
Mini Card
WLAN/ BT(Optional)
2.5A for WLAN

OCP : 3.2A





IoT Circuit

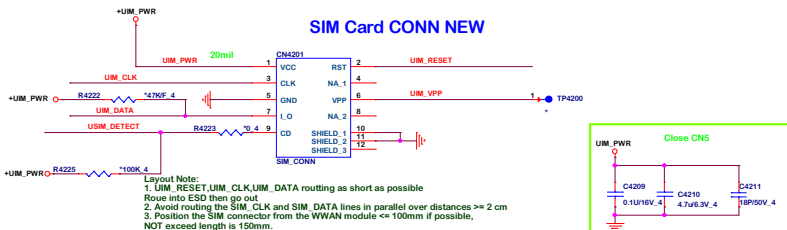


+VCC	Power_On/Off (Pin6)	W_Disable (Pin8)	SP3_Disable (Pin26)
S0 ON	High	High	High
S3 ON	High	Low	Low
S4 ON	Low	Low	Low
S5 ON	Low	Low	Low



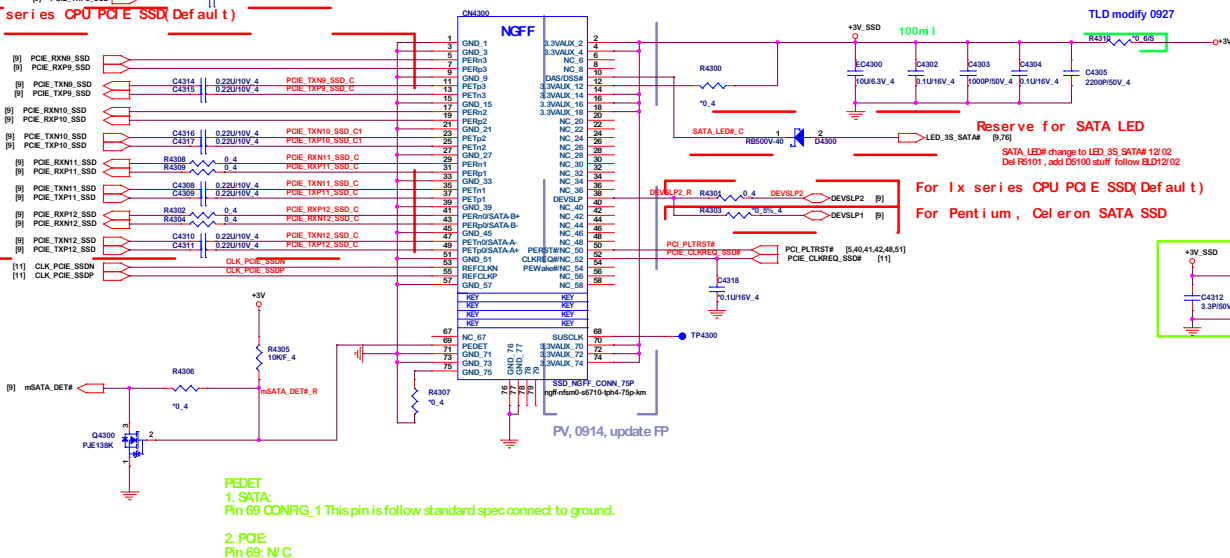
PROJECT :X8B
Quanta Computer Inc.

Size Custom	Document Number 42 – NGFF B KEY FOR WWAN	Rev 1
Date: Friday, November 02, 2018	Sheet 42 of	10



Layout Note:

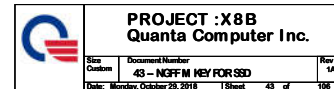
1. UIM_RESET, UIM_CLK, UIM_DATA routing as short as possible
Route into ESD then go out
2. Avoid routing the SIM_CLK and SIM_DATA lines in parallel over distances ≥ 2 cm
3. Position the SIM connector from the WWAN module ≤ 100 mm if possible,
NOT exceed length is 150mm.



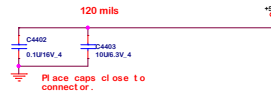
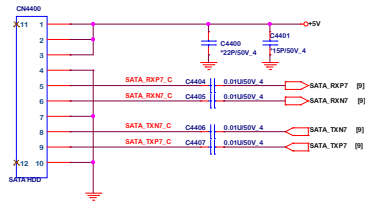
PEDET
1. SATA:
Pin 69 CONFIG 1 This pin is follow standard spec connect to ground.

2. PQIE
Pin 69: N/ C

[4,5,6,7,9,11,28,29,34,35,36,40,41,47,50,51,52,53,54,73,76,77,81,88,95]



SATA-HDD



[A,5,6,7,9,11,28,29,34,35,36,40,41,43,47,50,51,52,53,54,73,76,77,81,88,90]
[10,34,35,36,50,54,81,73,81,90]

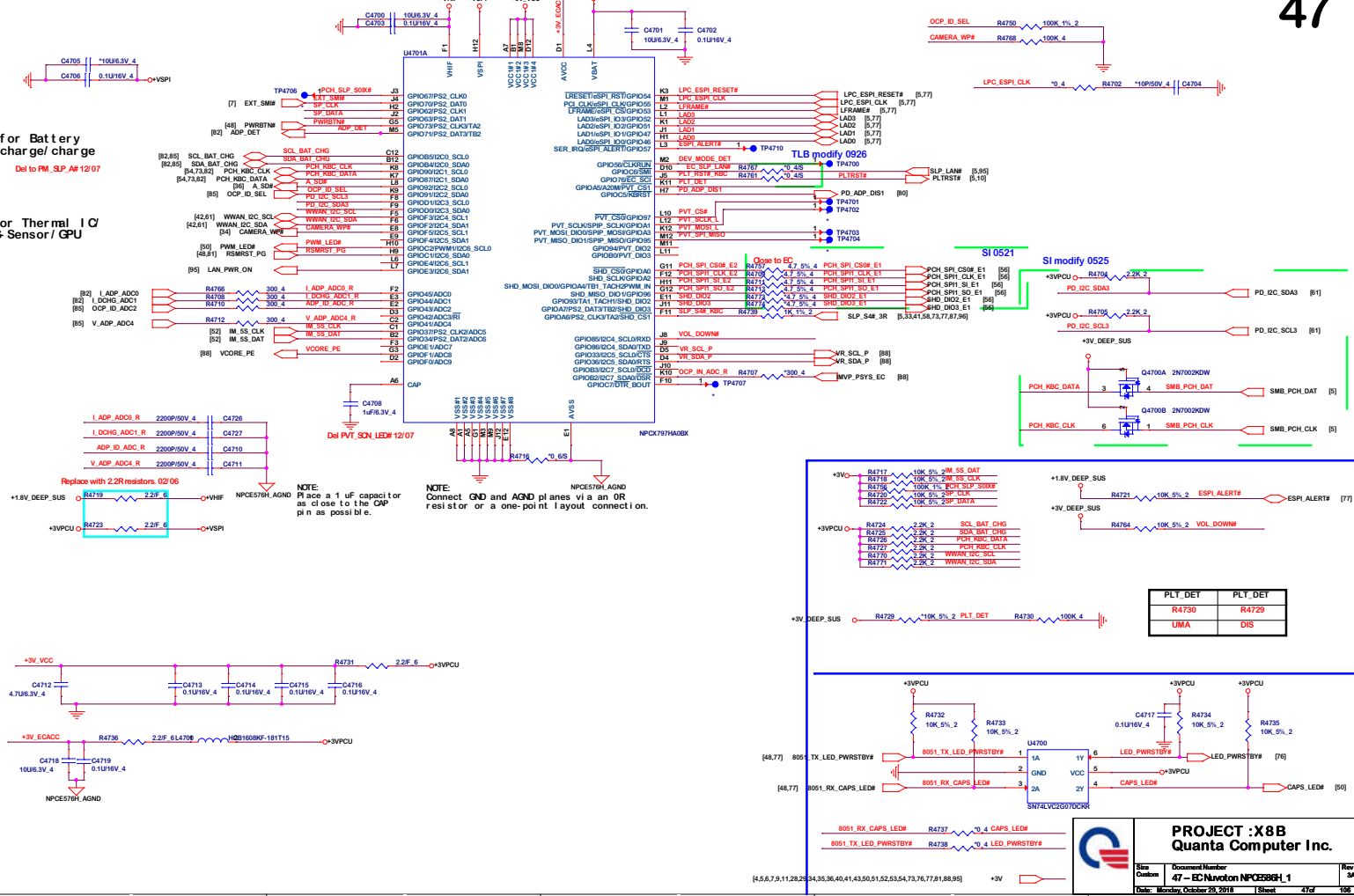
+5V
+5V

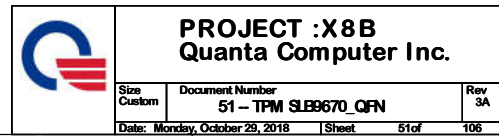
		PROJECT :X8B Quanta Computer Inc.	
		Doc ID	Document Number 44 - HDD EMI cap
Date: Monday, October 29, 2018		18mm	440

for Battery
charge/charge

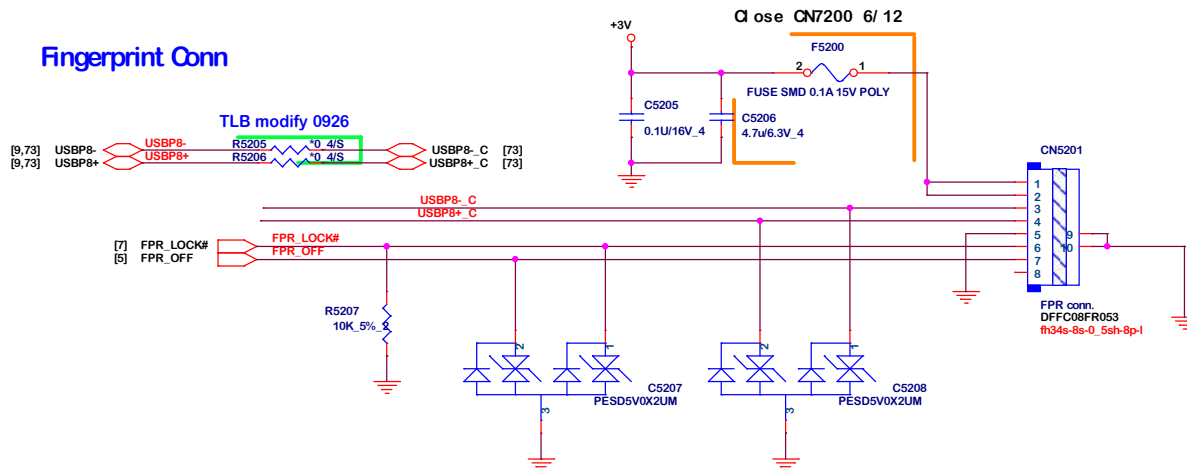
Del to PM_SLP_A# 12/07

for Thermal I/O
G-Sensor/GPU

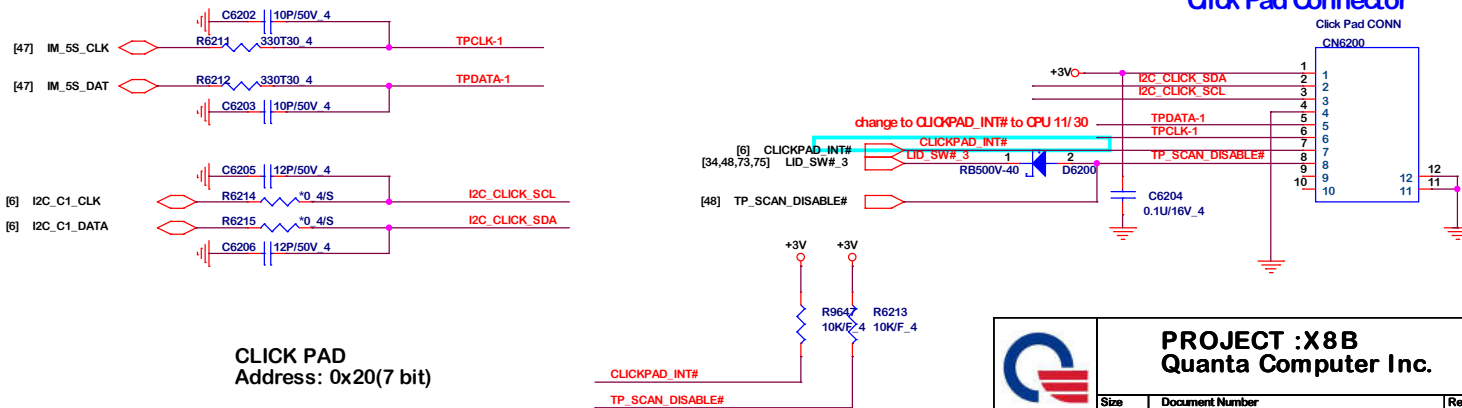




Fingerprint Conn



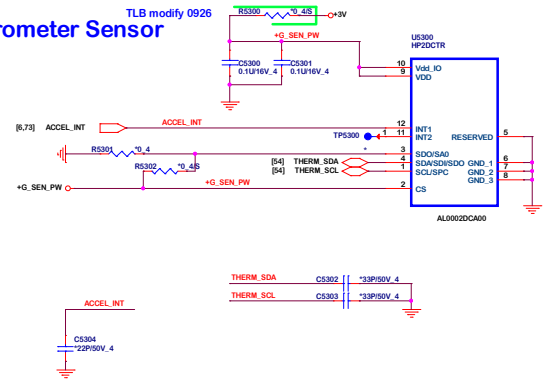
Click Pad Connector



PROJECT :X8B
Quanta Computer Inc.

Size Custom	Document Number S2 - FPR / Click PAD	Rev 3A
Date: Monday, October 29, 2018	Sheet 52 of 106	

Accelerometer Sensor



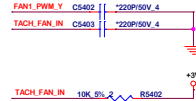
[4,5,6,7,8,11,28,29,34,35,36,40,41,43,47,50,51,52,54,73,76,77,81,88,90] +3V

[5,7,12,33,34,36,41,42,47,48,50,54,58,61,73,75,77,80,81,82,85,86,87,88,92,93,95] +3V/PCU



PROJECT :X8B
Quanta Computer Inc.

Doc	Document Number	Rev
Custom	53 - TS and Accelerometer	3A
Date:	Monday, October 28, 2019	1 Sheet 63 of 104



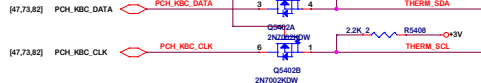
CPU Thermal Sensor



Under CPU

Over Temperature Protection	
DEGREE	RS406
68	38.4K
70	36.5K
73	34K
75	33.2K
81	28K

Over Temperature Protection		
DEGREE	R5406	
68	38.4K	C/S3832FB08
70	36.5K	
73	34K	C/S3402FB18
75	33.2K	C/S3322FB13
81	28K	
85	25.5K	C/S2552FB11



5) +3V +3V_ALW



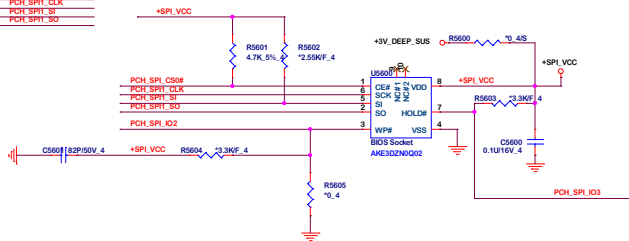
Size Custom	Document Number 54- FAN and Thermal IC	Rev 3A
Date: Monday, October 29, 2018	Sheet 54 of	106

SPI ROM

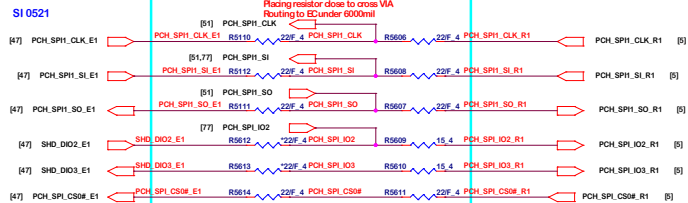
Vendor	Size	P/N
GD	128MB	AKE2DF00Q00 Pilot run stage
Winbond	128MB	AKE3DF-KN00
Socket		DFHS08FS046
GD	128MB	AKE3DZNOQ02 MV stage
Winbond	128MB	AKE3DF-KN01

PCH SPI ROM(CLG) Place TP at TOP side

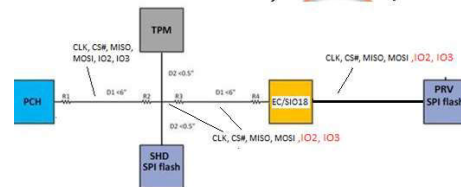
TP5600	1	PCH_SPL_CS0#
TP5601	2	PCH_SPL_CLK
TP5602	3	PCH_SPL_SI
TP5603	4	PCH_SPL_SO



PCH 6*5mm WSON 16M SPI ROM Socket



Placing resistor close to cross VIA
Routing to EC under 6000mil



nuvoton

Nuvoton Confidential - Provided under NDA

Based on EC18 testing: PCH to TPM/SPI flash = Quad I/O, SIO18 to SPI Flash = Quad I/O

R1 = 4.7 ohm for CLK, CS#, MISO, MOSI; R1 = 0 ohm for IO2, IO3

R2 = 22 ohm for CLK, CS#, MISO, MOSI; R2 = 15 ohm for IO2, IO3

R3 = 22 ohm for CLK, CS#, MISO, MOSI; R3 = for IO2, IO3 22 ohm

R4 = 4.7 ohm for CLK, CS#, MISO, MOSI; R4 = for IO2, IO3 4.7 ohm

Use same values to start on IO2 and IO3

Note: The value of the resistors should be tuned according to the signal integrity simulations or actual PCB measurements.



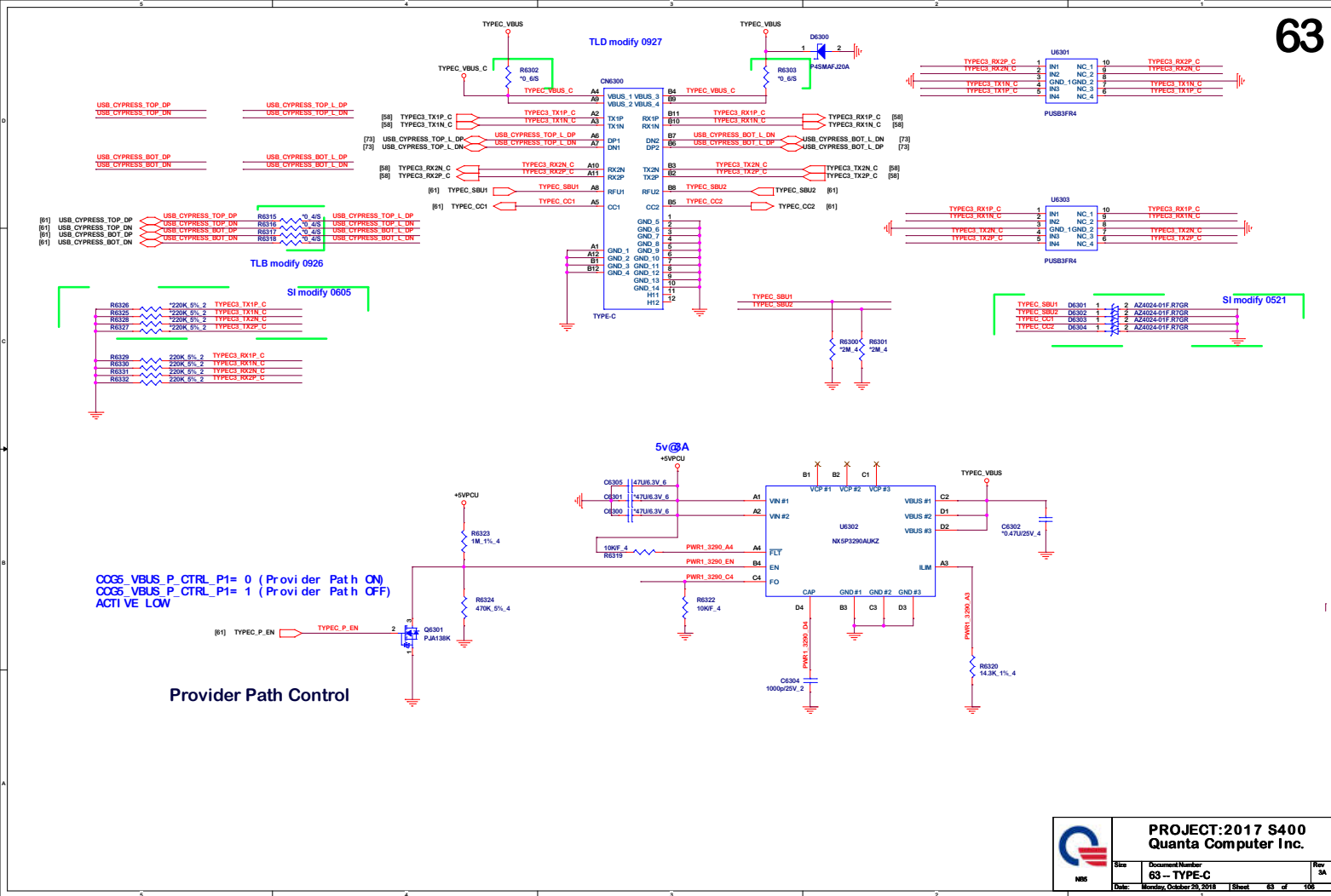
PROJECT :X8B
Quanta Computer Inc.

Doc Custom	Document Number	Rev 3A
	55 - Flash(PCH-PCH)	
Date: Monday, October 28, 2018	Sheet	66 of 106



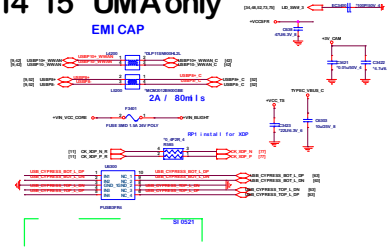
PROJECT :X8B
Quanta Computer Inc.

Size	Document Number	Rev
Custom	57 - US35.0 v2	24
Date	Monday, October 28, 2019	Sheet 57 of 104

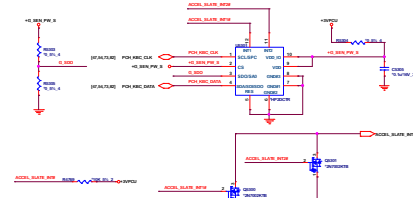


14"15" UMA only

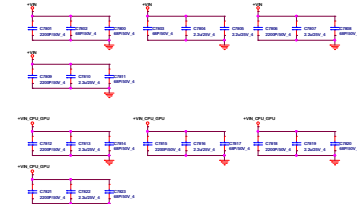
EMI CAP



G-Sensor (SLATE) 360 14"15" Used



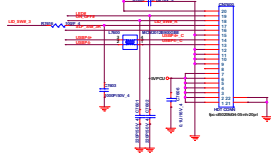
RF Cap



USB LID Daughter Board Connector

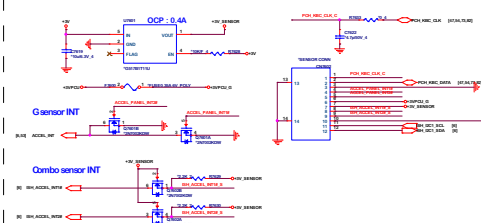


14"15" Used



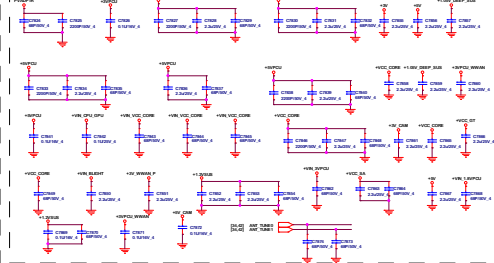
Sensor connector for 360

14"15" Used

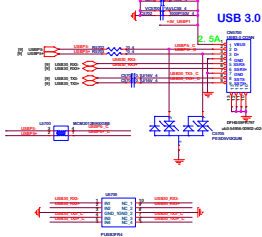


14 UMA RF Cap

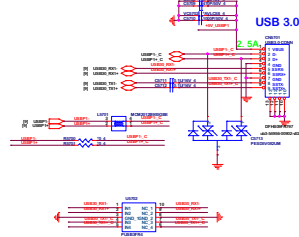
WWAN ONLY



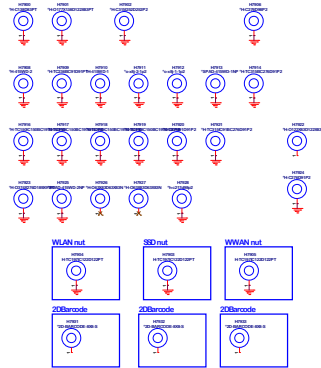
USB 2.0/3.0 Combo 14"15" Used



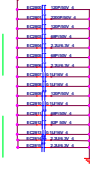
USB 2.0/3.0 Combo



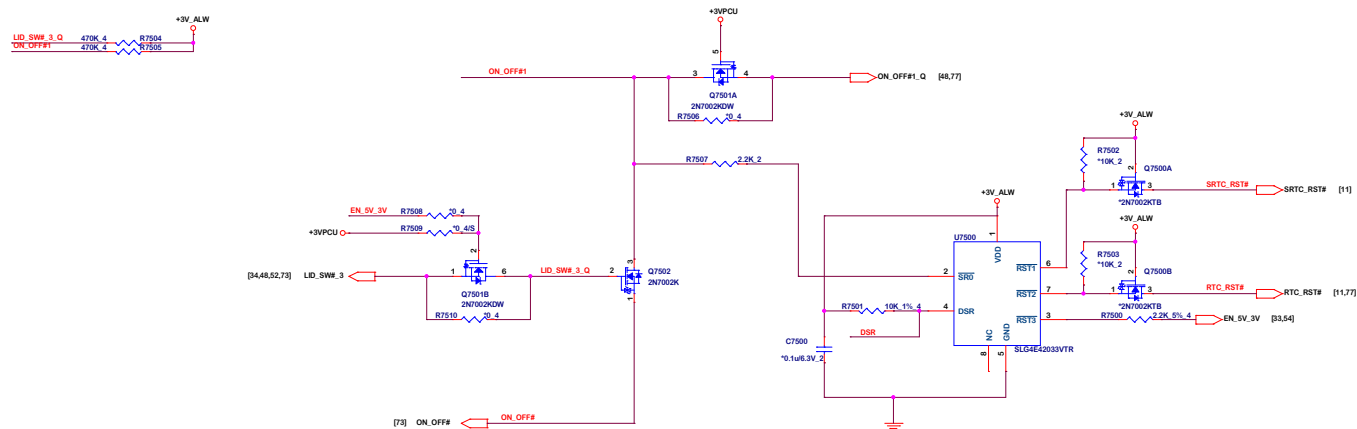
14"15 Hole



WWAN ONLY



Power Button Connector



[4,5,6,7,9,11,28,29,34,35,36,40,41,43,47,50,51,52,53,54,73,76,77,81,88,95] +3V
[10,34,35,36,44,50,54,61,73,81,95] +5V
[11,33,41,73,81,85,86,87,93] +3V_ALW

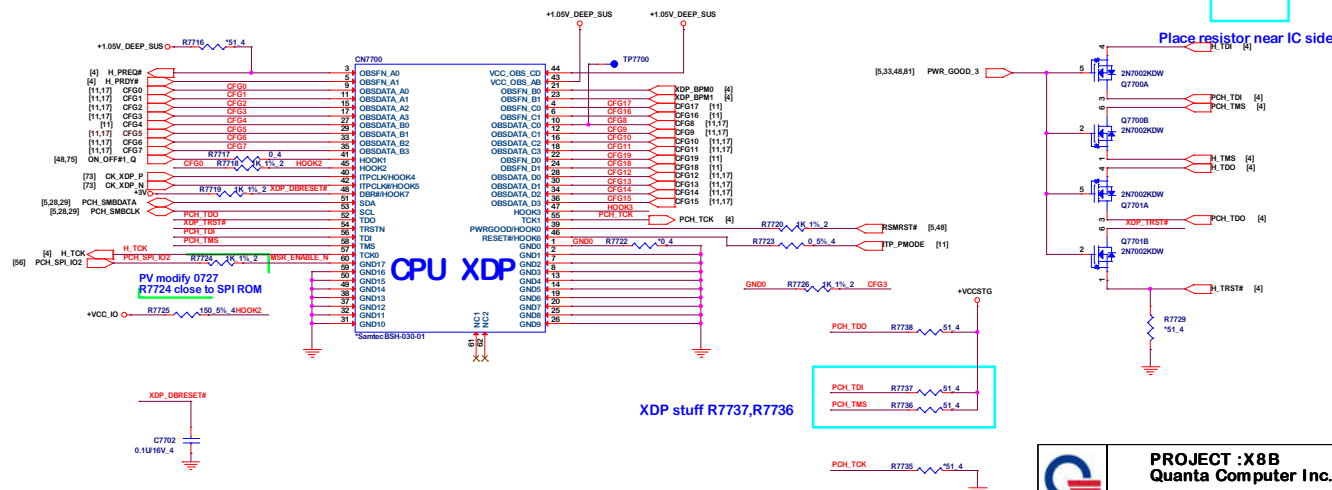
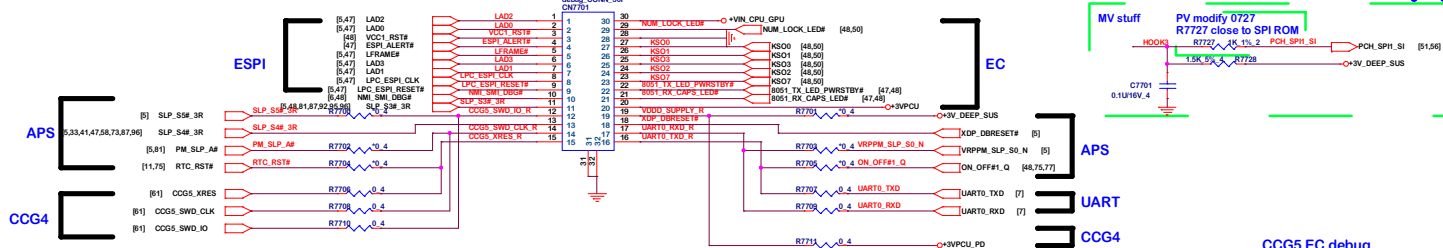


PROJECT :X8B
Quanta Computer Inc.

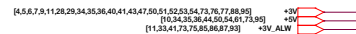
Size Custom	Document Number 75 – Power Button/ HW Reset	Rev 3A
Date: Monday, October 29, 2018		Sheet 75 of 106

[illegible]

ESPI+EC+APS debug conn on MB



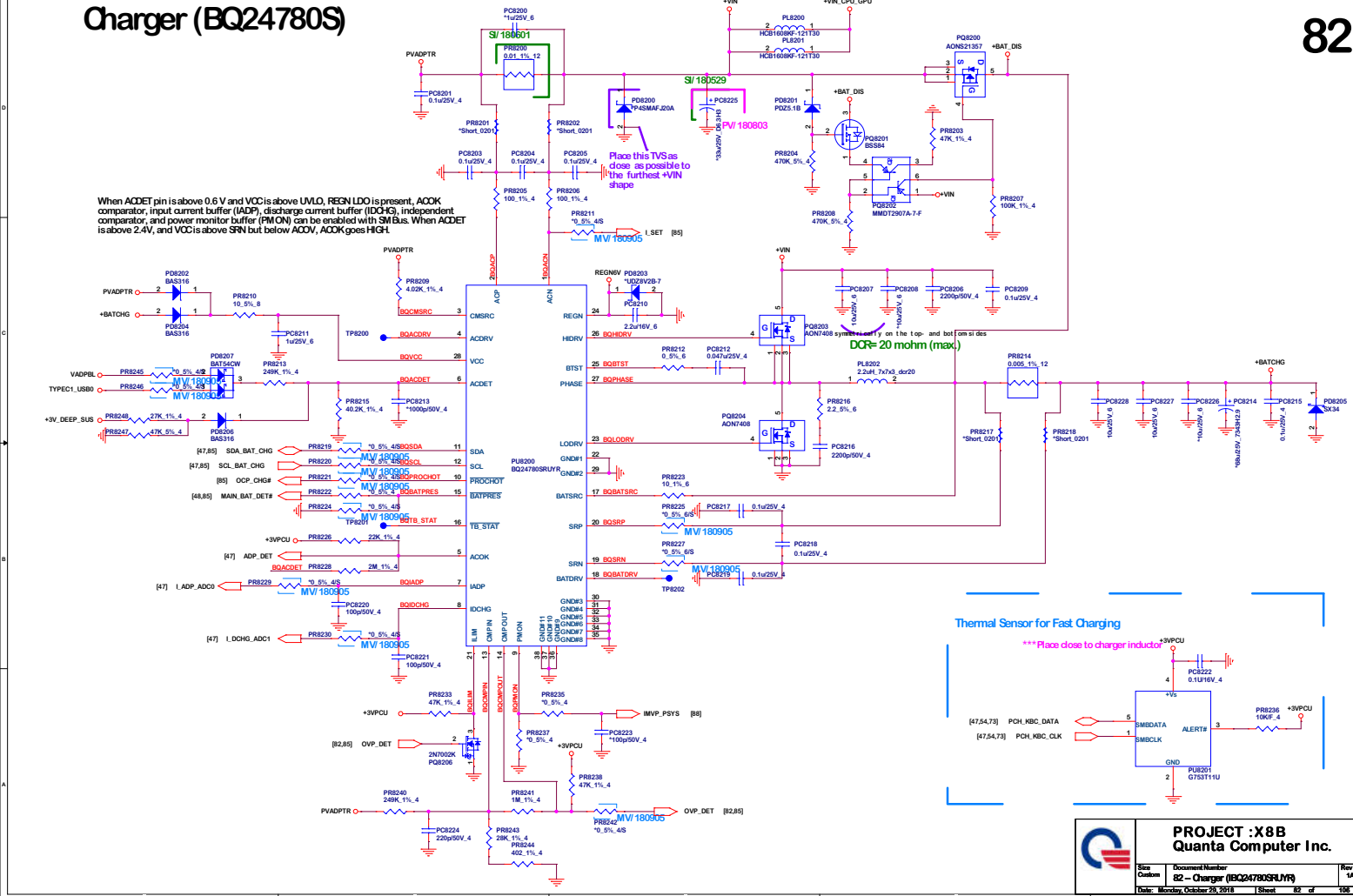
		PROJECT :X8B Quanta Computer Inc.	
Site Custom	Document Number 25 - LVDS converter R112136		Rev 3A
Date: Monday, October 28, 2018		1 Sheet	2nd 108



Charger (BQ24780S)

82

When ACDDET pin is above 0.6 V and VOC is above UVLO, REGN LDO is present, ACOCK comparator, input current buffer (IADP), discharge current buffer (IDCHG), independent comparator, and power monitor buffer (PMON) can be enabled with SMIUS. When ACDDET is above 2.4V, and VOC is above SRN but below ACOV, ACOCK goes HIGH.



PROJECT :X8B
Quanta Computer Inc.

Rev	Document Number	Rev
01	82 - Charger (BQ24780S)U/R	01
Date: Monday, October 28, 2019	Sheet	82 of 106



PROJECT :X8B
Quanta Computer Inc.

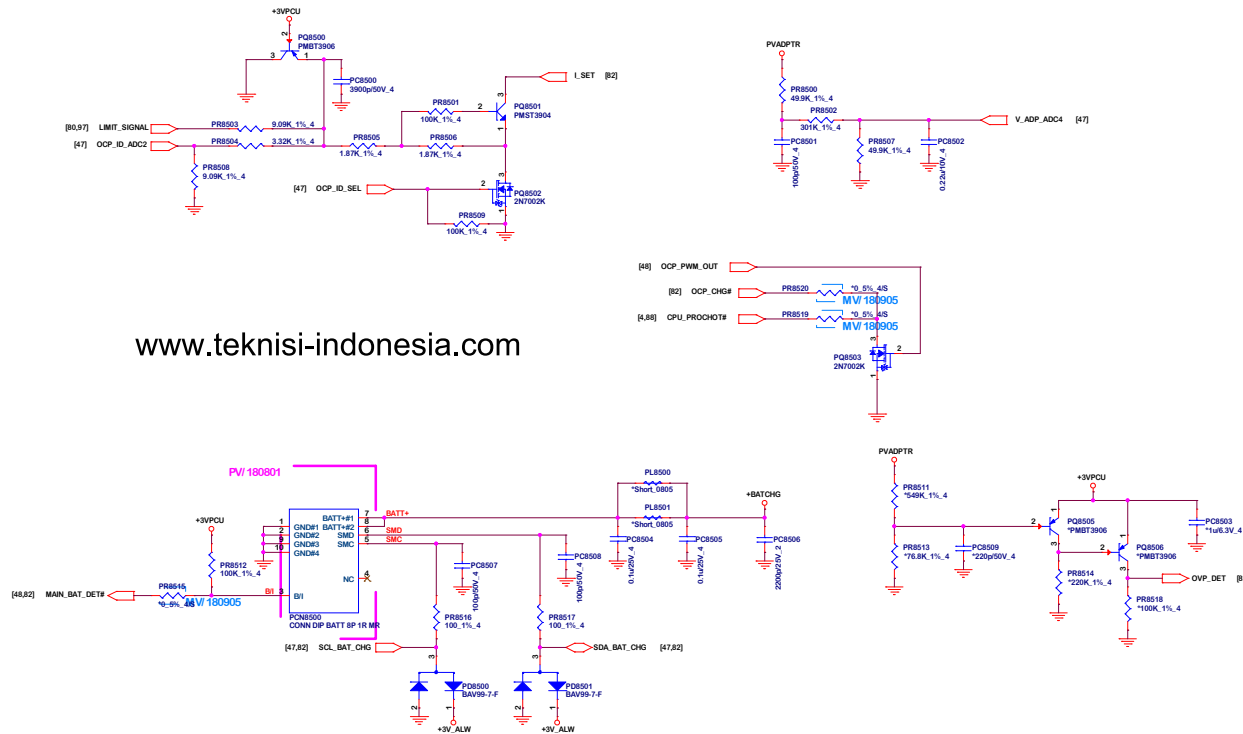
Size	Document Number	Rev
Custom	83 -- Charger (839636HRTZ-T)	1A
Date: Monday, October 26, 2015		1 Sheet 83 of 104



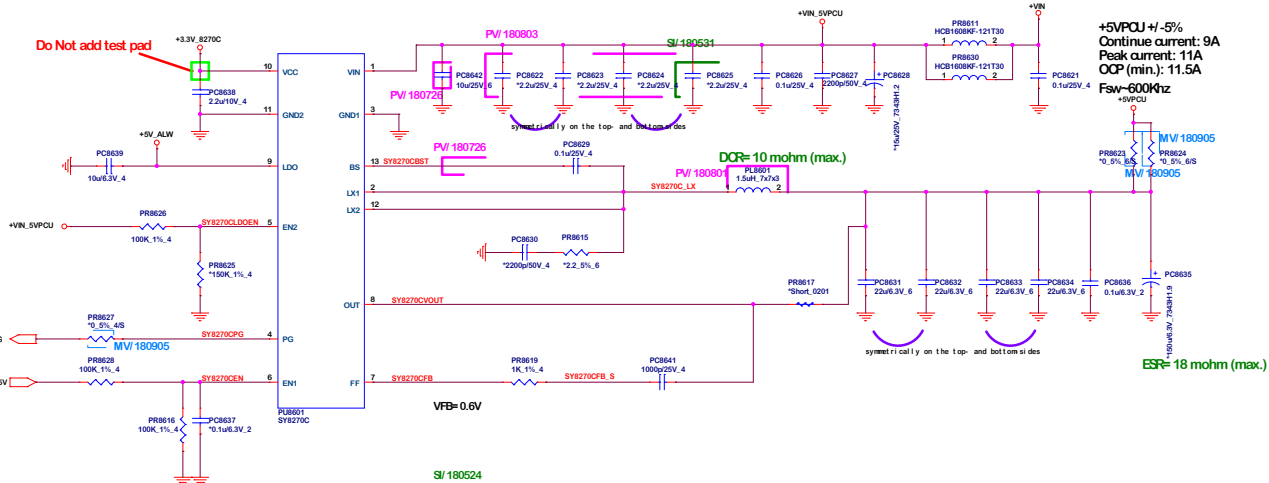
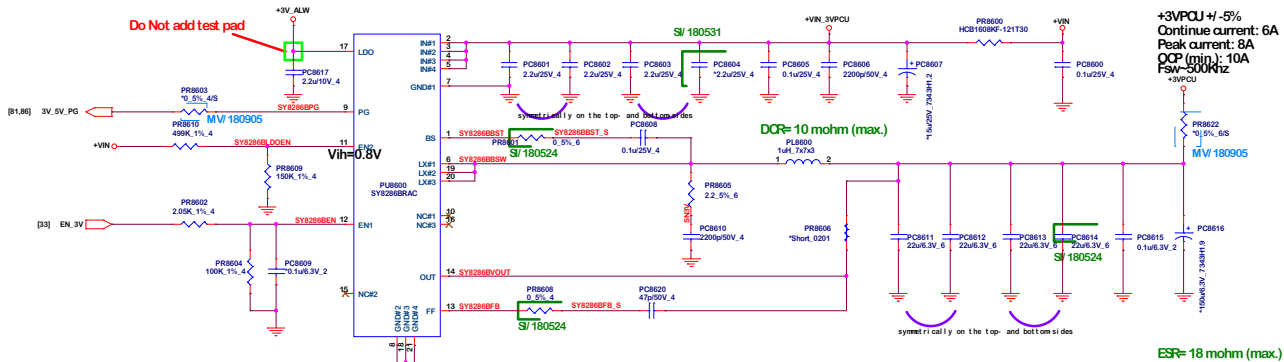
PROJECT :X8B
Quanta Computer Inc.

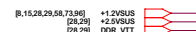
Size	Document Number	Rev
Custom	94 -- Charger (939636HRTZ-T)	1A
Date: Monday, October 26, 2015		1 Sheet 84 of 106

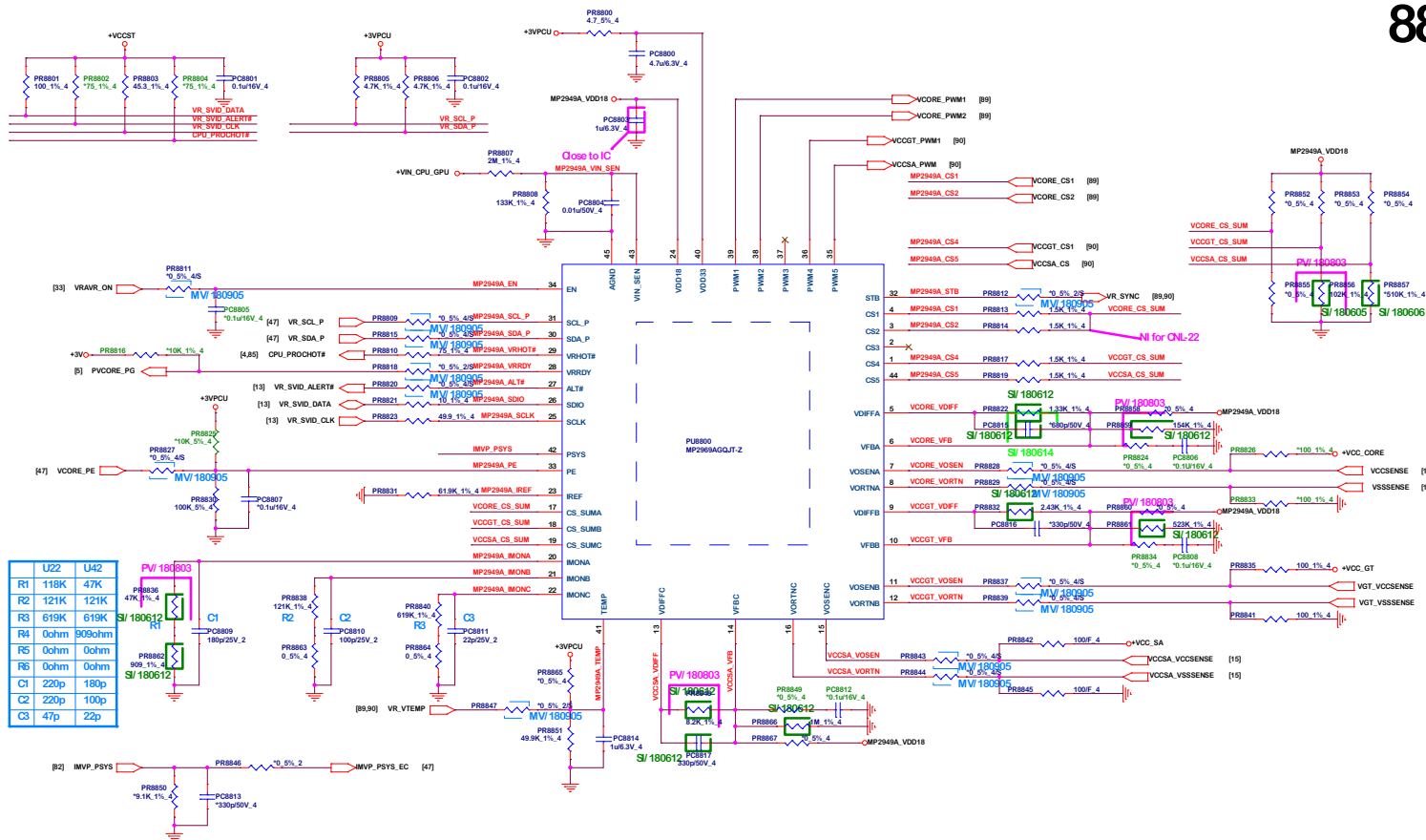
Barrel Adapter OCP



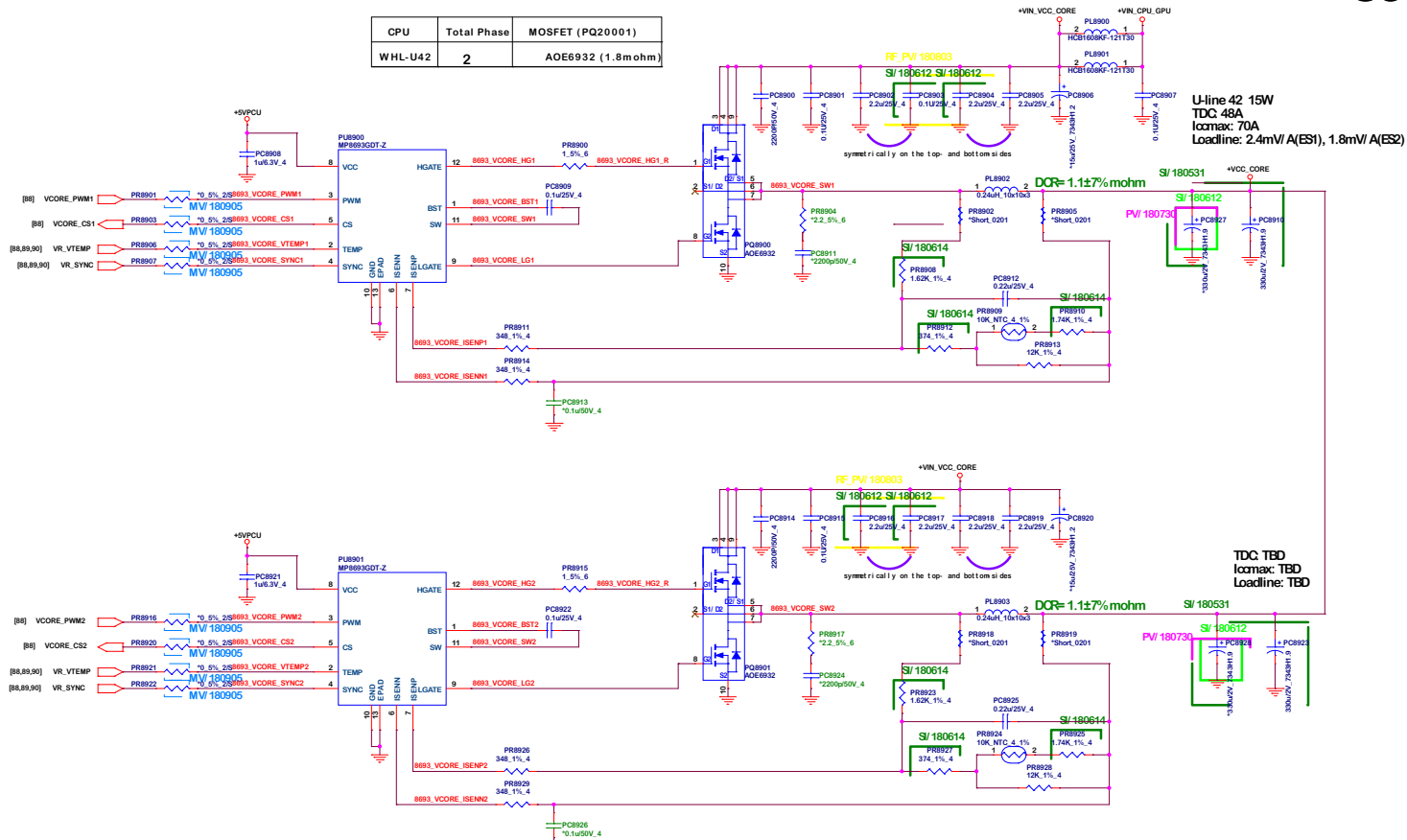
	PROJECT :X8B		
	Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	85 - Charger II	1A
Date: Monday, October 26, 2015		Sheet	85 of 105





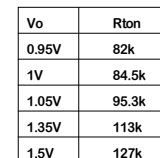


CPU	Total Phase	MOSFET (PQ20001)
WHL-U42	2	AOE6932 (1.8mohm)

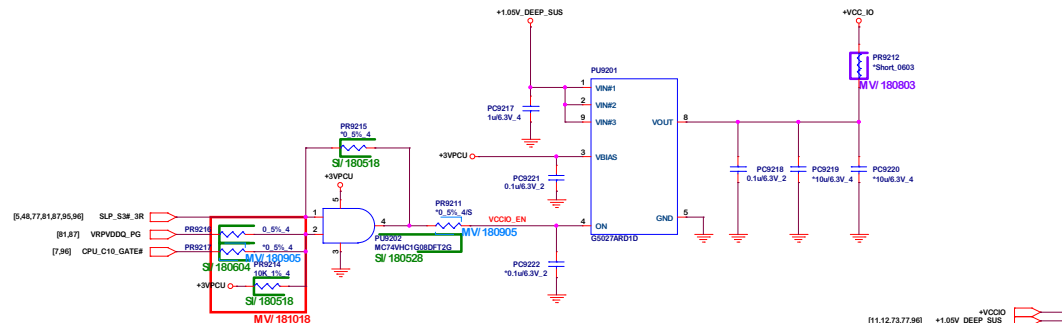




 NBS	PROJECT : 400 SERIES Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	91 -- reserve page(N/A)	1A
Date: Monday, October 26, 2015		Sheet	91 of 105

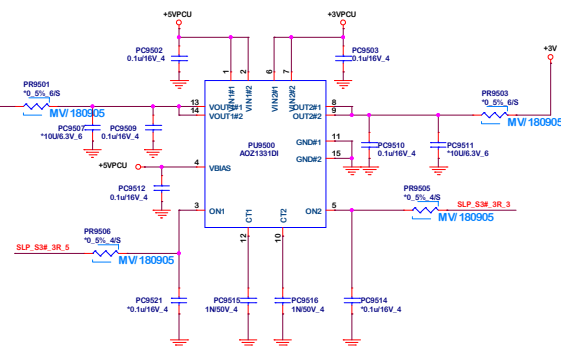


Size	Document Number	Rev
	92 -- +1.0V_DEEP_SUS(A0Z2260Q)	1A
Date	Monday, October 29, 2018 Sheet 92 of 106	

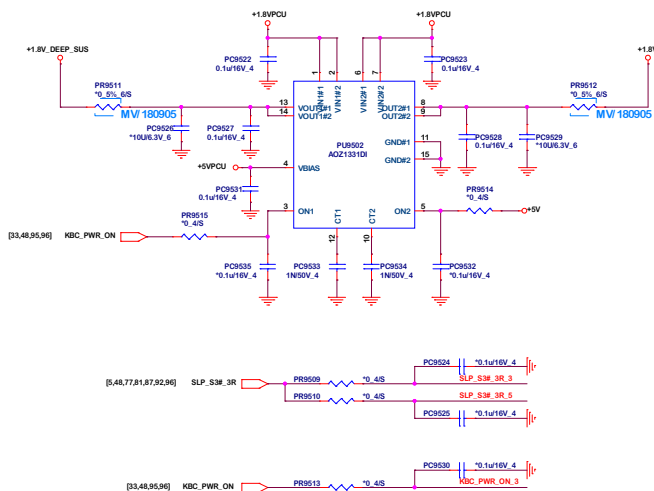


[11,12,73,77,96]	+VCCIO	
	+1.05V_DEEP_SUS	
	+1.05V	

	PROJECT : 400 SERIES	
	Quanta Computer Inc.	
	Doc Custom	Document Number 94 -- reserve page
Date: Monday, October 26, 2015		Sheet 94 of 105

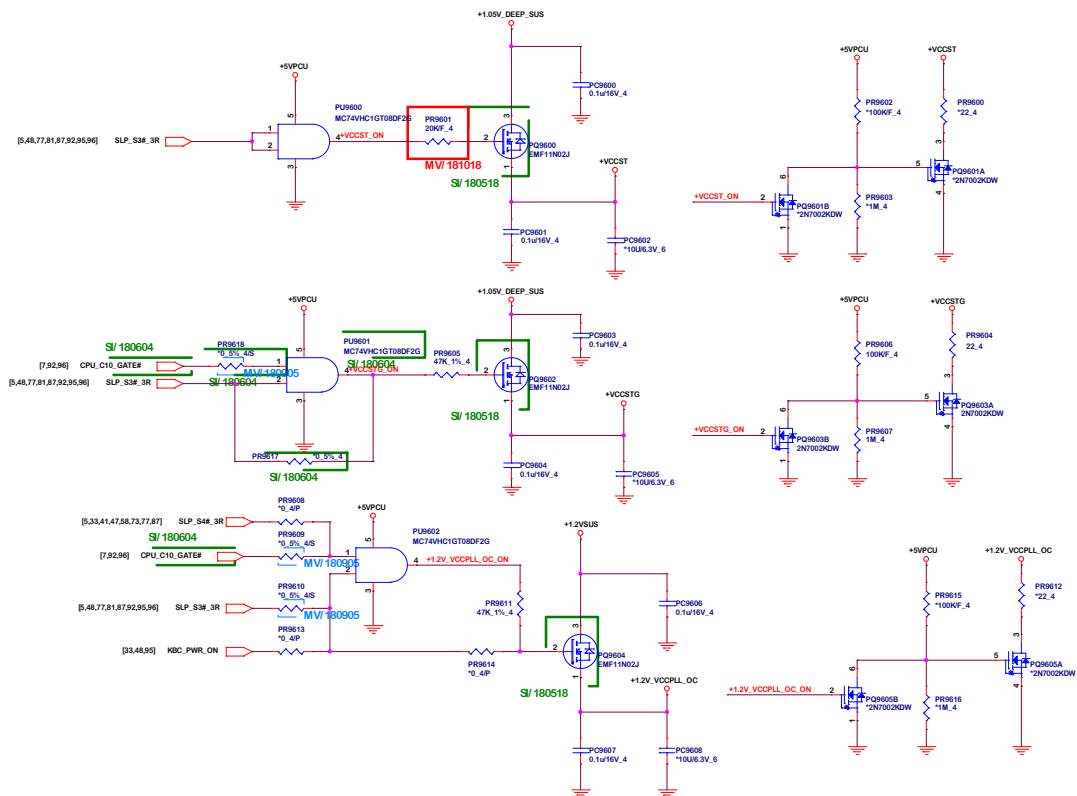


VPRO	PR9508
Non-Vpro	PR9507



[4,5,6,7,9,11,28,29,34,35,36,40,41,43,47,50,51,52,53,54,73,76,77,81,88] +3V
[10,34,35,36,44,50,54,61,73,81] +5V
[73,81,82,86,87,93,97] +VPU
[82,85,86,87,88,92,93] +3VPU
[61,63,73,76,80,81,86,87,89,90,92,96] +5VPU
[40] +3VLANVCC

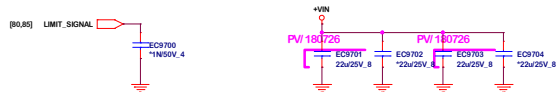




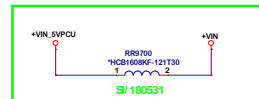
[4,5,6,7,8,11,28,29,34,35,36,40,41,43,47,50,51,52,53,54,73,76,77,81,88,95] +5V
 [10,34,35,36,44,50,54,81,73,81,95] +5V
 [73,81,82,86,87,93,97] +VBI
 [81,83,73,76,80,81,86,87,89,90,92,95] +5VPCU
 [40,95] +3V_LANVCC

	PROJECT :X8B		
	Quanta Computer Inc.		
	96 – Load Switch II		
Rev	Document Number	Rev	
Custom	96 – Load Switch II	1A	
Date: Monday, October 28, 2013	Sheet	96 of	106

Reserve for EMI & ISEN test



RF Cap



	PROJECT : 400 SERIES	
	Quanta Computer Inc.	
	Doc Custom	Document Number 98 -- reserve page
Date: Monday, October 26, 2015		Sheet 98 of 105

 NBS	PROJECT : 400 SERIES Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	99 -- reserve page	1A
Date: Monday, October 26, 2015		Sheet	99 of 105

A0

 NB5	PROJECT : 400 SERIES Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	A0 - reserve page	1A
Date: Monday, October 26, 2015		Sheet	100 of 100



PROJECT : 400 SERIES
Quanta Computer Inc.

Size	Document Number		Rev
Custom	A1 - reserve page		1A
Date: Monday, October 26, 2015		Sheet	101 of 105

 NB5	PROJECT : 400 SERIES Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	A2 - reserve page	1A
Date: Monday, October 26, 2015		Sheet	192 of 195

 NB5	PROJECT : 400 SERIES Quanta Computer Inc.		
	Doc Custom	Document Number A3 - reserve page	Rev 1A
	Date: Monday, October 26, 2015		Sheet 188 of 188

	PROJECT : 400 SERIES		
	Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	A4 - reserve page(N/A)	1A
	Date: Monday, October 29, 2018	Sheet	104 of 105

A5

	PROJECT : 400 SERIES		
	Quanta Computer Inc.		
	Doc	Document Number	Rev
	Custom	A5 - reserve page(N/A)	1A
	Date	Monday, October 29, 2018	Sheet 106 of 106

 NBS	PROJECT : 400 SERIES Quanta Computer Inc.		
	Size	Document Number	Rev
	Custom	A6 - reserve page	1A
Date: Monday, October 26, 2015		Sheet	106 of 106