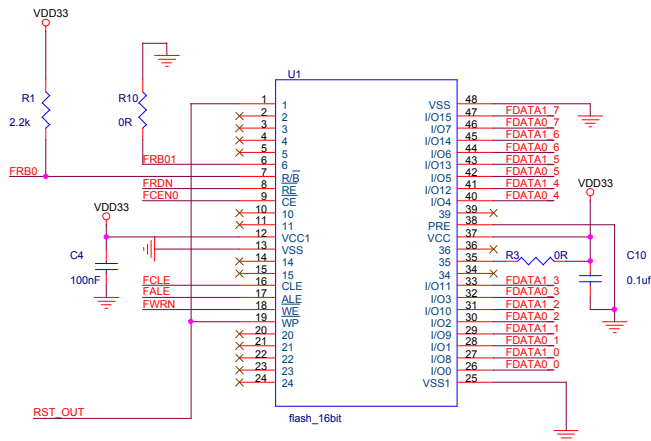
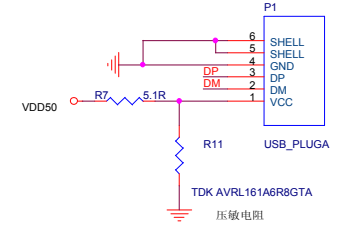
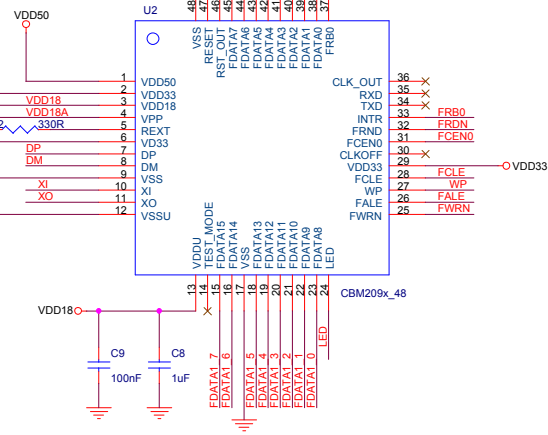
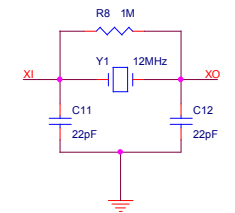


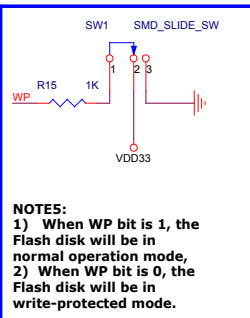
C8,C9 need approaches the 13 pin



NOTE4:  
if don't consider ESD, The R11 should removed .otherwise all of them should be added .



**NOTE1**  
1)When the Flash is 16bit sandisk flash,R3,R10,should be added  
2)otherwise R3,R10, should be removed



**NOTE5:**  
1) When WP bit is 1, the Flash disk will be in normal operation mode,  
2) When WP bit is 0, the Flash disk will be in write-protected mode.



|                                     |                                |                                                                                       |
|-------------------------------------|--------------------------------|---------------------------------------------------------------------------------------|
| 设计:                                 | steven.luo                     |  |
| 审核:                                 |                                |                                                                                       |
| 版本: V1.05                           | 图纸尺寸: Custom                   | 工程名: 209x-1pcs-16bit-flash application                                                |
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