



Shenzhen Tianxinyuan Electronics Co., Ltd.

ATC9501(Document Number:11025)

summary:

This application note describes ATC9501 The built-in triode primary feedback control chip and its application circuit are described in detail. Its characteristics and working principle are described.

Features:

- ◆ Primary side feedback constant voltage (CV) and constant current (CC) control 3%
- ◆ Constant pressure accuracy, 2% Constant current precision energy saving mode:
- ◆ pulse frequency modulation control mode (PFM)
- ◆ built-in 900V Power switch tube
- ◆ Built-in frequency jitter to reduce EMI
- ◆ Cycle-by-cycle current limiting
- ◆ Built-in soft start
- ◆ Low starting current (5uA)
- ◆ Feedback overvoltage protection (OVP)
- ◆ Undervoltage Lockout (UVLO)
- ◆ Over temperature protection (OTP)
- ◆ Output short circuit protection
- ◆ Feedback loop open circuit protection
- ◆ Adjustable output line compensation voltage

Main description:

ATC9501 Suitable for constant current and constant voltage with low cost and high precision (CC/CV) Application. Constant current and constant voltage loop does not require optocoupler, TL431 and compensation circuits, internally integrated 900V power tube, reducing system cost. ATC9501 Can meet the output voltage $\pm 5\%$ The mass production accuracy is very low. The quasi-valley detection can easily meet EPA2.0 The voltage compensation of the output cable can be adjusted according to the customer's requirements, which greatly facilitates the customer's system design. ATC9501 It also integrates a variety of protection functions: undervoltage lockout, leading edge blanking, overvoltage protection, overcurrent protection, loop open circuit protection, and output short circuit protection, which greatly increases the stability of the system. ATC9501 supply SOP-8 The package, ATC9501 S for SOP-7 Encapsulation.

Applications:

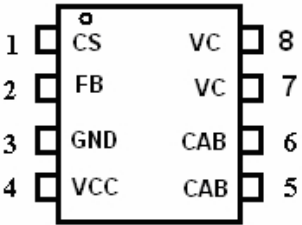
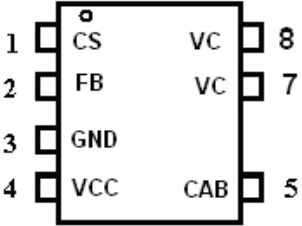
- ◆ Cell phones, cordless phones, PDA, MP3/MP4, battery chargers for digital cameras and power tools led
- ◆ Spotlights, lamp cups and bulbs
- ◆ Linear Power Supplies and RC The best choice for upgrading switching power supply

◆ Limit parameters

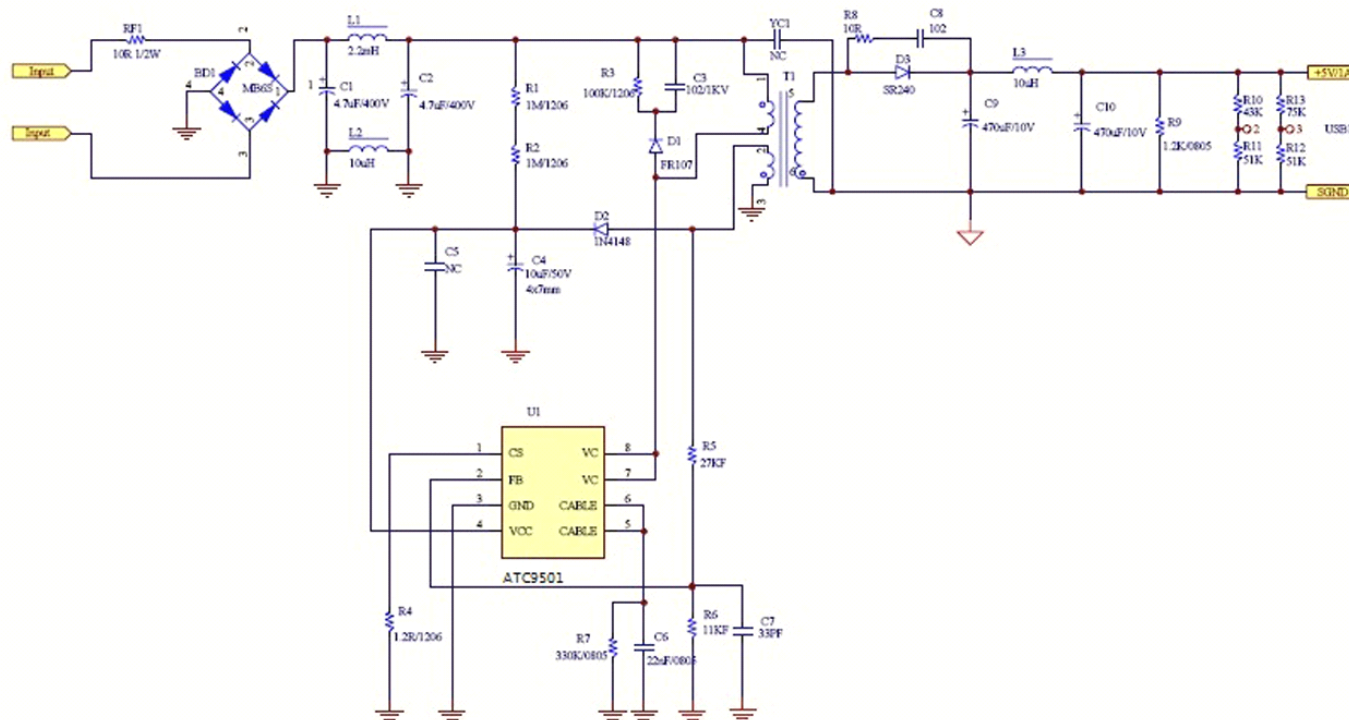
VCC to GND.....	-0.3V to + 18V FB to
GND.....	-0.3V to +8V
CABLE to GND.....	-0.3V to +8V VC to GND
.....	-0.3V to +900V CS to GND.....
.....	-0.3V to +8V Operating Temperature
Range.....	-40°C to +125°C Junction
Temperature.....	-40°C to +150°C Storage
Temperature Range.....	-60°C to +150°C ESD Protection
HBM.....	2000V
MM.....	500V

ATC9501(Document Number:11025)

Pin definition:

ATC9501		Pin No.	Pin Name	Description
		ATC9501/ATC9501S		
1	CS	1	CS	Primary current sensing pin
2	FB	2	FB	Output voltage feedback pin
3	GND	3	GND	Chip Ground
4	VCC	4	VCC	Chip operating voltage
		5,6	CABLE	External capacitor and resistor, output line compensation adjustment
		7,8	VC	Connected to the primary high-voltage output terminal of the transformer, can withstand voltage900VVoltage

Typical application circuit diagram:



picture1

ATC9501(Document Number:11025)

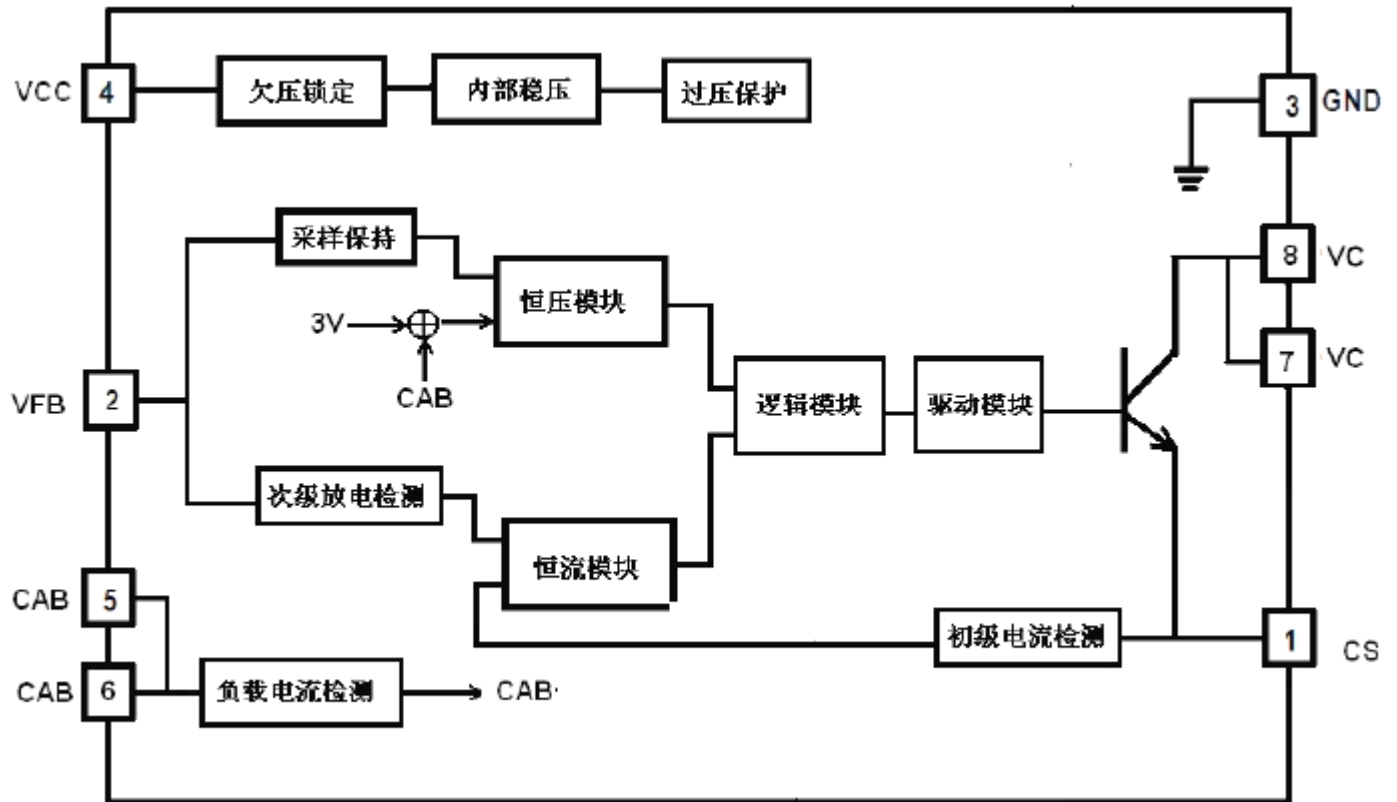
Electrical Characteristics

(TA=25°C,Vcc=10V,unless otherwise noted)

Characteristic	Symbol	Min	Type	Max	Unit
FBPin part					
FBFeedback voltage	VFB	2.98	3.01	3.04	V
FBOvervoltage protection voltage	FB ovp	4.4	4.5	4.6	V
FBMaximum output current	IFB max		5		mA
FBMinimum output current	IFB min		200		uA
CSPin part					
Overcurrent limit voltage	Vcs	0.49	0.50	0.51	V
Leading edge blanking time	LEB		400		ns
VCCPin part					
Starting current	Istart		5	10	uA
VCCStarting voltage	Vcc(on)	13.5	15.2	17	V
VCCShutoff voltage	Vcc(off)	5.5	6.6	7.5	V
VCCStatic operating current	Iq		0.7		mA
VCCOvervoltage protection voltage	Vcc(ovp)	17.5	18.5	19.5	V
CABLEPin part					
The ratio of cable compensation voltage to output voltage (Rbis connected toCABandGNDResistance, unitKohm)	Kcab		0.012*Rb		%
Built-in switch tubeNPNpart					
Maximum continuous conduction current	Icc		1.5		A
Collector-base breakdown voltage (Ic=1mA)	BnB	900			V
Collector-emitter breakdown voltage (Ic=10mA)	BV	450			V
Emitter-base breakdown voltage (Ic=1mA)	BV	9			V
Saturation voltage (Ic=200mA,Ib=40mA)	Vcesat1			0.3	V
Saturation voltage (Ic=750mA,Ib=250mA)	Vcesat2			0.5	V

ATC9501(Document Number:11025)

Internal Block Diagram



picture2

introduce

This highly integrated primary feedback control chip includes many functions that effectively improve the performance of low-power flyback converters. The primary feedback topology simplifies the circuit design and can achieve accurate constant voltage and constant current without secondary feedback. Through the frequency jitter technology inside the chip, EMI can be solved with the smallest filter possible, so the charger is smaller and lighter than traditional designs and linear transformers.

This primary feedback control chip operates in pulse frequency modulation (PFM) mode, the lighter the load, the smaller the frequency, so the system power consumption is very small when the load is light. The maximum starting current is 10uA, so a large starting resistor can be used to improve energy saving.

This primary side control chip also provides a lot of protection functions. The pins are configured with overvoltage protection and input undervoltage lockout. The current limit and constant current control of each cycle ensure overcurrent protection under heavy load. In addition, the overtemperature protection can shut down the control chip and restart when the overtemperature state is released.

By using this primary side feedback chip, the charger can achieve constant voltage and constant current functions with very few peripheral devices and the lowest cost. The pin is connected to an external resistor-capacitor network to adjust the output line compensation voltage.

ATC9501(Document Number:11025)

Internal working diagram

Constant pressure control

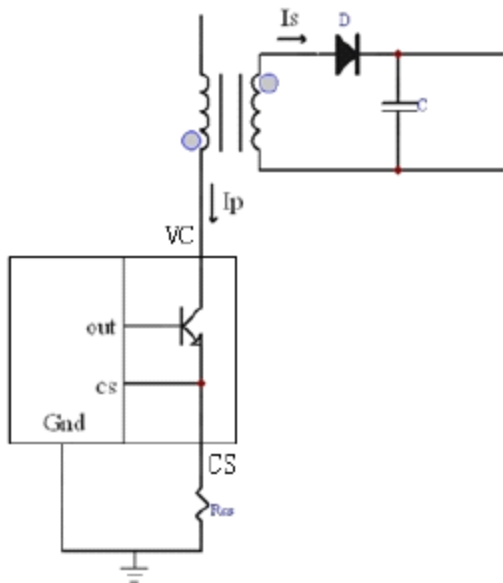
The primary side feedback control method can achieve accurate constant voltage/constant current control without the need for secondary side voltage and current detection.1and3This is a typical application circuit.2This is the internal block diagram of the chip.4These are some of the main waveforms. The secondary output state is obtained from the primary auxiliary winding when the power transistor is turned off. Some unique sampling methods are used to replicate the output winding voltage (V_s)and the discharge time of the secondary diode (T_{dis})The sampled voltage is compared with the internal precise reference voltage (V_{ref}) and then modulate the output of the error amplifier to determine the switch off time. This simple method achieves accurate output voltage regulation.

Constant current control

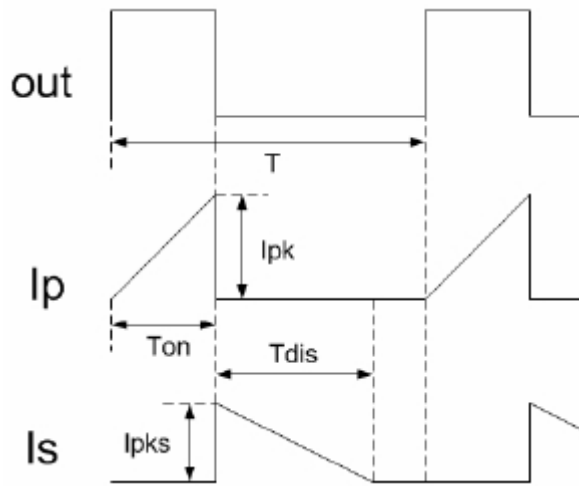
picture4As shown, the output current I_o in intermittent mode (DCM) in the flyback topology, the equation (1) To express.

$$I_o = \frac{2 \times N_{ps} \times V_{csth}}{7 \times R_{cs}} \quad (1)$$

in: N_{ps} is the ratio of the number of turns of the primary to the secondary coil. R_{cs} is the resistance of the current sensing resistor. V_{xys} R_{cs} The voltage limit value on the chip is set $V_{csth}=0.5V$ Therefore, the output current I_o can be N_{ps} and R_{cs} Let's calculate and make sure N_{ps} and R_{cs} After that, the primary feedback control chip can determine the turn-off time of the power transistor, thereby providing a constant output current.



picture3



picture4

Frequency reduction working mode

When the primary feedback control chip is in constant voltage working mode, the operating frequency decreases as the load current decreases.0With this control mode, the power control chip can easily meet the most stringent power conversion efficiency requirements.

ATC9501 (Document Number:11025)

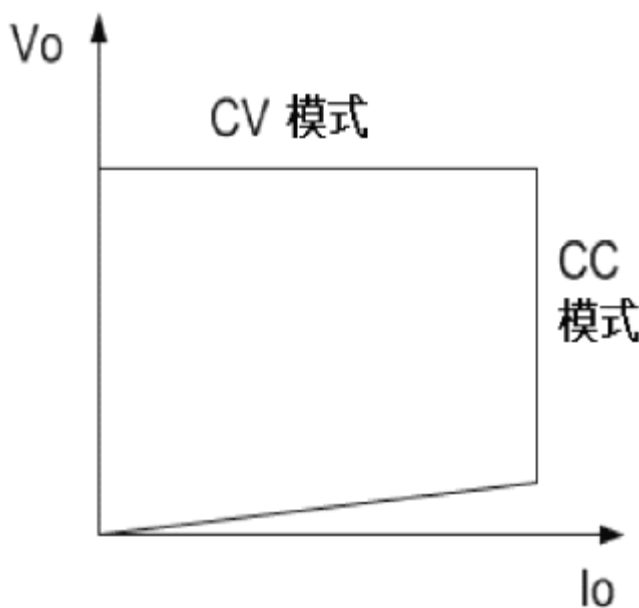
Frequency Jitter

This primary side feedback control chip integrates an internal frequency jitter function to improve EMI performance.

Output voltage and current characteristics

Battery chargers are generally designed with two operating modes: constant voltage charging and constant current charging. The following figure shows the basic charging characteristics. When the battery voltage is very low, the charger works in a constant current charging state. This is the main mode of current charging. When the battery voltage reaches its final voltage, the current gradually stops.

The device enters constant voltage charging mode. Eventually, the charging current gradually decreases to zero.



picture5

Startup Circuit

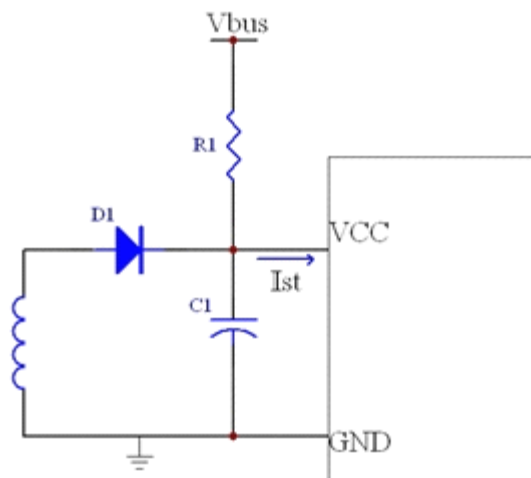
When the power is turned on, as shown in the figure 6, the input voltage V_{bus} is applied through the start resistor $R1$. When the capacitor voltage (V_{CC}) reaches the chip startup voltage (V_{CC-ON}), the primary feedback control chip starts to start.

Power supply start-up delay T_{st} can be calculated by the following formula:

$$T_{st} = -R1 \times C1 \times \ln\left(1 - \frac{V_{CC-ON}}{\sqrt{2} \times V_{ac} - I_{st} \times R1}\right) \quad (2)$$

in: V_{CC-ON} is the chip startup voltage; I_{st} is the chip startup current.

Because of the small starting current, $R1$ can be very large, such as 1.2MΩ. If V_{CC} is 4.7μF, the startup delay is 90μs. The input can be done within seconds.

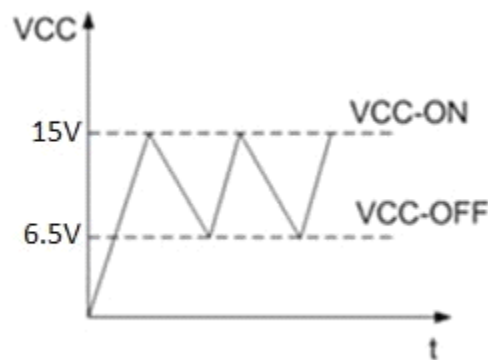


picture6

ATC9501 (Document Number: 11025)

VCC Undervoltage Lockout

As shown in Figure 7, the start and shut-down thresholds of this primary feedback control chip are fixed at 15V and 6.5V. At startup, the capacitor must be connected across the start-up resistor R1 to charge to 15V, thereby starting the control chip. When energy cannot be obtained from the auxiliary winding, the capacitor will continue to supply power to the control chip until the auxiliary winding starts to supply greater than 6.5V. If VCC voltage is below 6.5V, the chip will enter VCC undervoltage lockout state, shutting down some circuits inside the chip. When the bus passes R1 to capacitor C1, the capacitor charges until VCC voltage reaches 15V, the chip starts up again and turns on all internal circuits. This undervoltage lockout hysteresis loop will ensure that the capacitor is sufficient to power the control chip.

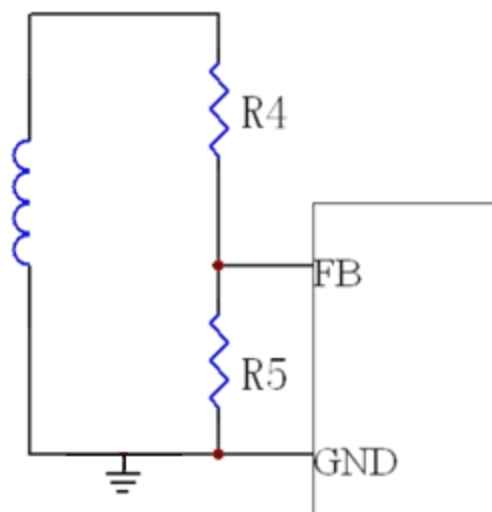


picture7

Output overvoltage and open loop protection

As shown in Figure 8, in normal operation, the output voltage feedback is the voltage of the auxiliary winding, and the auxiliary winding is R4 and R5. The voltage divider is fed back to the chip FB. The chip adjusts the off time to make the FB pin voltage remain stable at 3V. When the output voltage suddenly increases, the voltage of the auxiliary winding also increases, resulting in the FB pin voltage rising. When the FB pin voltage reaches 4.5V, the chip maintains 6μs. When the chip turns off, the output of the pin remains on until the chip is started next time.

If the resistor R4 is short-circuited or R5 is open-circuited, the FB pin voltage will quickly rise to 4.5V, the chip will restart continuously and work in hiccup mode all the time; if the resistor R4 is open-circuited or R5 is short-circuited or open-circuited of auxiliary winding, no signal is input to the chip FB. The chip will start working for one cycle and then shut down. The output of the pin remains on until the chip is started next time.



picture8

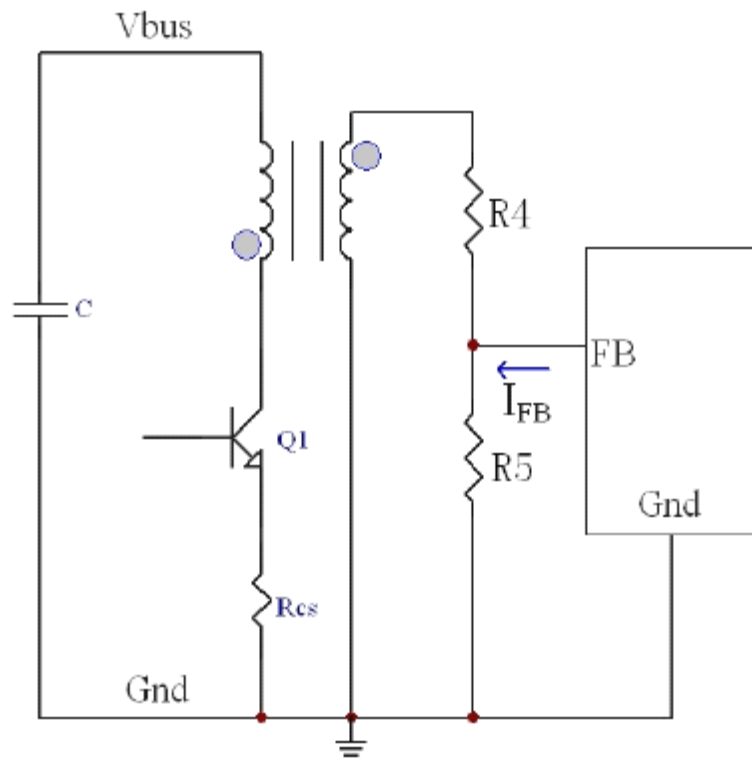
ATC9501(Document Number:11025)

Input undervoltage protection

This primary feedback control chip has a built-in input undervoltage protection function. As shown, when the power transistor Q1 is conducting,

$$I_{FB} = \frac{V_{bus} \times N_{aux}}{N_p \times R_4} \quad (3)$$

When the input voltage V_{bus} is low, the chip feedback current flowing out of the feedback pin I_{FB} will also decrease. When V_{bus} drops to a certain extent, I_{FB} becomes lower than the chip setting value. When the chip output is turned off immediately, the voltage gradually decreases to the undervoltage lockout voltage and then enters the VCC restart process.



picture9

LEBtime

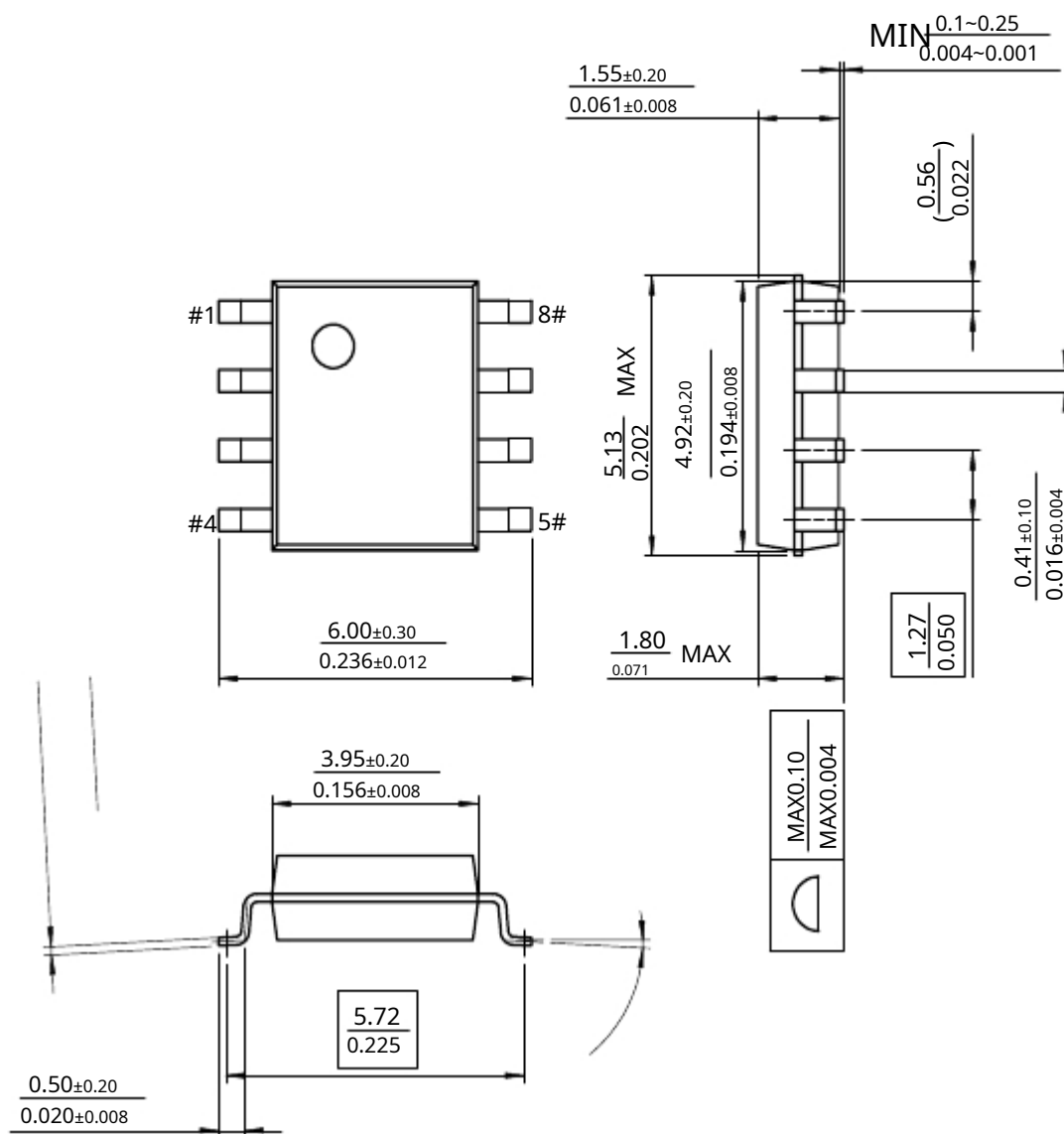
Each power transistor Q1, when turned on, the absorption diode D5 reverse recovery and power transistors Q1 generate spikes. The spikes generated by the parasitic capacitance will reflect back to the input. In order to prevent the power transistor Q1 from malfunctioning, the detection of this primary feedback control chip will be blank for a period of time on the rising edge. During this blank time, the current limiting comparator is not enabled and the base drive current cannot be driven.

Over temperature protection

When the temperature exceeds 145°C, this primary feedback control chip has a built-in over-temperature detection circuit to shut down the chip output. The output of the pin is turned off. The voltage will gradually decrease to the undervoltage lockout voltage (6.5V). Some internal circuits will shut down, causing the VCC voltage to rise again. When VCC reaches 15V, all internal circuits, including the over-temperature detection circuit, will start up normally again. At 145°C, the chip output will be turned off immediately. This state will continue until the over temperature state is released.

SOP8 Packaging information

Dimensions in millimeters



ATC9501(Document Number:11025)

SOP7Packaging information

Dimensions in millimeters

