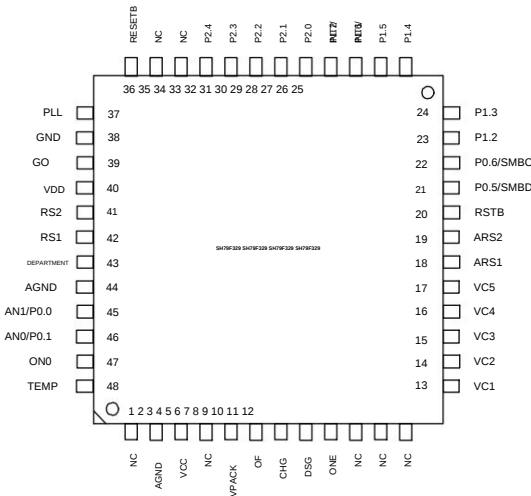




SH79F329

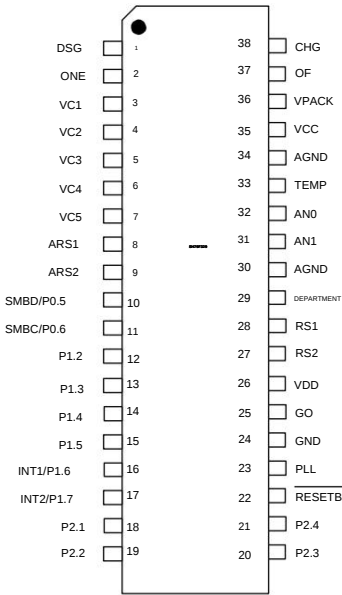
4. Pin configuration

TQFP48 (Total: 48 pins)



TQFP48 pin configuration diagram

TSSOP38 TSSOP38 TSSOP38 TSSOP38 (Total: 38 pins)



TSSOP38 TSSOP38 TSSOP38 TSSOP38 Pin Configuration Diagram

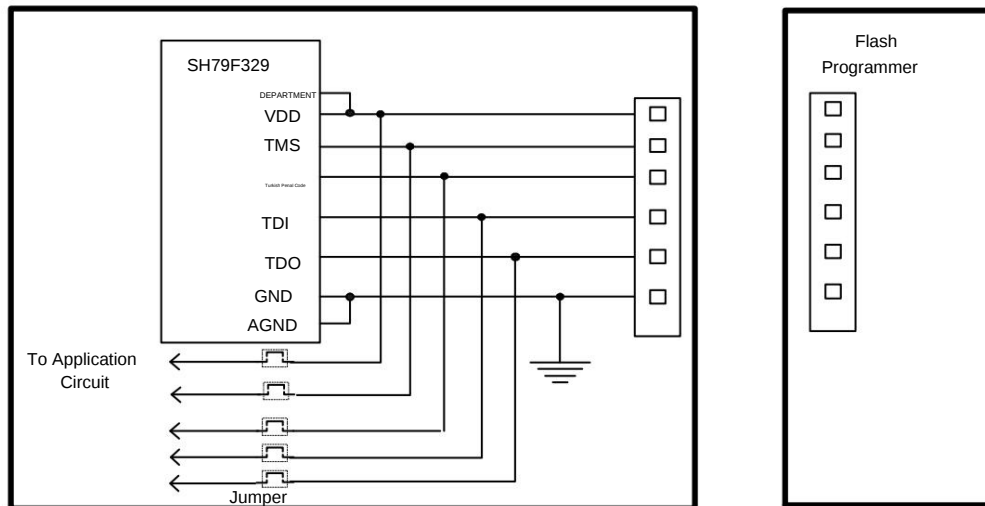
Notice:

In pin naming, the function written on the outermost side has the highest priority, and the function written on the innermost side has the lowest priority. When a pin is occupied by a high-priority function, it cannot be used as a pin for a low-priority function even if a low-priority function is allowed. Only when the software prohibits the high-priority function of the pin can the corresponding pin be released for use as a low-priority port.



## SH79F329

In ICP mode, all Flash operations can be completed through the 6-wire interface programmer. Because the programming signal is very sensitive, the user needs to use 5 jumpers to connect the programming pins (VDD, TDO, TDI, TCK, TMS) are separated from the application circuit. As shown in the figure below.



It is recommended to follow the following steps: (1) Before

connecting the programming interface, you must disconnect the jumper and separate the programming pins from the application circuit before programming can begin. (2)

After the programmer is connected to the programming interface, start

programming. (3) After programming is completed, disconnect the programmer and connect the jumper.



## SH79F329

### 12. Application Circuit

- 4 LED displays - 2

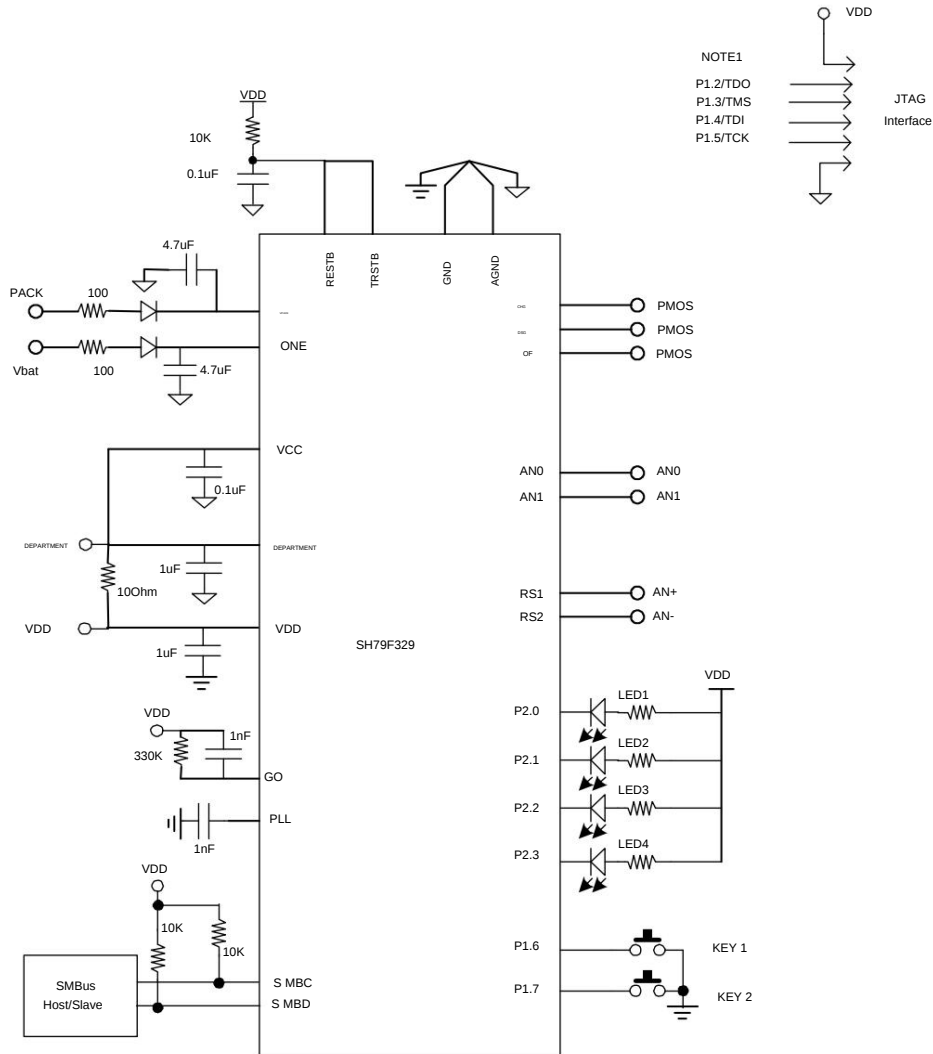
buttons

- SMBus communication

- 2 single-ended voltage

measurements - 1 differential

voltage measurement - 3 PMOS controls



Note: Refer to the JTAG pin connection diagram